

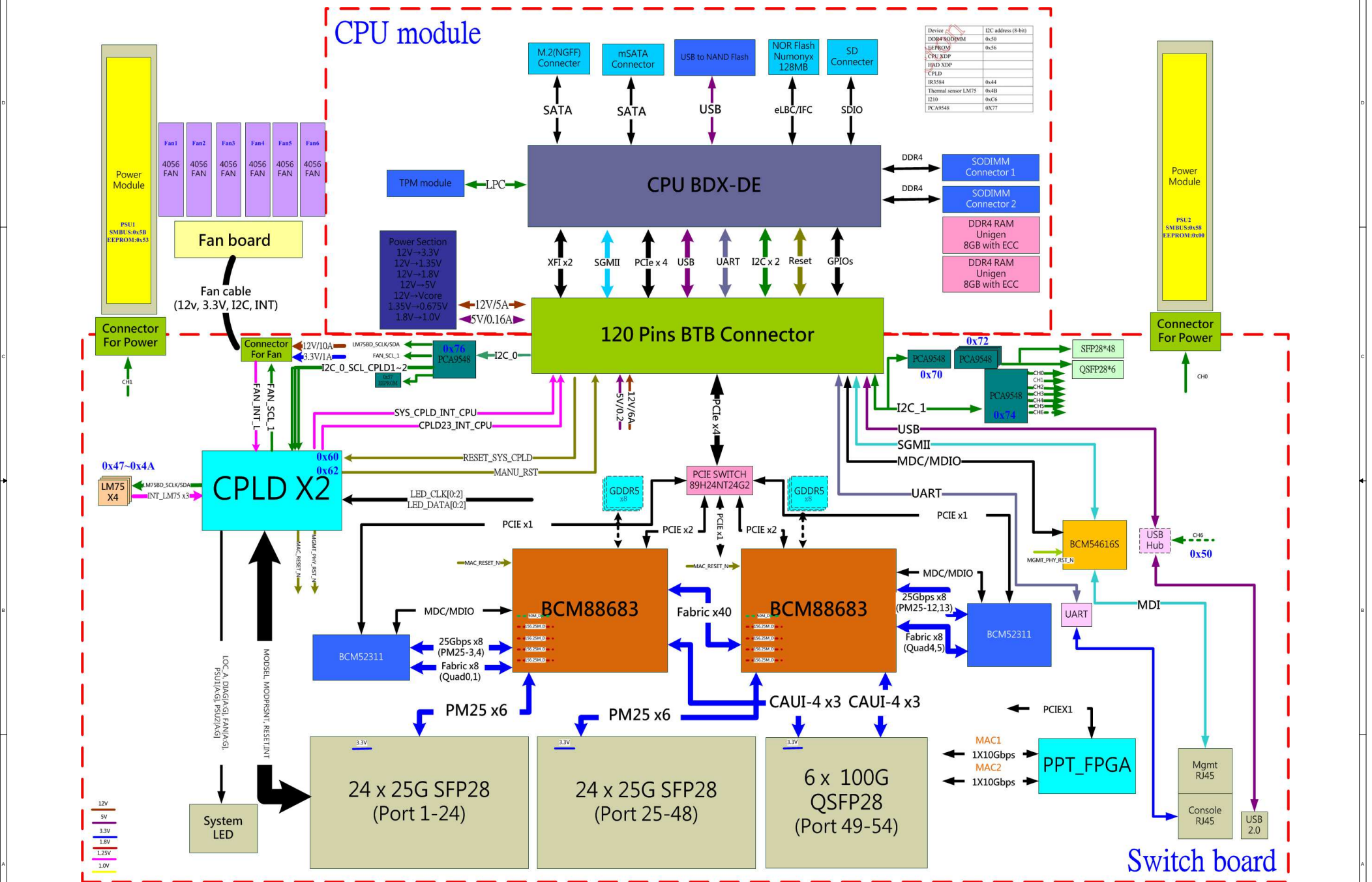
ASF7680BBX-0418-EC Cover Page

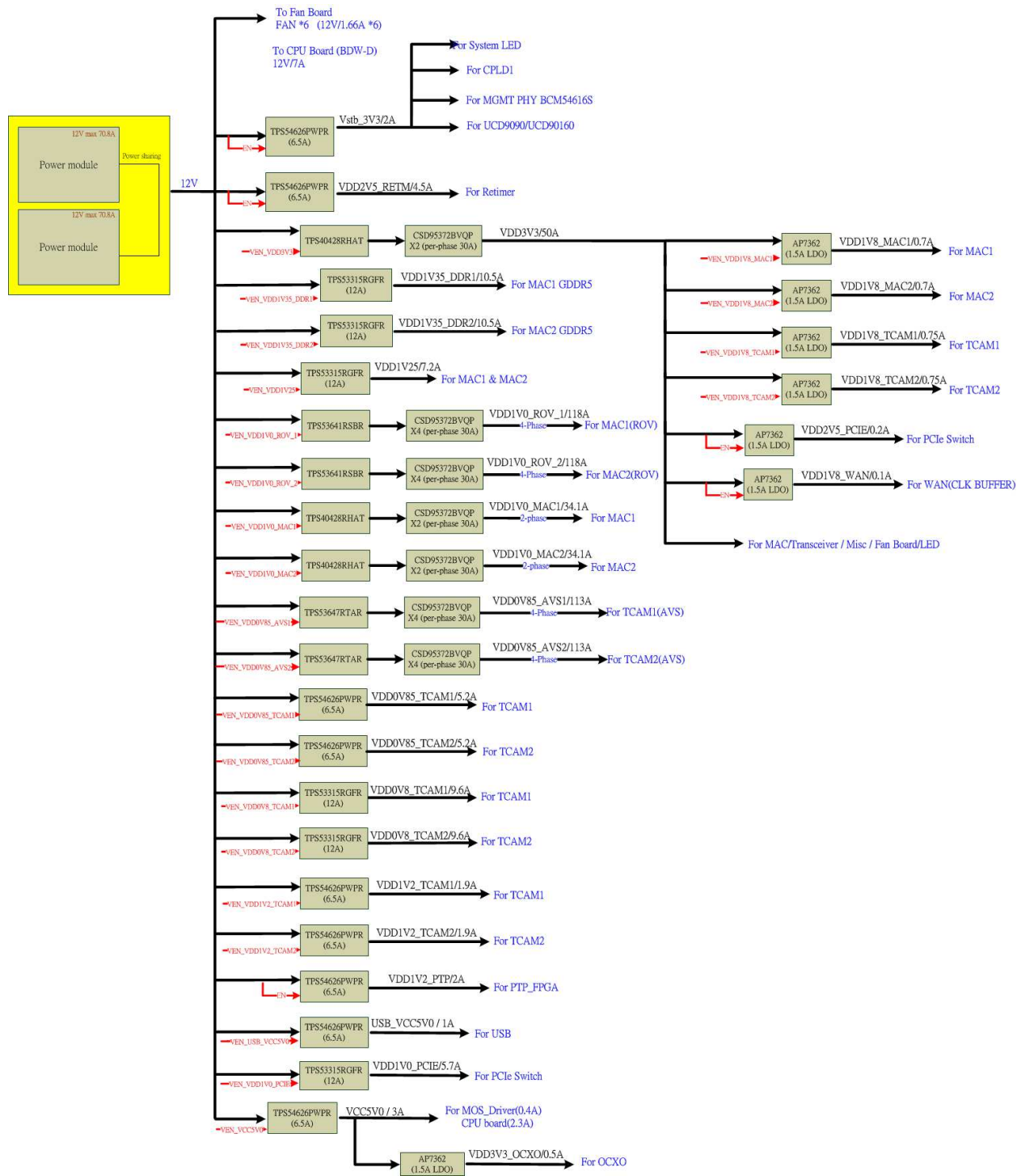
Number	Name	Number	Name
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19	- JC2 AGND	74	- JTAG
20	- JC2 AGND	75	- Jitter_Attenuator
21	- JC2 GND	76	- CLKGEN_100MHz
22	- OP2 MISC	77	- CLKBUFF_25MHz
23	- OP2 SerDes	78	- CLKBUFF_156.25&312.5MHz
24	- OP2 Power	79	- WAN PLL
25	- OP2 DDR POWER	80	- CLKBUFF_4K/10M/1PPS
26	- OP2 CAPs of AVS	81	- PWR_InRush_Circuit
27	- OP2 GND	82	- PWR_Vstb_3V3/PCIE1V8
28	- GBX1 SerDes & MISC	83	- PWR_VDD5V
29	- GBX1 Power	84	- PWR_VDD3V3/PCIE_0V9
30	- GBX2 SerDes & MISC	85	- PWR_TRX3V3
31	- GBX2 Power	86	- PWR_J2R0V_Control1
32	- GBX3 SerDes & MISC	87	- PWR_J2R0V_SPS_P1-6
33	- GBX3 Power	88	- PWR_J2R0V_SPS_P7-12
34	- GBX4 SerDes & MISC	89	- PWR_J2TRVDD0V8
35	- GBX4 Power	90	- PWR_J2PVDD0V8/TVDD1V2
36	- GBX5 SerDes & MISC	91	- PWR_J2VPP2V5/0V88
37	- GBX5 Power	92	- PWR_J2VDDC1V2/VDD01V2
38	- GBX6 SerDes & MISC	93	- PWR_GBAVDD0V8_MOS
39	- GBX6 Power	94	- PWR_OP2AVS0V85
40	- GBX7 SerDes & MISC	95	- PWR_OP2AVS0V85_MOS
41	- GBX7 Power	96	- PWR_OP2PWRx5
42	- GBX8 SerDes & MISC	97	- PWR_GBAVDD0V8
43	- GBX8 Power	98	- PWR_GBTVDD1V/PVDD
44	- GBX9 SerDes & MISC	99	- PWR_GBAVS0V8_1-2
45	- GBX9 Power	100	- PWR_GBAVS0V8_3-4
46	- GBX10 SerDes & MISC	101	- PWR_GBAVS0V8_5
47	- GBX10 Power	102	- PWR_MON_UCD90320
48	- QSFP 01-04	103	- UCD90320 Closed Loop Marg
49	- QSFP 05-08	104	- PSU Fan Mini_I0 CONN
50	- QSFP 09-012	105	- High Speed B2B L
51	- QSFP 013-016	106	- High Speed B2B L
52	- QSFP 017-20	107	- High Speed B2B R
53	- QSFP 021-24	108	- High Speed B2B R
54	- QSFP 25-28		
55	- QSFP 29-32		

ASF6654BBX-1116 ZZ Revision History

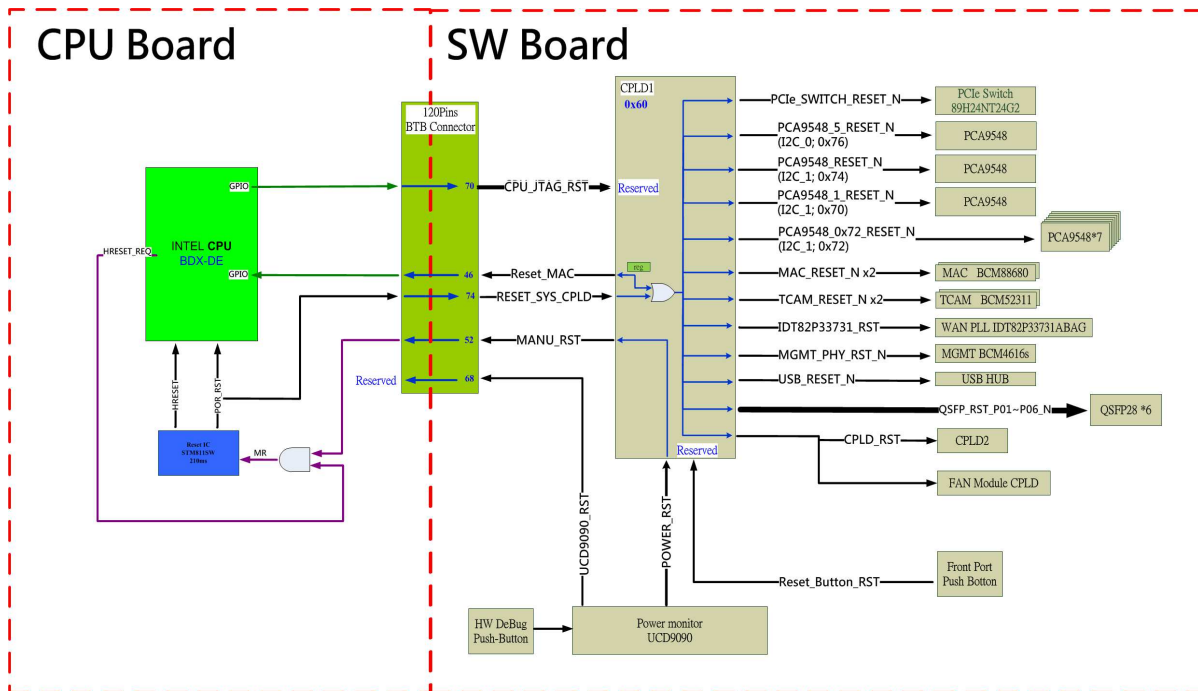
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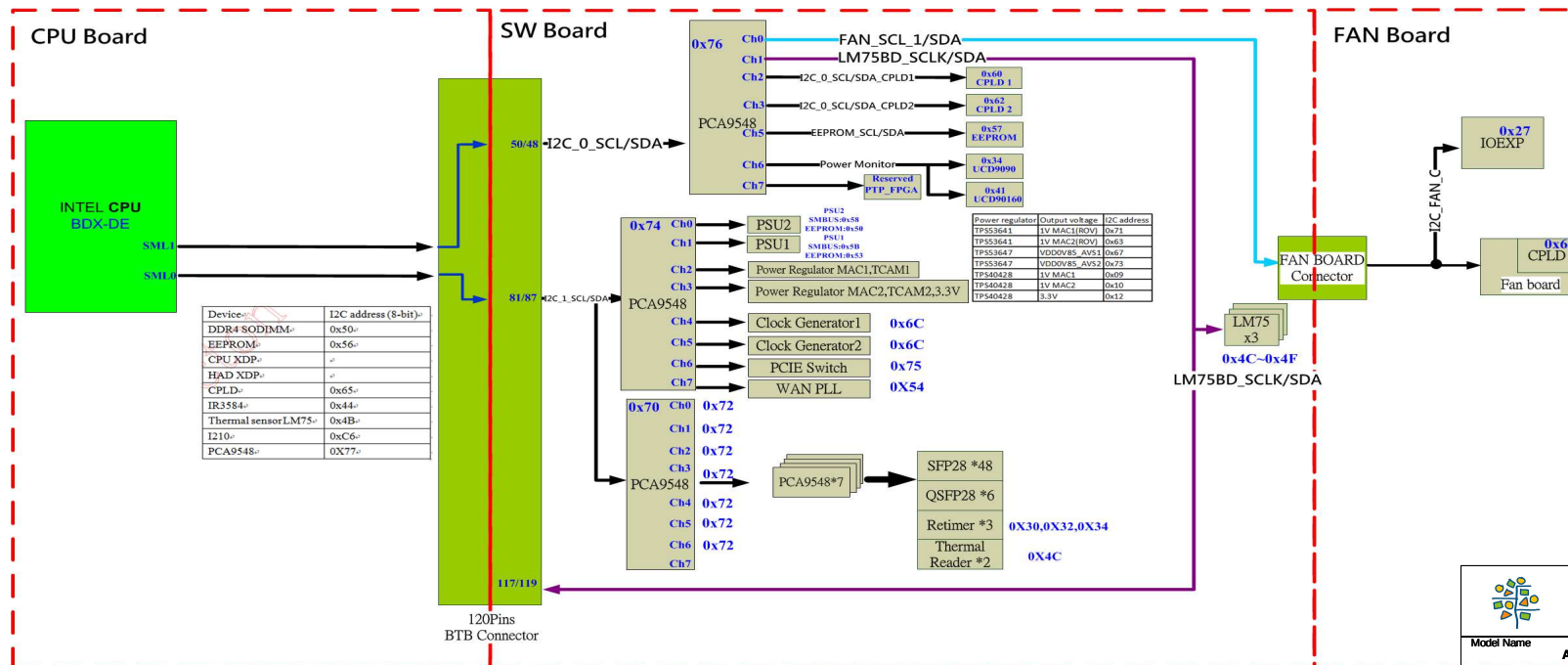


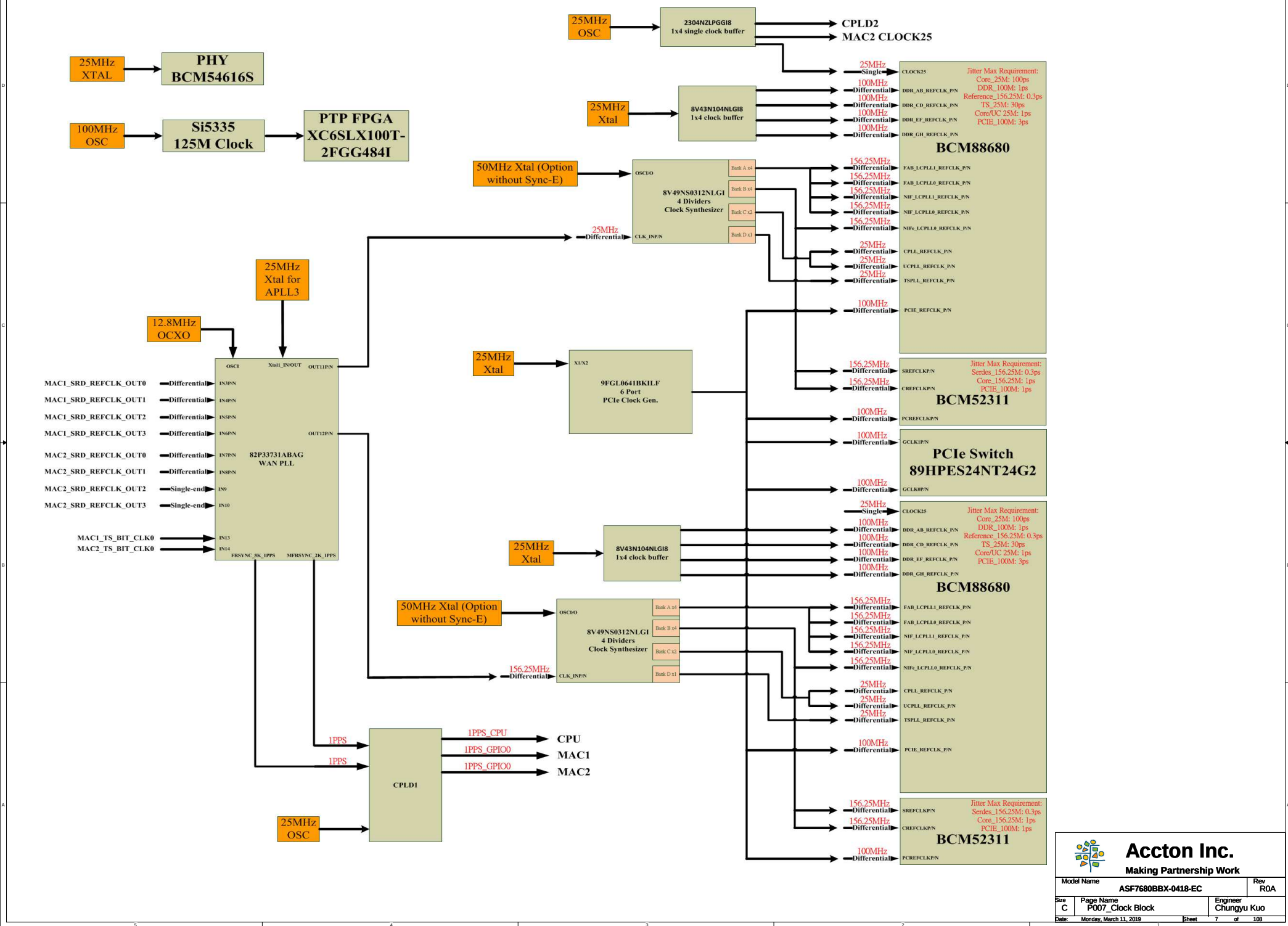


Reset BLOCK



I2C BLOCK





UID			Jericho-2 NIF SerDes #1		
[28]	GBX_J2_NIF0_RX_P_4	N9	NIF_RX_00_N	B14	J2_GBX_NIF0_TX_N_7
[28]	GBX_J2_NIF0_RX_N_4	N10	NIF_RX_00_P	C15	J2_GBX_NIF0_TX_P_7
[28]	GBX_J2_NIF0_RX_P_5	M7	NIF_RX_01_N	D16	J2_GBX_NIF0_TX_N_6
[28]	GBX_J2_NIF0_RX_N_5	M6	NIF_RX_01_P	D15	J2_GBX_NIF0_TX_P_6
[28]	GBX_J2_NIF0_RX_P_1	L10	NIF_RX_02_N	C17	J2_GBX_NIF0_TX_P_4
[28]	GBX_J2_NIF0_RX_N_1	K13	NIF_RX_02_P	D18	J2_GBX_NIF0_TX_N_4
[28]	GBX_J2_NIF0_RX_N_0	K32	NIF_RX_03_N	B16	J2_GBX_NIF0_TX_N_5
[28]	GBX_J2_NIF0_RX_P_0	P7	NIF_RX_03_P	C21	J2_GBX_NIF0_TX_P_5
[28]	GBX_J2_NIF0_RX_N_3	K6	NIF_RX_04_N	B15	J2_GBX_NIF0_TX_N_0
[28]	GBX_J2_NIF0_RX_P_5	R8	NIF_RX_04_P	D20	J2_GBX_NIF0_TX_P_0
[28]	GBX_J2_NIF0_RX_P_7	R10	NIF_RX_05_N	A20	J2_GBX_NIF0_TX_P_1
[28]	GBX_J2_NIF0_RX_N_7	M13	NIF_RX_05_P	C18	J2_GBX_NIF0_TX_N_1
[28]	GBX_J2_NIF0_RX_N_2	M12	NIF_RX_06_N	D19	J2_GBX_NIF0_TX_N_2
[28]	GBX_J2_NIF0_RX_P_2	P12	NIF_RX_06_P	B18	J2_GBX_NIF0_TX_P_2
[28]	GBX_J2_NIF0_RX_N_2	P13	NIF_RX_07_N	A18	J2_GBX_NIF0_TX_P_3
[28]	GBX_J2_NIF0_RX_P_6	L9	NIF_RX_07_P	D1	J2_GBX_NIF0_TX_N_3
[28]	GBX_J2_NIF1_RX_P_7	I10	NIF_RX_08_N	D2	J2_GBX_NIF1_TX_P_0
[28]	GBX_J2_NIF1_RX_N_7	I13	NIF_RX_08_P	P2	J2_GBX_NIF1_TX_N_0
[28]	GBX_J2_NIF1_RX_N_0	I12	NIF_RX_09_N	F1	J2_GBX_NIF1_TX_N_2
[28]	GBX_J2_NIF1_RX_P_3	V12	NIF_RX_09_P	G3	J2_GBX_NIF1_TX_P_2
[28]	GBX_J2_NIF1_RX_N_3	V13	NIF_RX_10_N	G4	J2_GBX_NIF1_TX_P_3
[28]	GBX_J2_NIF1_RX_P_3	T7	NIF_RX_10_P	E4	J2_GBX_NIF1_TX_N_3
[28]	GBX_J2_NIF1_RX_N_3	T6	NIF_RX_11_N	E3	J2_GBX_NIF1_TX_P_1
[28]	GBX_J2_NIF1_RX_P_6	Y6	NIF_RX_12_N	H1	J2_GBX_NIF1_TX_N_4
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[28]	GBX_J2_NIF1_RX_P_7	V7	NIF_RX_13_N	K2	J2_GBX_NIF1_TX_N_5
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[28]	GBX_J2_NIF1_RX_N_4	W10	NIF_RX_15_N	L4	J2_GBX_NIF1_TX_N_7
[28]	GBX_J2_NIF1_RX_P_4	AD12	NIF_RX_16_N	N4	J2_GBX_NIF2_TX_N_1
[28]	GBX_J2_NIF2_RX_P_4	AB13	NIF_RX_16_P	P1	J2_GBX_NIF2_TX_P_1
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[28]	GBX_J2_NIF2_RX_N_5	AD7	NIF_RX_23_N	W4	J2_GBX_NIF2_TX_N_6
[28]	GBX_J2_NIF2_RX_P_1	A39	NIF_RX_23_P	AA4	J2_GBX_NIF2_TX_N_7
[28]	GBX_J2_NIF3_RX_P_1	AE10	NIF_RX_24_N	AA3	J2_GBX_NIF3_TX_N_1
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[28]	GBX_J2_NIF3_RX_P_5	AH12	NIF_RX_25_P	AC4	J2_GBX_NIF3_TX_N_3
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[28]	GBX_J2_NIF3_RX_P_5	AM6	NIF_RX_30_N	AG4	J2_GBX_NIF3_TX_P_6
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[28]	GBX_J2_NIF5_RX_N_0	AY6	NIF_RX_44_N	AY2	J2_GBX_NIF5_TX_P_4
[28]	GBX_J2_NIF5_RX_P_4	BA10	NIF_RX_45_N	BA4	J2_GBX_NIF5_TX_N_5
[28]	GBX_J2_NIF5_RX_N_4	BB7	NIF_RX_46_P	BB1	J2_GBX_NIF5_TX_P_5
[28]	GBX_J2_NIF5_RX_P_7	AY12	NIF_RX_47_N	BC4	J2_GBX_NIF5_TX_P_6
[28]	GBX_J2_NIF5_RX_N_7			BC3	J2_GBX_NIF5_TX_P_7

BCM88690CA0KFSBG

UID			Jericho-2 NIF SerDes #2		
[23]	KBP_J2_ILNK_RX_P_49	CA29	NIF_RX_48_N	CC28	J2_KBP_ILKN_TX_P_48
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[23]	KBP_J2_ILNK_RX_P_48	BV27	NIF_RX_49_P	CC26	J2_KBP_ILKN_TX_N_49
[23]	KBP_J2_ILNK_RX_N_50	BV26	NIF_RX_50_N	CC25	J2_KBP_ILKN_TX_P_50
[23]	KBP_J2_ILNK_RX_P_50	BV25	NIF_RX_50_P	CF25	J2_KBP_ILKN_TX_N_50
[23]	KBP_J2_ILNK_RX_N_51	BV24	NIF_RX_51_N	CC24	J2_KBP_ILKN_TX_P_51
[23]	KBP_J2_ILNK_RX_P_51	BV23	NIF_RX_51_P	CF24	J2_KBP_ILKN_TX_N_51
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[23]	KBP_J2_ILNK_RX_N_53	BV20	NIF_RX_53_N	CC22	J2_KBP_ILKN_TX_P_53
[23]	KBP_J2_ILNK_RX_P_53	BV19	NIF_RX_53_P	CF22	J2_KBP_ILKN_TX_N_53
[23]	KBP_J2_ILNK_RX_N_54	BV18	NIF_RX_54_N	CC21	J2_KBP_ILKN_TX_P_54
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[23]	KBP_J2_ILNK_RX_N_58	BV10	NIF_RX_58_N	CC17	J2_KBP_ILKN_TX_P_58
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[23]	KBP_J2_ILNK_RX_N_64	BV00	NIF_RX_64_N	CC11	J2_KBP_ILKN_TX_P_64
[23]	KBP_J2_ILNK_RX_P_64	BV00	NIF_RX_64_P	CF11	J2_KBP_ILKN_TX_N_64
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[23]	KBP_J2_ILNK_RX_N_68	BV00	NIF_RX_68_N	CC07	J2_KBP_ILKN_TX_P_68
[23]	KBP_J2_ILNK_RX_P_68	BV00	NIF_RX_68_P	CF07	J2_KBP_ILKN_TX_N_68
[23]	KBP_J2_ILNK_RX_N_69	BV00	NIF_RX_69_N	CC06	J2_KBP_ILKN_TX_P_69
[23]	KBP_J2_ILNK_RX_P_69	BV00	NIF_RX_69_P	CF06	J2_KBP_ILKN_TX_N_69
[23]	KBP_J2_ILNK_RX_N_70	BV00	NIF_RX_70_N	CC05	J2_KBP_ILKN_TX_P_70
[23]	KBP_J2_ILNK_RX_P_70	BV00	NIF_RX_70_P	CF05	J2_KBP_ILKN_TX_N_70
[23]	KBP_J2_ILNK_RX_N_71	BV00	NIF_RX_71_N	CC04	J2_KBP_ILKN_TX_P_71
[23]	KBP_J2_ILNK_RX_P_71	BV00	NIF_RX_71_P	CF04	J2_KBP_ILKN_TX_N_71
[23]	KBP_J2_ILNK_RX_N_72	BV00	NIF_RX_72_N	CC03	J2_KBP_ILKN_TX_P_72
[23]	KBP_J2_ILNK_RX_P_72	BV00	NIF_RX_72_P	CF03	J2_KBP_ILKN_TX_N_72
[23]	KBP_J2_ILNK_RX_N_73	BV00	NIF_RX_73_N	CC02	J2_KBP_ILKN_TX_P_73
[23]	KBP_J2_ILNK_RX_P_73	BV00	NIF_RX_73_P	CF02	J2_KBP_ILKN_TX_N_73
[23]	KBP_J2_ILNK_RX_N_74	BV00	NIF_RX_74_N	CC01	J2_KBP_ILKN_TX_P_74
[23]	KBP_J2_ILNK_RX_P_74	BV00	NIF_RX_74_P	CF01	J2_KBP_ILKN_TX_N_74
[23]	KBP_J2_ILNK_RX_N_75	BV00	NIF_RX_75_N	CC00	J2_KBP_ILKN_TX_P_75
[23]	KBP_J2_ILNK_RX_P_75	BV00	NIF_RX_75_P	CF00	J2_KBP_ILKN_TX_N_75
[23]	KBP_J2_ILNK_RX_N_76	BV00	NIF_RX_76_N	CC00	J2_KBP_ILKN_TX_P_76
[23]	KBP_J2_ILNK_RX_P_76	BV00	NIF_RX_76_P	CF00	J2_KBP_ILKN_TX_N_76
[23]	KBP_J2_ILNK_RX_N_77	BV00	NIF_RX_77_N	CC00	J2_KBP_ILKN_TX_P_77
[23]	KBP_J2_ILNK_RX_P_77	BV00	NIF_RX_77_P	CF00	J2_KBP_ILKN_TX_N_77
[23]	KBP_J2_ILNK_RX_N_78	BV00	NIF_RX_78_N	CC00	J2_KBP_ILKN_TX_P_78
[23]	KBP_J2_ILNK_RX_P_78	BV00	NIF_RX_78_P	CF00	J2_KBP_ILKN_TX_N_78
[23]	KBP_J2_ILNK_RX_N_79	BV00	NIF_RX_79_N	CC00	J2_KBP_ILKN_TX_P_79
[23]	KBP_J2_ILNK_RX_P_79	BV00	NIF_RX_79_P	CF00	J2_KBP_ILKN_TX_N_79
[23]	KBP_J2_ILNK_RX_N_80	BV00	NIF_RX_80_N	CC00	J2_KBP_ILKN_TX_P_80
[23]	KBP_J2_ILNK_RX_P_80	BV00	NIF_RX_80_P	CF00	J2_KBP_ILKN_TX_N_80
[23]	KBP_J2_ILNK_RX_N_81	BV00	NIF_RX_81_N	CC00	J2_KBP_ILKN_TX_P_81
[23]	KBP_J2_ILNK_RX_P_81	BV00	NIF_RX_81_P	CF00	J2_KBP_ILKN_TX_N_81
[23]	KBP_J2_ILNK_RX_N_82	BV00	NIF_RX_82_N	CC00	J2_KBP_ILKN_TX_P_82
[23]	KBP_J2_ILNK_RX_P_82	BV00	NIF_RX_82_P	CF00	J2_KBP_ILKN_TX_N_82
[23]	KBP_J2_ILNK_RX_N_83	BV00	NIF_RX_83_N	CC00	J2_KBP_ILKN_TX_P_83
[23]	KBP_J2_ILNK_RX_P_83	BV00	NIF_RX_83_P	CF00	J2_KBP_ILKN_TX_N_83
[23]	KBP_J2_ILNK_RX_N_84	BV00	NIF_RX_84_N	CC00	J2_KBP_ILKN_TX_P_84
[23]	KBP_J2_ILNK_RX_P_84	BV00	NIF_RX_84_P	CF00	J2_KBP_ILKN_TX_N_84
[23]	KBP_J2_ILNK_RX_N_85	BV00	NIF_RX_85_N	CC00	J2_KBP_ILKN_TX_P_85
[23]	KBP_J2_ILNK_RX_P_85	BV00	NIF_RX_85_P	CF00	J2_KBP_ILKN_TX_N_85
[23]	KBP_J2_ILNK_RX_N_86	BV00	NIF_RX_86_N	CC00	J2_KBP_ILKN_TX_P_86
[23]	KBP_J2_ILNK_RX_P_86	BV00	NIF_RX_86_P	CF00	J2_KBP_ILKN_TX_N_86
[23]	KBP_J2_ILNK_RX_N_87	BV00	NIF_RX_87_N	CC00	J2_KBP_ILKN_TX_P_87
[23]	KBP_J2_ILNK_RX_P_87	BV00	NIF_RX_87_P	CF00	J2_KBP_ILKN_TX_N_87
[23]	KBP_J2_ILNK_RX_N_88	BV00	NIF_RX_88_N	CC00	J2_KBP_ILKN_TX_P_88
[23]	KBP_J2_ILNK_RX_P_88	BV00	NIF_RX_88_P	CF00	J2_KBP_ILKN_TX_N_88
[23]	KBP_J2_ILNK_RX_N_89	BV00	NIF_RX_89_N	CC00	J2_KBP_ILKN_TX_P_89
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[23]	KBP_J2_ILNK_RX_N_90	BV00	NIF_RX_90_N	CC00	J2_KBP_ILKN_TX_P_90
[23]	KBP_J2_ILNK_RX_P_90	BV00	NIF_RX_90_P	CF00	J2_KBP_ILKN_TX_N_90
[23]	KBP_J2_ILNK_RX_N_91	BV00	NIF_RX_91_N	CC00	J2_KBP_ILKN_TX_P_91
[23]	KBP_J2_ILNK_RX_P_91	BV00	NIF_RX_91_P	CF00	J2_KBP_ILKN_TX_N_91
[23]	KBP_J2_ILNK_RX_N_92	BV00	NIF_RX_92_N	CC00	J2_KBP_ILKN_TX_P_92
[23]	KBP_J2_ILNK_RX_P_92	BV00	NIF_RX_92_P	CF00	J2_KBP_ILKN_TX_N_92
[23]	KBP_J2_ILNK_RX_N_93	BV00	NIF_RX_93_N	CC00	J2_KBP_ILKN_TX_P_93
[23]	KBP_J2_ILNK_RX_P_93	BV00	NIF_RX_93_P	CF00	J2_KBP_ILKN_TX_N_93
[23]	KBP_J2_ILNK_RX_N_94	BV00	NIF_RX_94_N	CC00	J2_KBP_ILKN_TX_P_94
[23]	KBP_J2_ILNK_RX_P_94	BV00	NIF_RX_94_P	CF00	J2_KBP_ILKN_TX_N_94
[23]	KBP_J2_ILNK_RX_N_95	BV00	NIF_RX_95_N	CC00	J2_KBP_ILKN_TX_P_95
[23]	KBP_J2_ILNK_RX_P_95	BV00	NIF_RX_95_P	CF00	J2_KBP_ILKN_TX_N_95

BCM88690CA0KFSBG

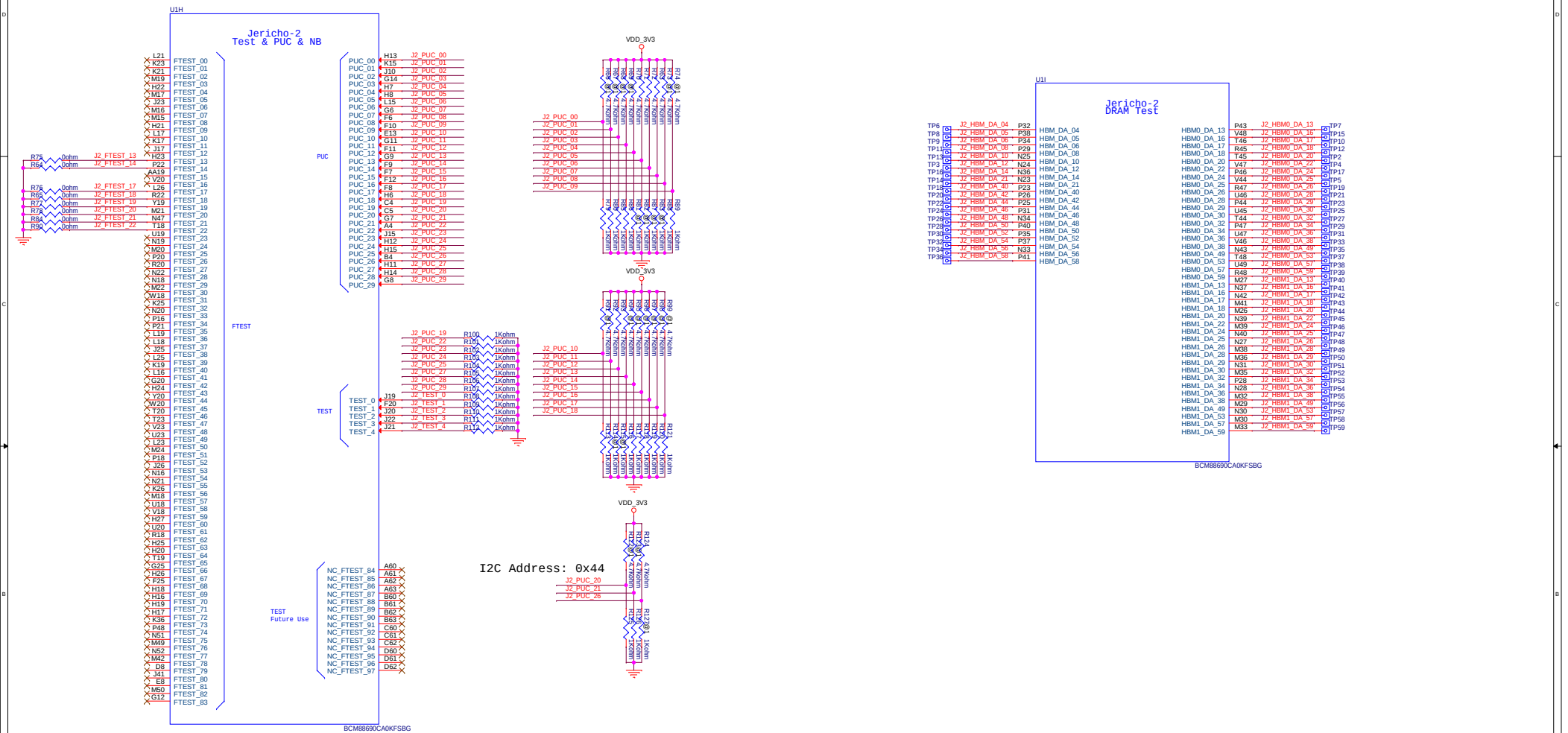
**Accton Inc.**

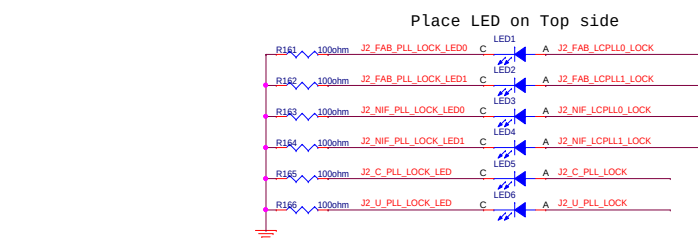
Making Partnership Work

Model Name		ASF7680BBX-0418-EC	Rev	ROA
Size	Page Name	P009_JC2 Network Interface	Engineer	Chungyu Kuo
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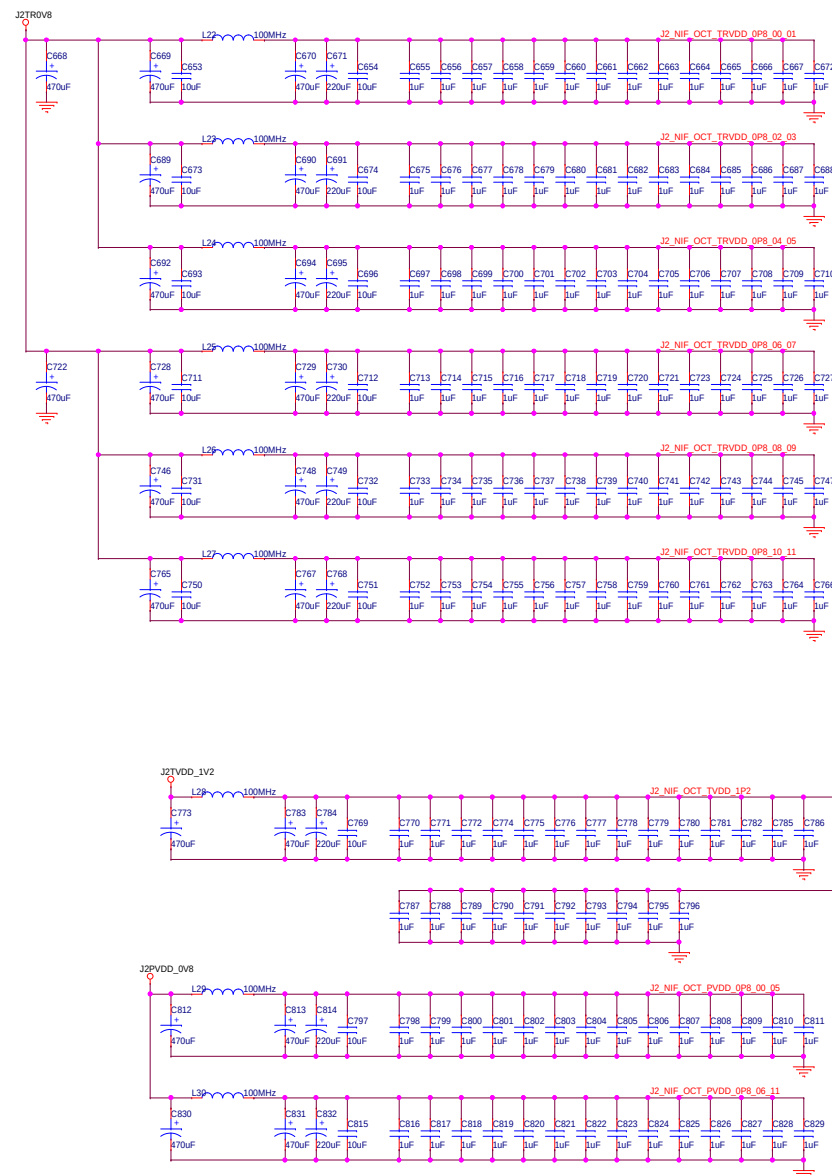
PUC	Function	Description
PUC_00	CORE_PLL_N_DIV[9:0]	Set to 240 (PUC[9:0] = 0x0F0)
PUC_01	CORE_PLL_M_DIV[8:0]	Set to 6 (PUC[18:0] = 0x018F0)
PUC_02	Factory test.	Set to 0
PUC_03	I2C_SA[1:0]	Device I2C (BSC) slave address LSB. When the I2C (BSC) interface is used, the device physical address is made up of the following 7 bits: ■ [A6, A5, A4, A3, A2] fixed as 0x10001 ■ [A1, A0] set according to [PUC_21, PUC_20] Available addresses are 0x44, 0x45, 0x46, and 0x47.
PUC_25:22	Factory test.	Set to 0x0
PUC_26	PCIE_GSPI_ENABLE	Set to 1.
PUC_29:27	Factory test.	Set to 0x0

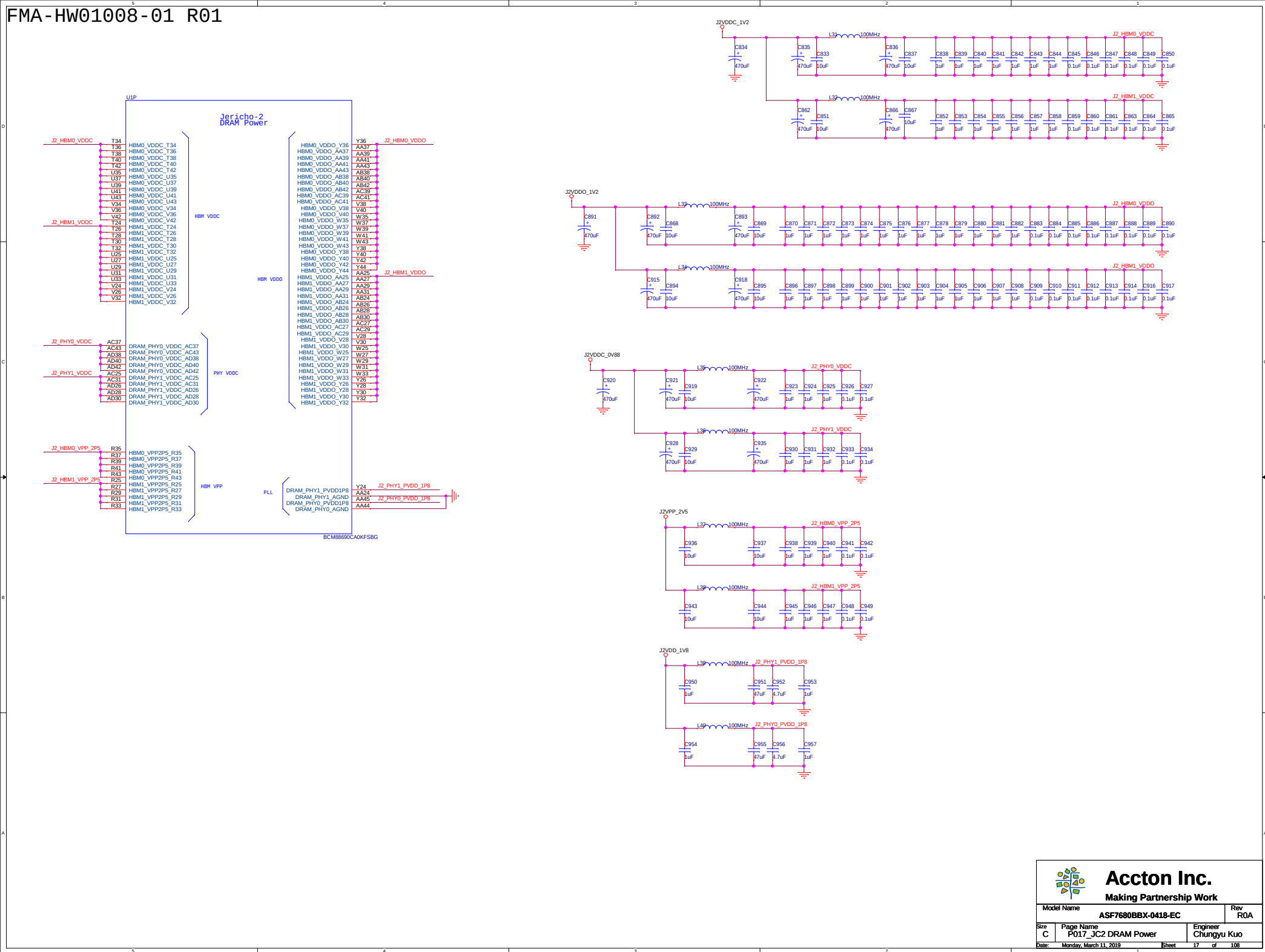




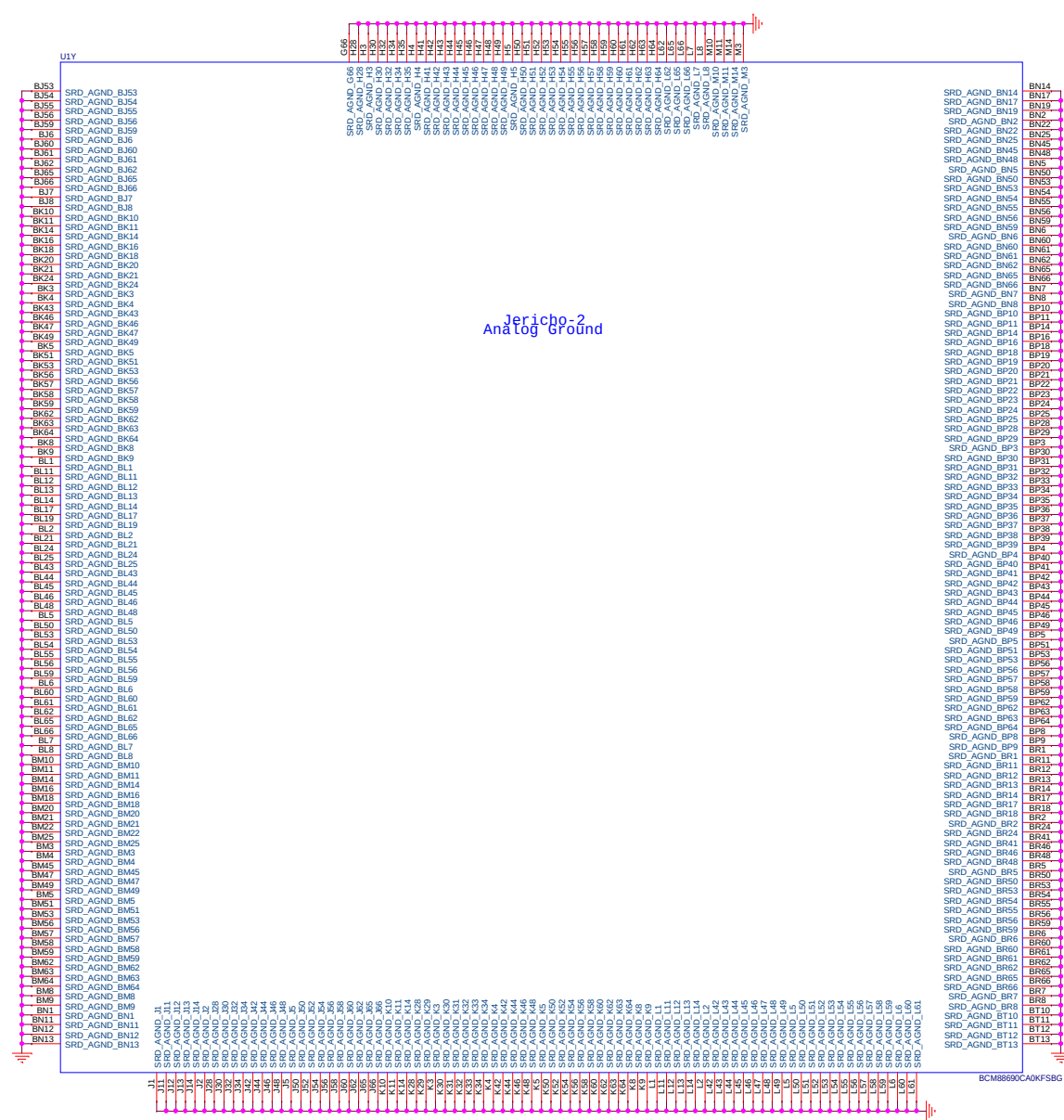


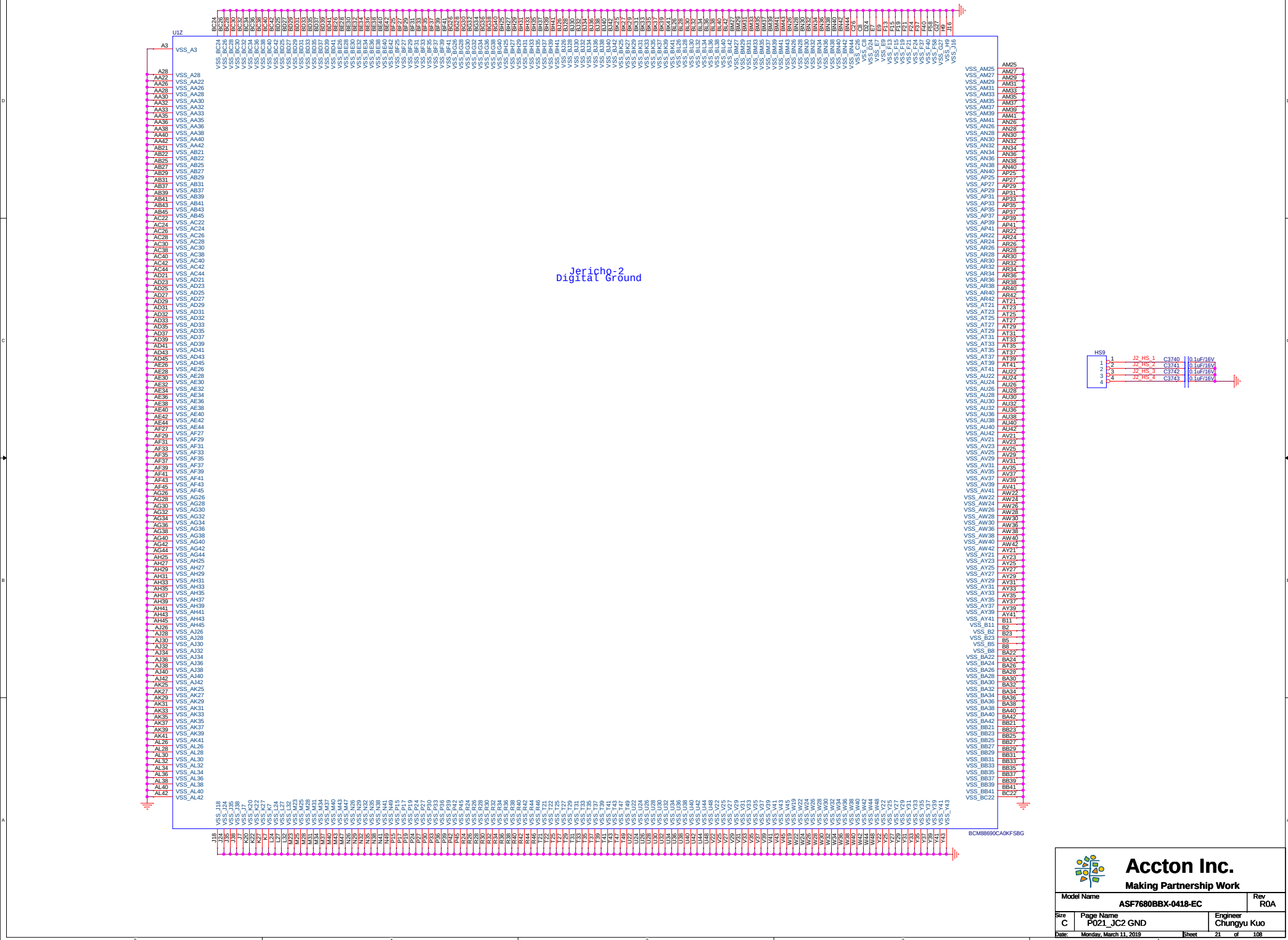


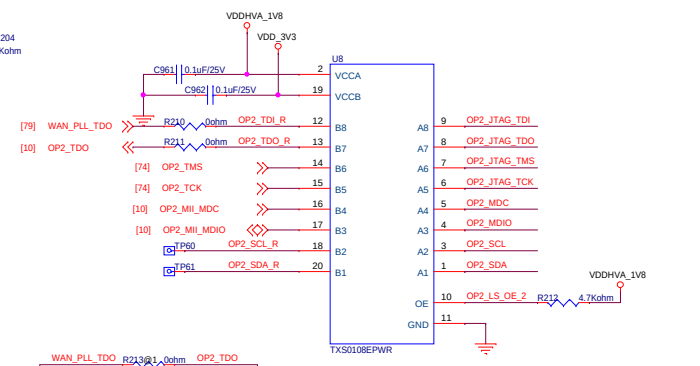


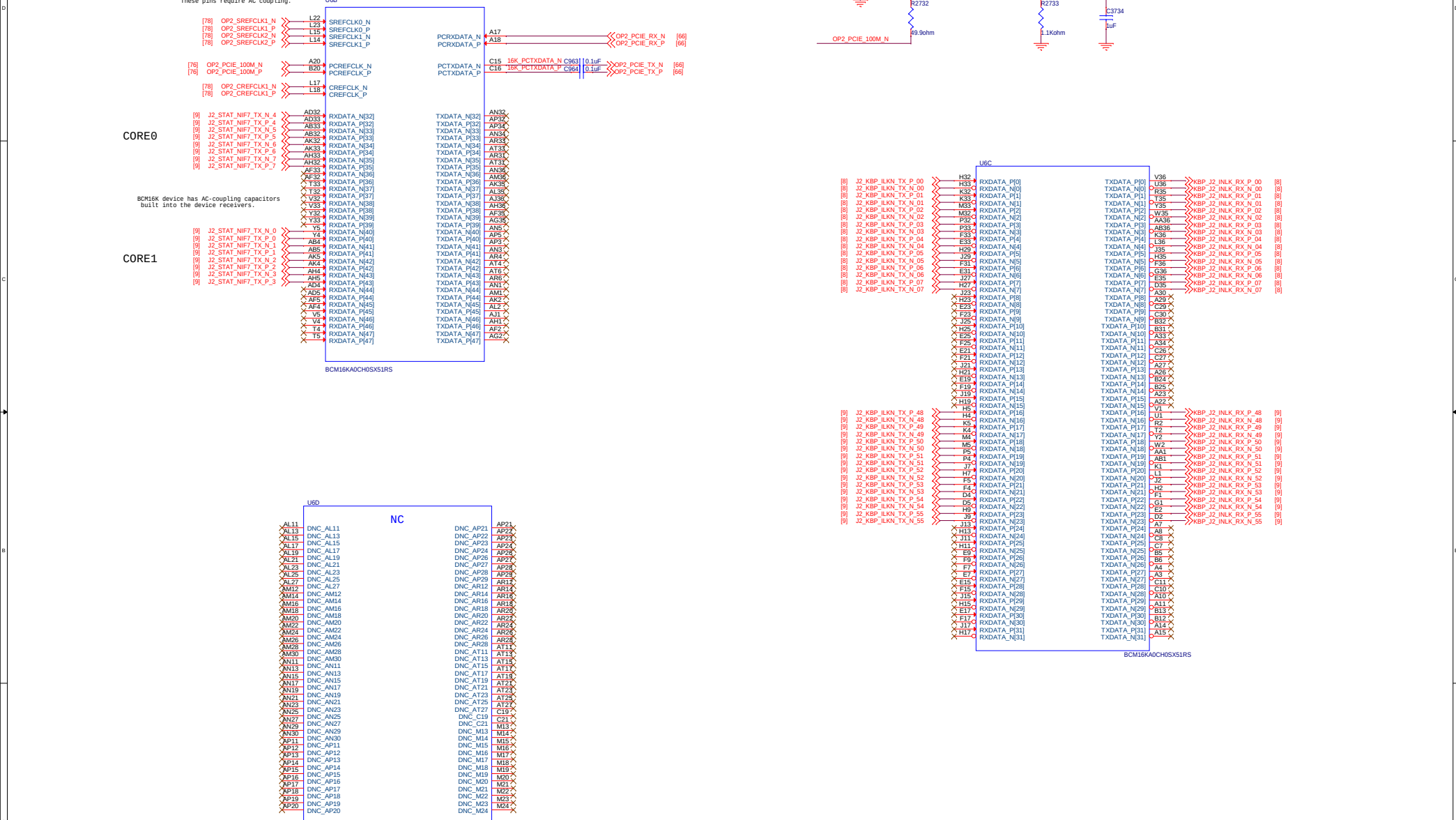


U1Q		
Jericho-2 Analog Ground		
BY36	SRD_AGND_BY36	CB26
BY38	SRD_AGND_BY38	CB27
BY4	SRD_AGND_BY4	CB28
BY40	SRD_AGND_BY40	CB29
BY42	SRD_AGND_BY42	CB30
BY44	SRD_AGND_BY44	CB31
BY46	SRD_AGND_BY46	CB32
BY48	SRD_AGND_BY48	CB33
BY50	SRD_AGND_BY50	CB34
BY52	SRD_AGND_BY52	CB35
BY54	SRD_AGND_BY54	CB36
BY56	SRD_AGND_BY56	CB37
BY58	SRD_AGND_BY58	CB38
BY6	SRD_AGND_BY6	CB39
BY60	SRD_AGND_BY60	CB40
BY62	SRD_AGND_BY62	CB41
BY64	SRD_AGND_BY64	CB42
BY66	SRD_AGND_BY66	CB43
BY68	SRD_AGND_BY68	CB44
BY70	SRD_AGND_BY70	CB45
BY72	SRD_AGND_BY72	CB46
BY74	SRD_AGND_BY74	CB47
BY76	SRD_AGND_BY76	CB48
BY78	SRD_AGND_BY78	CB49
BY80	SRD_AGND_BY80	CB50
BY82	SRD_AGND_BY82	CB51
BY84	SRD_AGND_BY84	CB52
BY86	SRD_AGND_BY86	CB53
BY88	SRD_AGND_BY88	CB54
BY90	SRD_AGND_BY90	CB55
BY92	SRD_AGND_BY92	CB56
BY94	SRD_AGND_BY94	CB57
BY96	SRD_AGND_BY96	CB58
BY98	SRD_AGND_BY98	CB59
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BY102	SRD_AGND_BY102	CB61
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BY106	SRD_AGND_BY106	CB63
BY108	SRD_AGND_BY108	CB64
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BY112	SRD_AGND_BY112	CB66
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BY624	SRD_AGND_BY624	CB322
BY626	SRD_AGND_BY626	CB323
BY628	SRD_AGND_BY628	CB324
BY630	SRD_AGND_BY630	CB325
BY632	SRD_AGND_BY632	CB326
BY634	SRD_AGND_BY634	CB327
BY636	SRD_AGND_BY636	CB328
BY638	SRD_AGND_BY638	CB329
BY640	SRD_AGND_BY640	CB330
BY642	SRD_AGND_BY642	CB331
BY644	SRD_AGND_BY644	CB332
BY646	SRD_AGND_BY646	CB333
BY648	SRD_AGND_BY648	CB334
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BY652	SRD_AGND_BY652	CB336
BY654	SRD_AGND_BY654	CB337
BY656	SRD_AGND_BY656	CB338
BY658	SRD_AGND_BY658	CB339
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BY662	SRD_AGND_BY662	CB341
BY664	SRD_AGND_BY664	CB342
BY666	SRD_AGND_BY666	CB343
BY668	SRD_AGND_BY668	CB344
BY670	SRD_AGND_BY670	CB345
BY672	SRD_AGND_BY672	CB346
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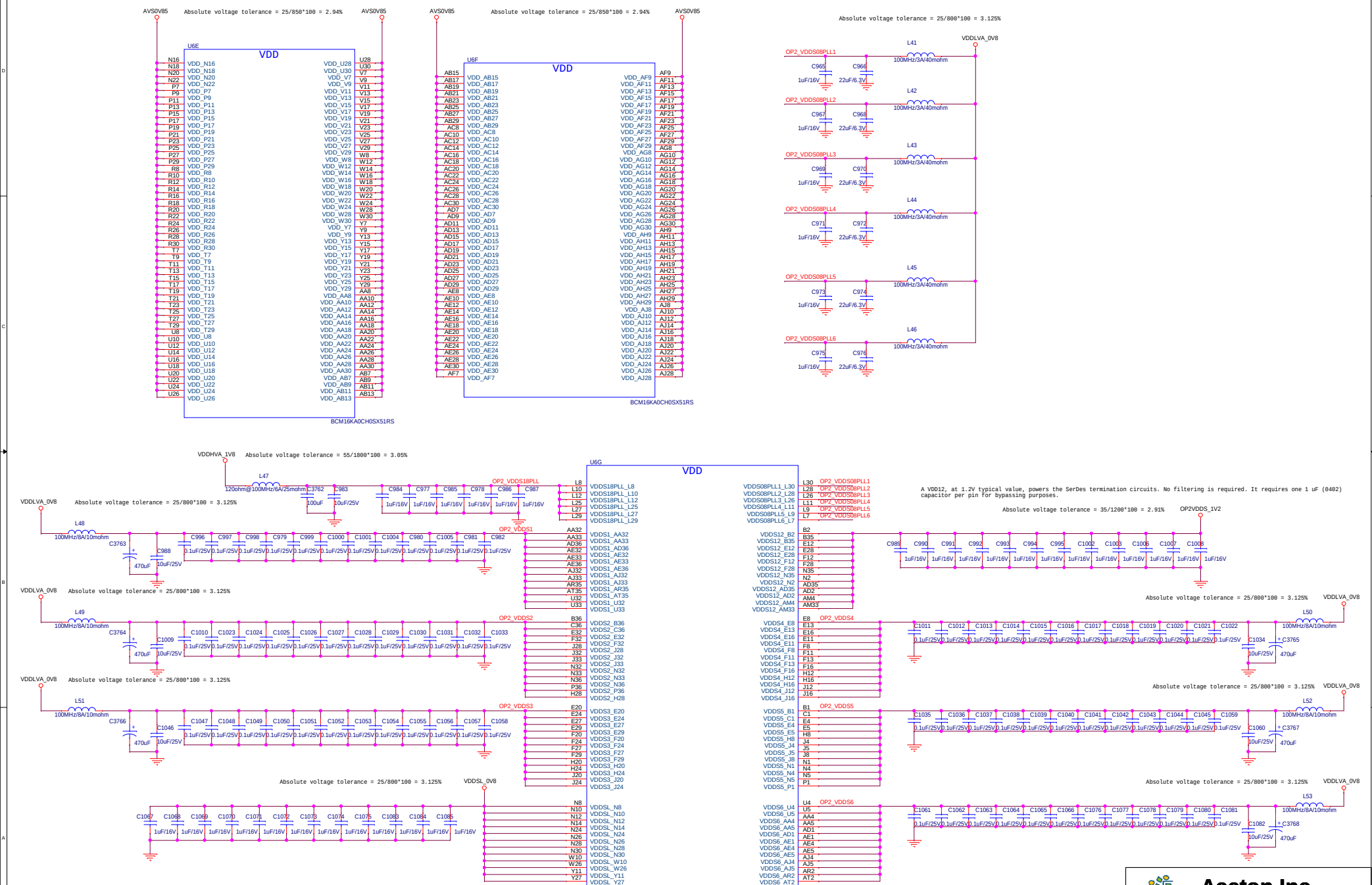








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FMA-HW01008-01 R01

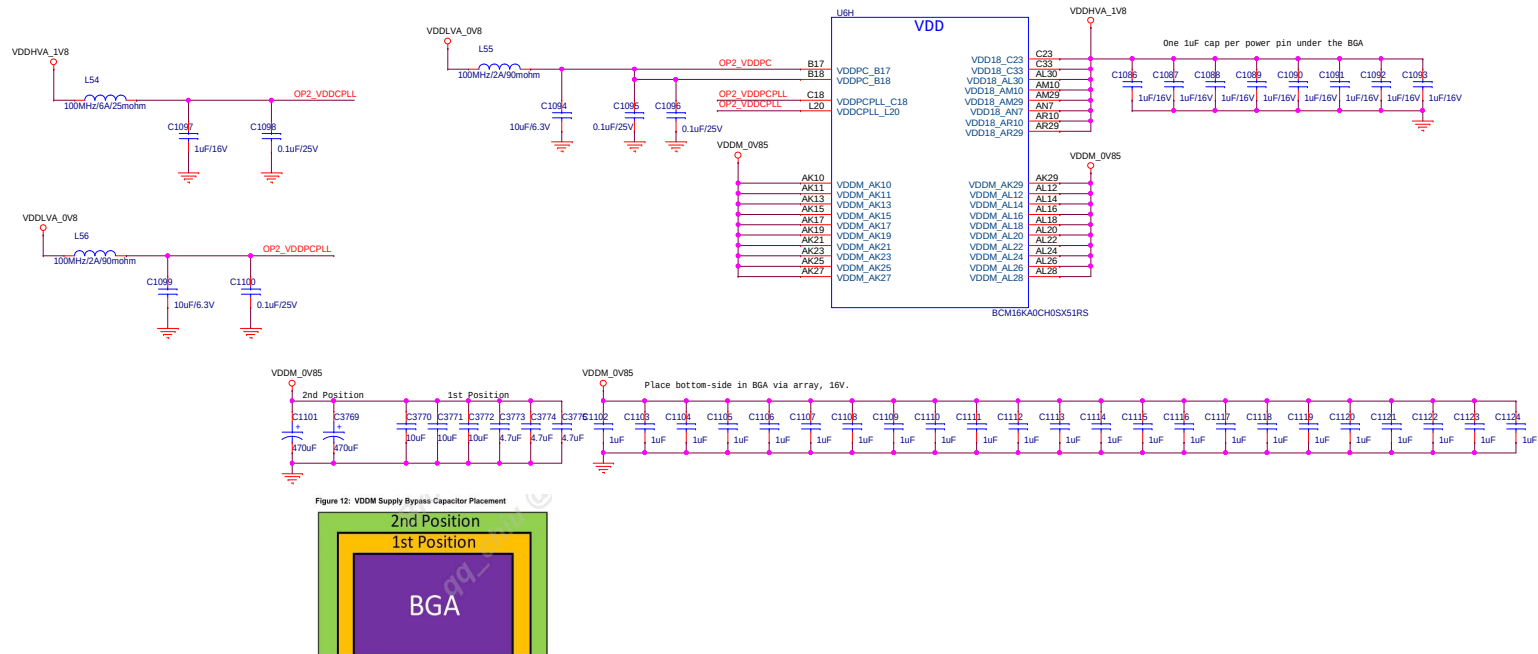
**Accton Inc.**

Model Name	ASF7680BBX-0418-EC
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Size	Page Name
C	P024 OP2 Power

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Chungyu Kuo

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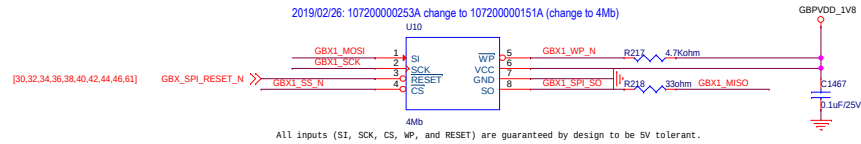
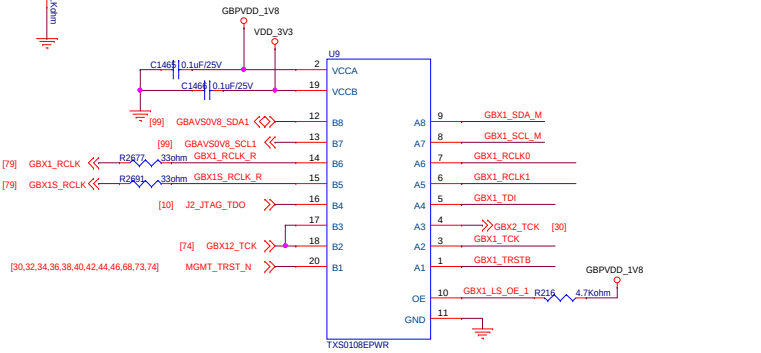
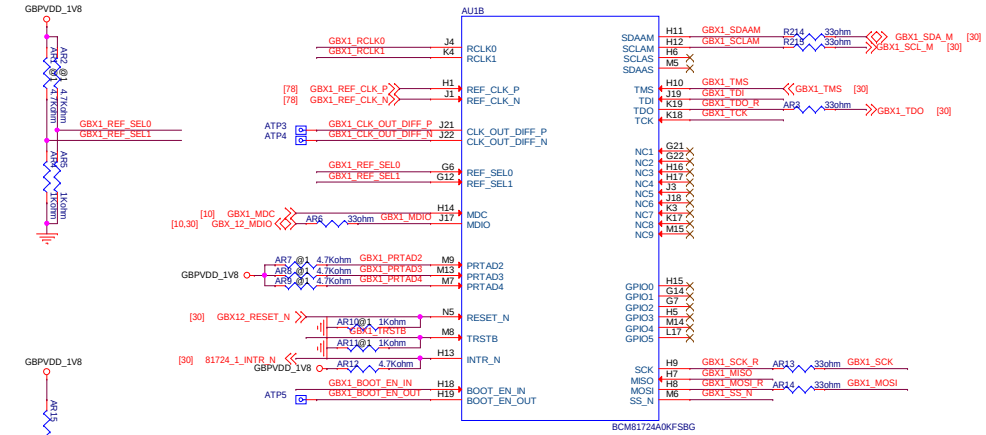
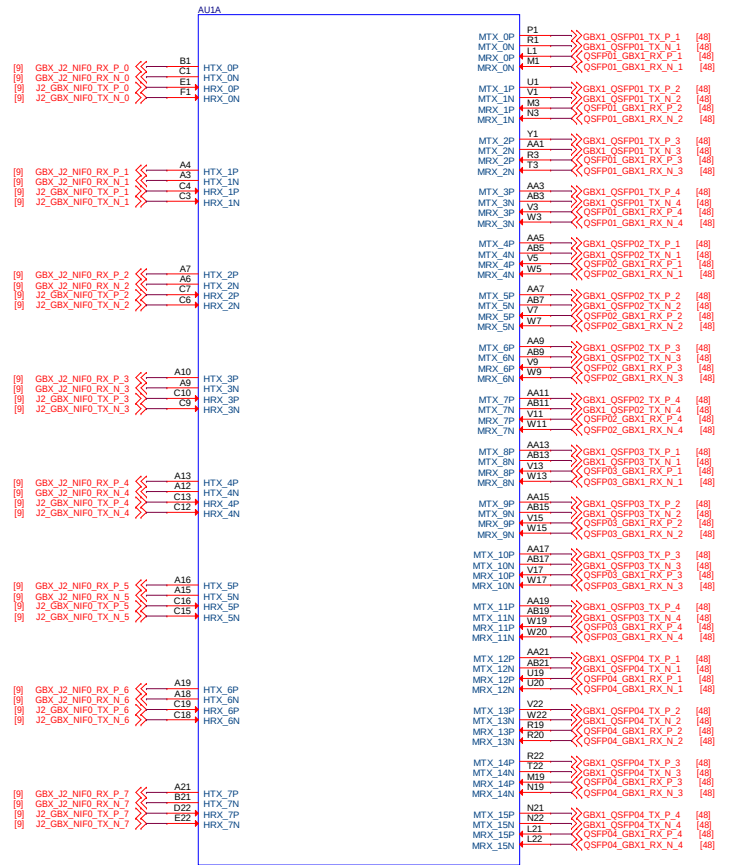


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		Accton Inc. Making Partnership Work	
Model Name		ASF7680BBX-0418-EC	Rev ROA
Size C	Page Name P026_OP2 CAPS of AVS		Engineer Chungyu Kuo
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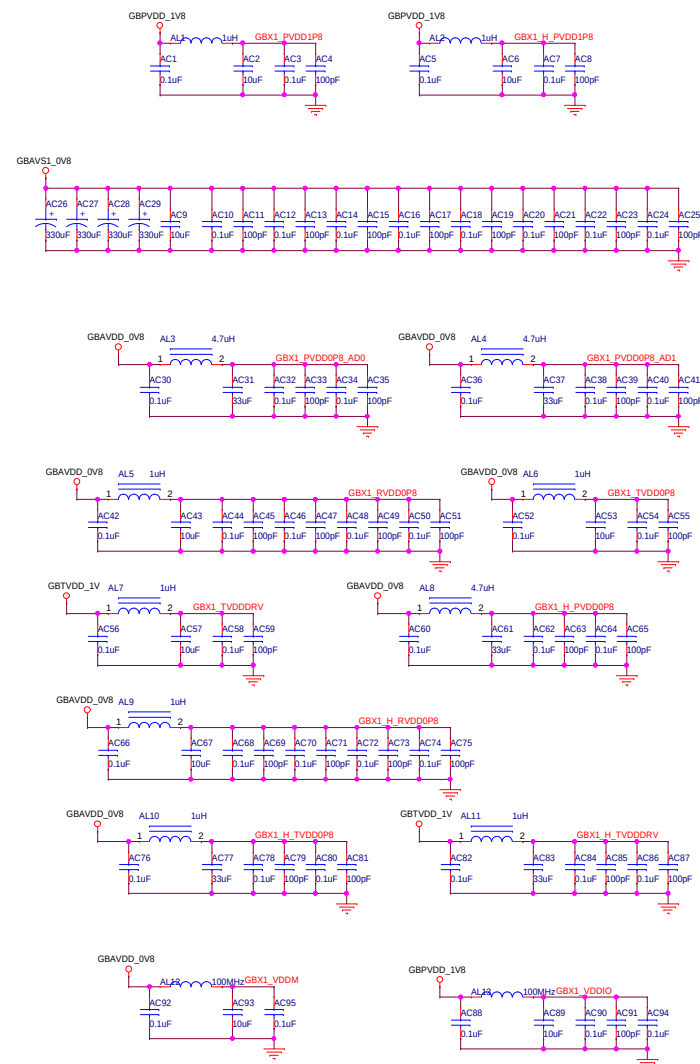
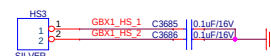
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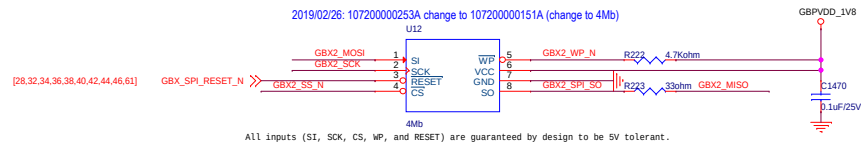
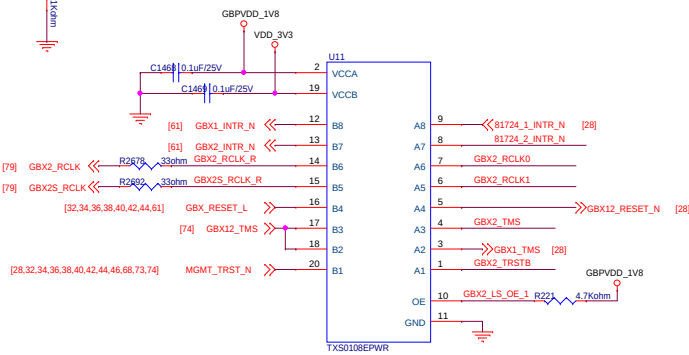
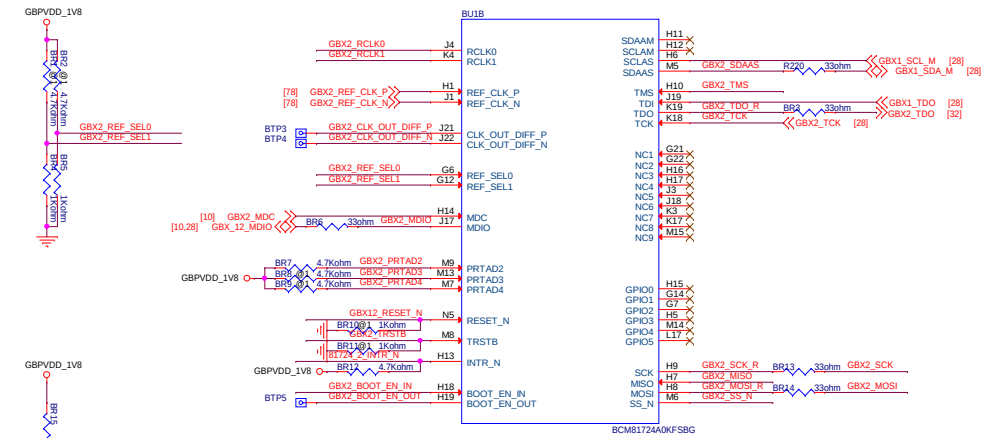
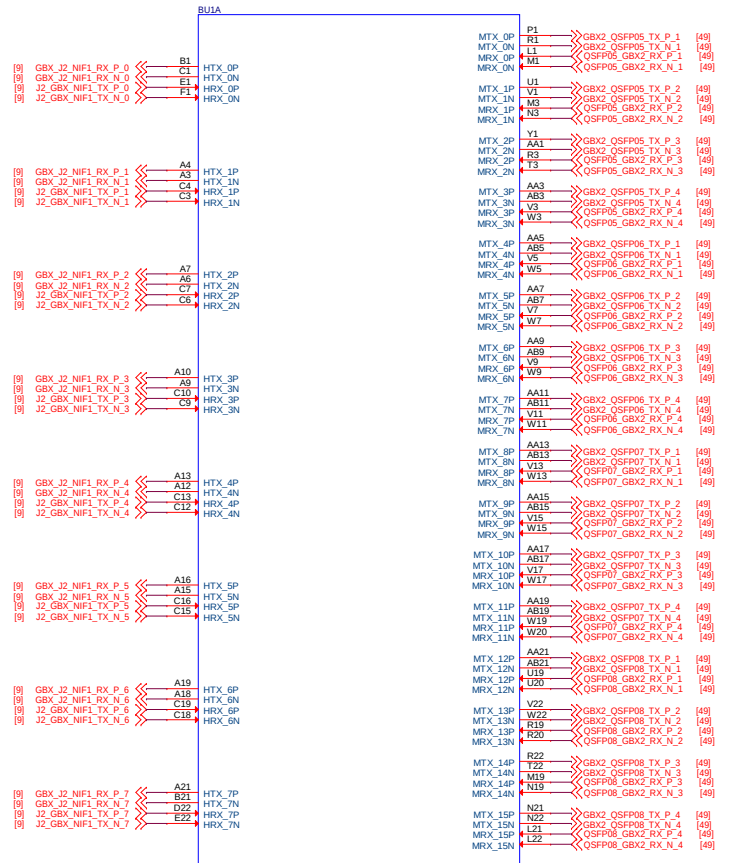
All inputs (SI, SCK, CS, WP, and RESET) are guaranteed by design to be 5V tolerant.

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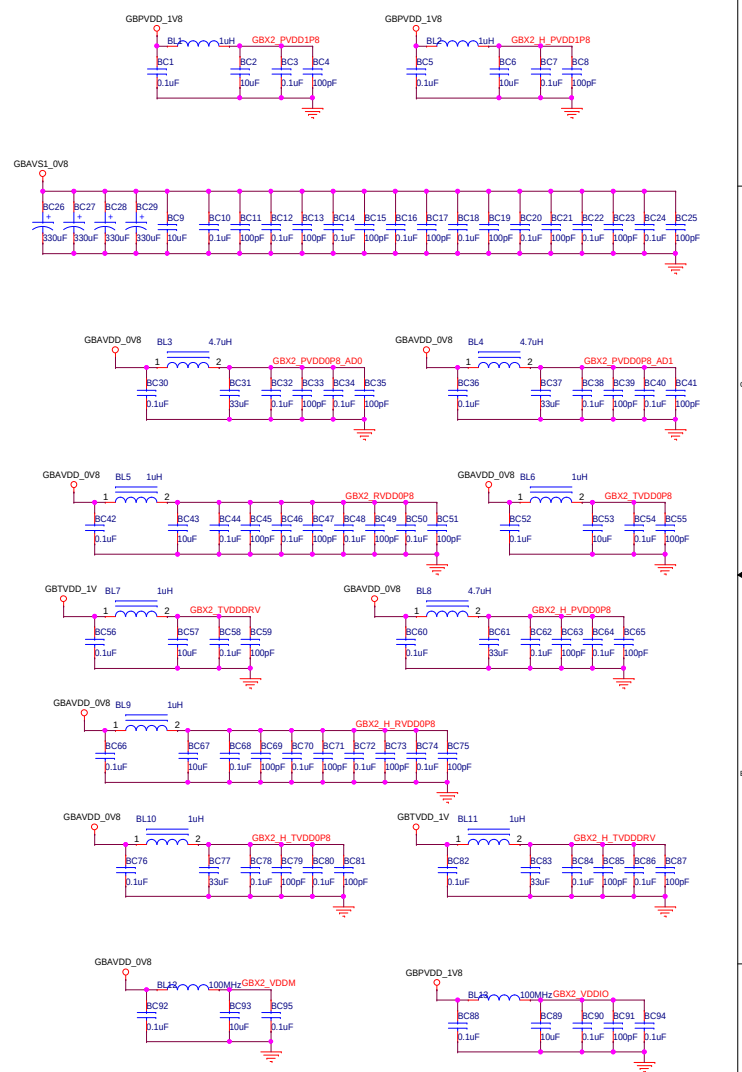
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Size	Page Name	P028_GBXX1_SerDes & MISC	Engineer	Chungyu Kuo
C				
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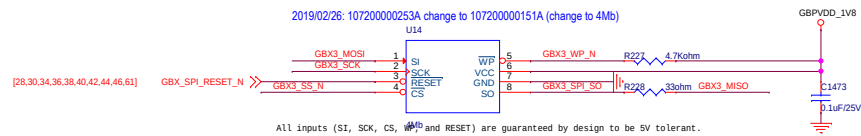
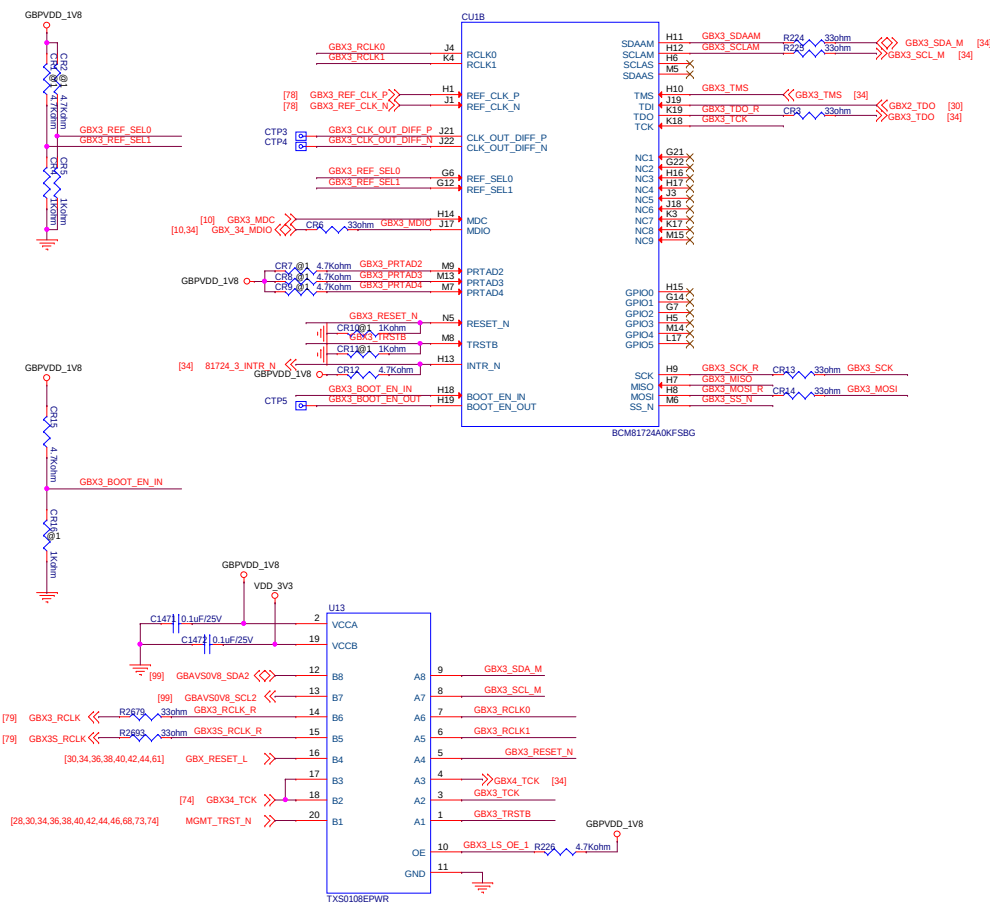
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FMA-HW01008-01 R01



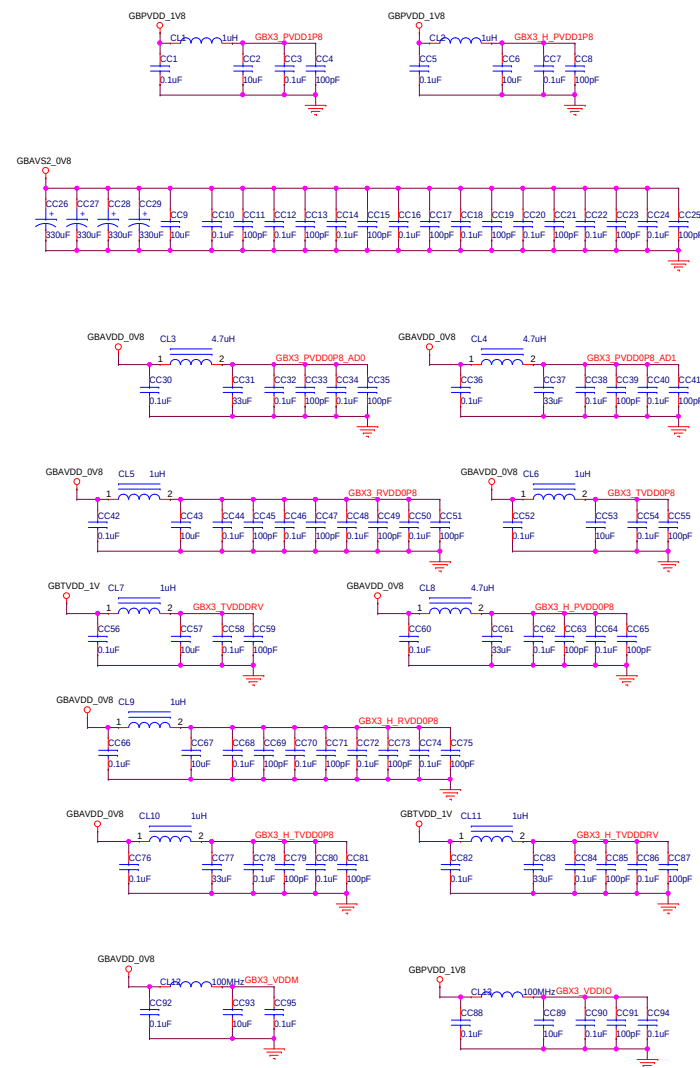
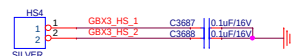


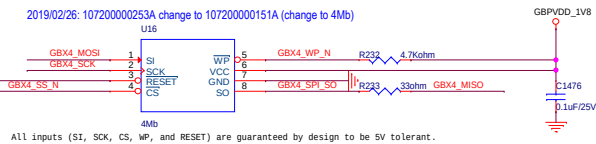
All inputs (SI, SCK, CS, WP, and RESET) are guaranteed by design to be 5V tolerant.

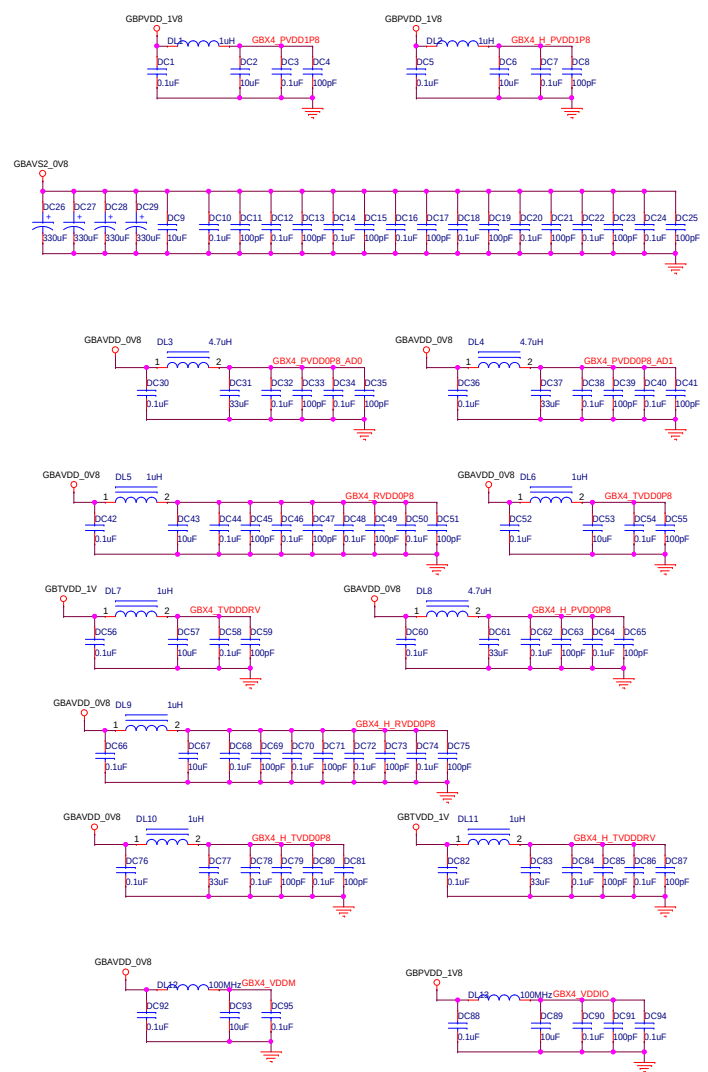




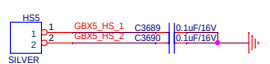
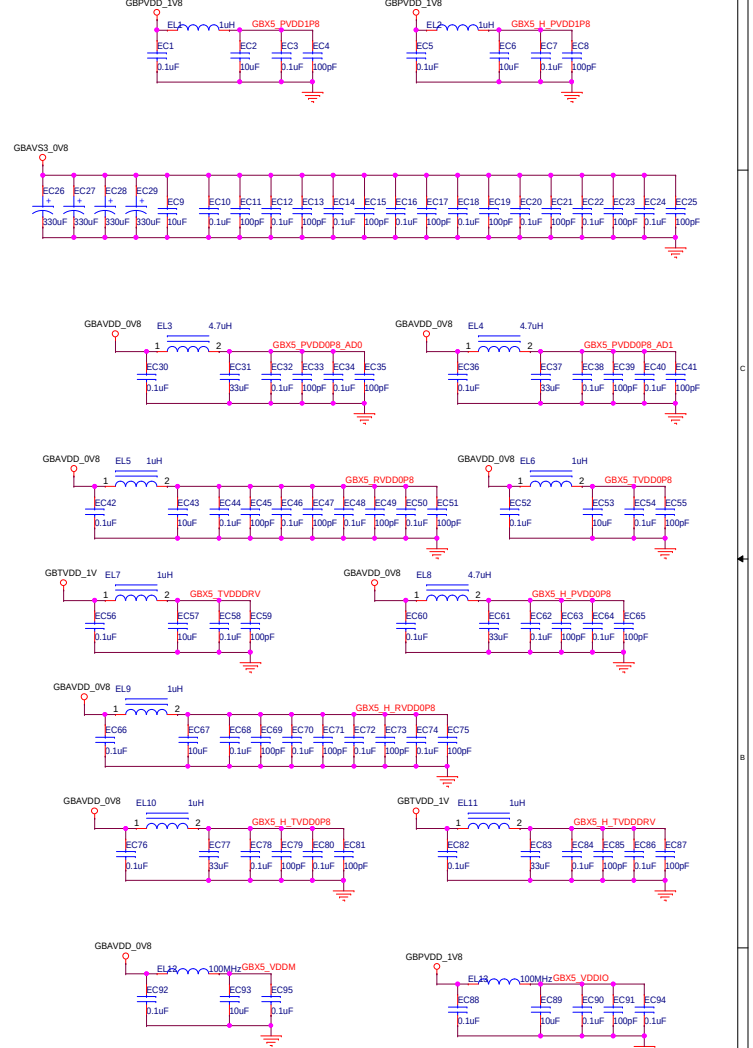
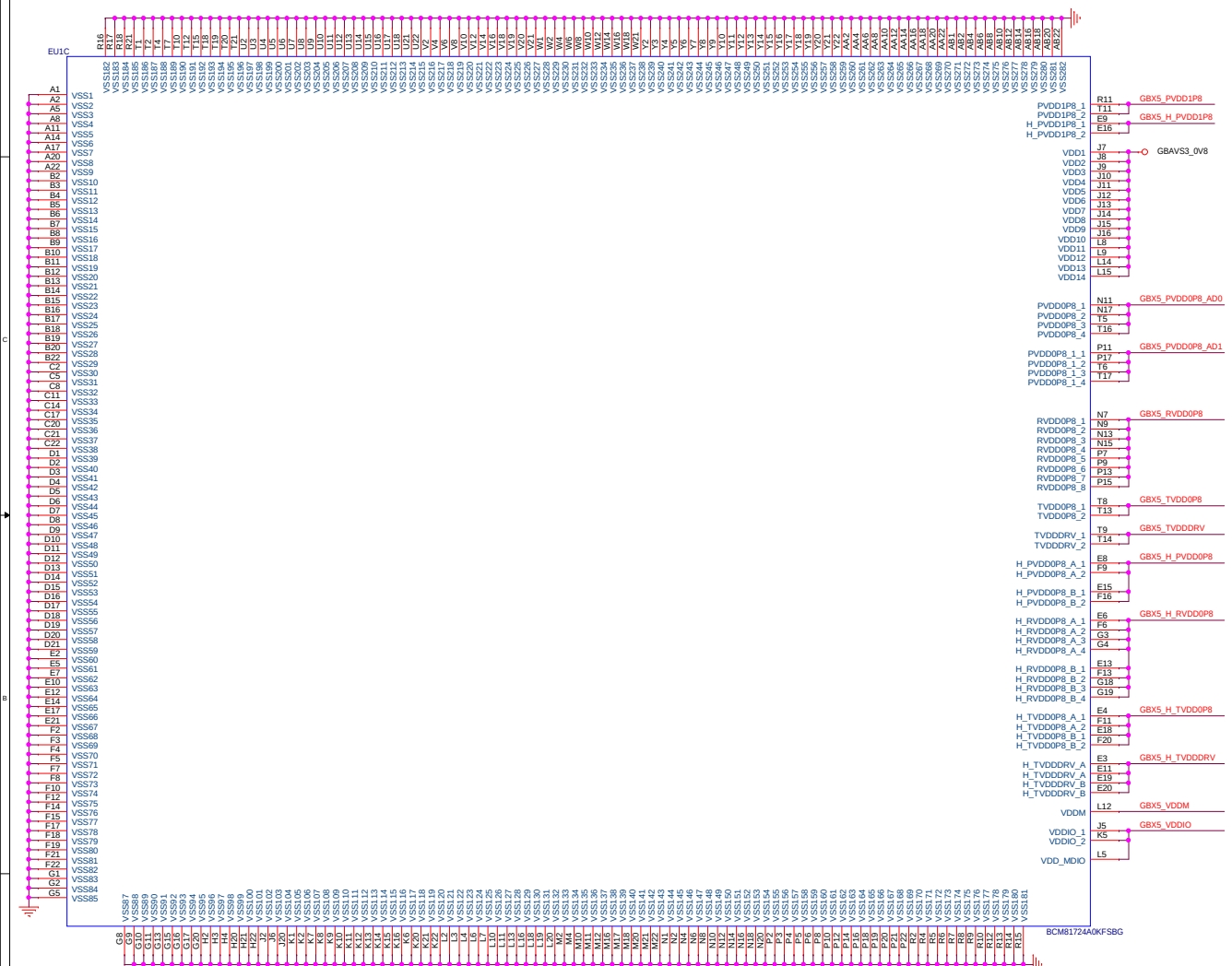
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FMA-HW01008-01 R01

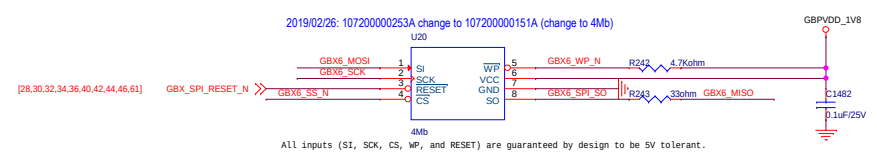


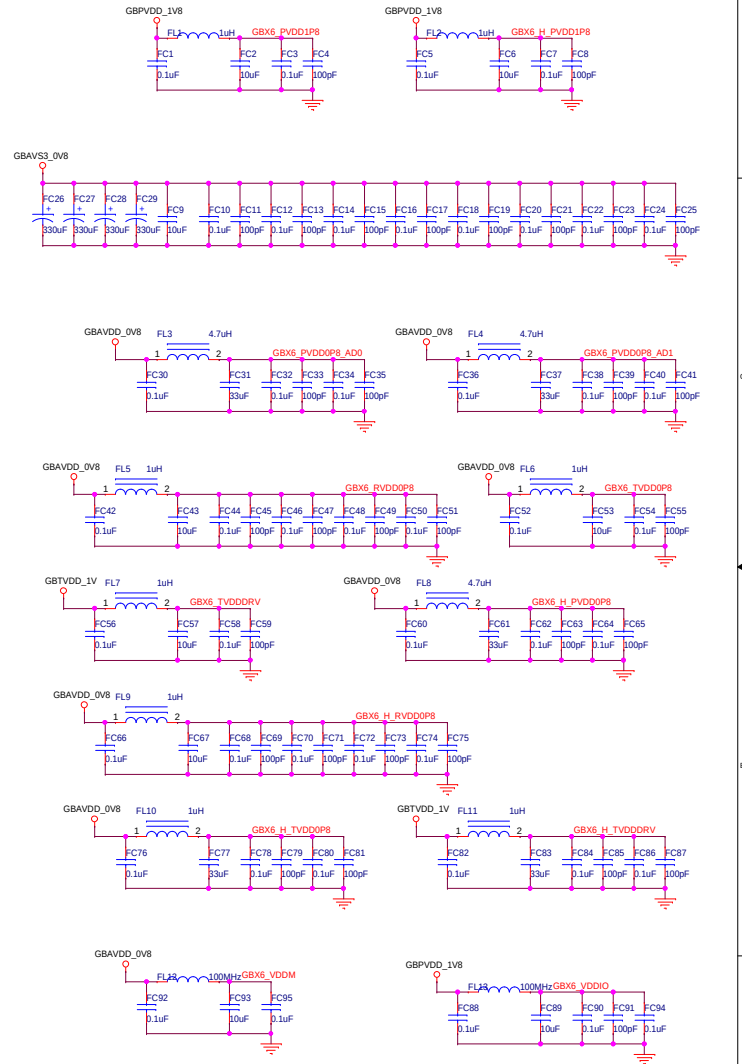


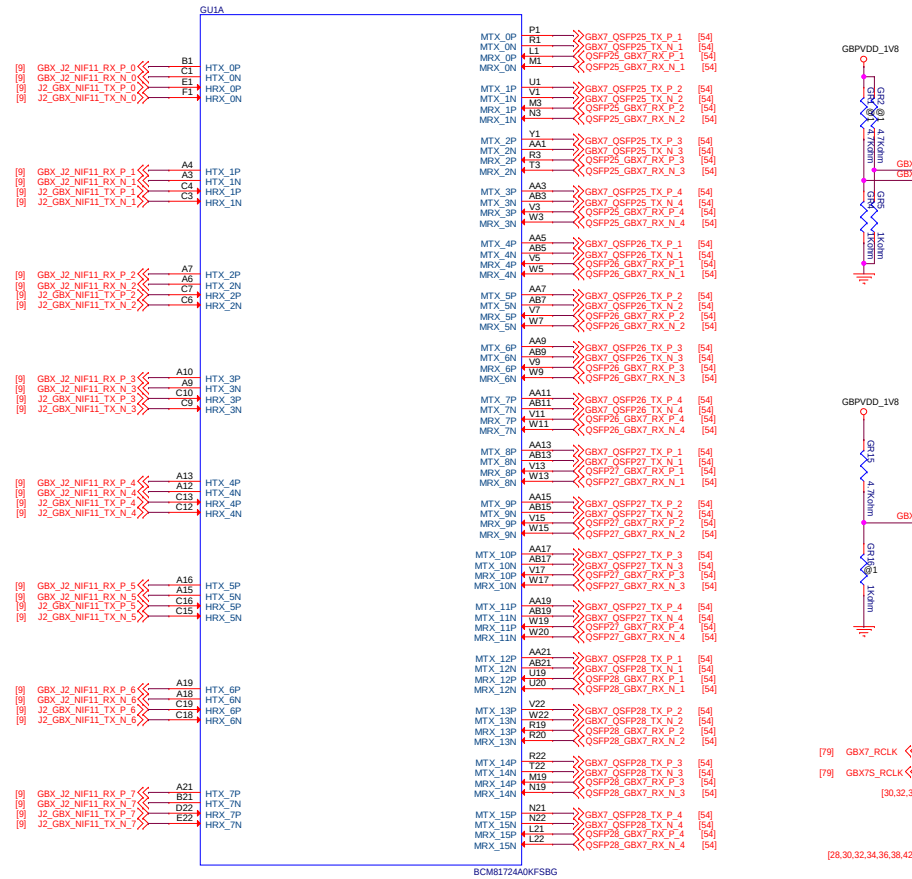




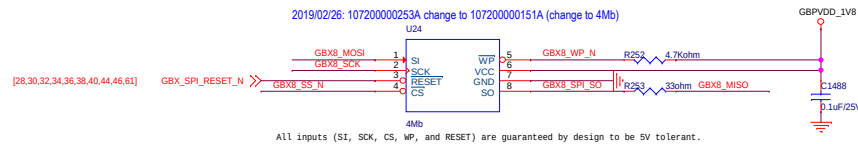
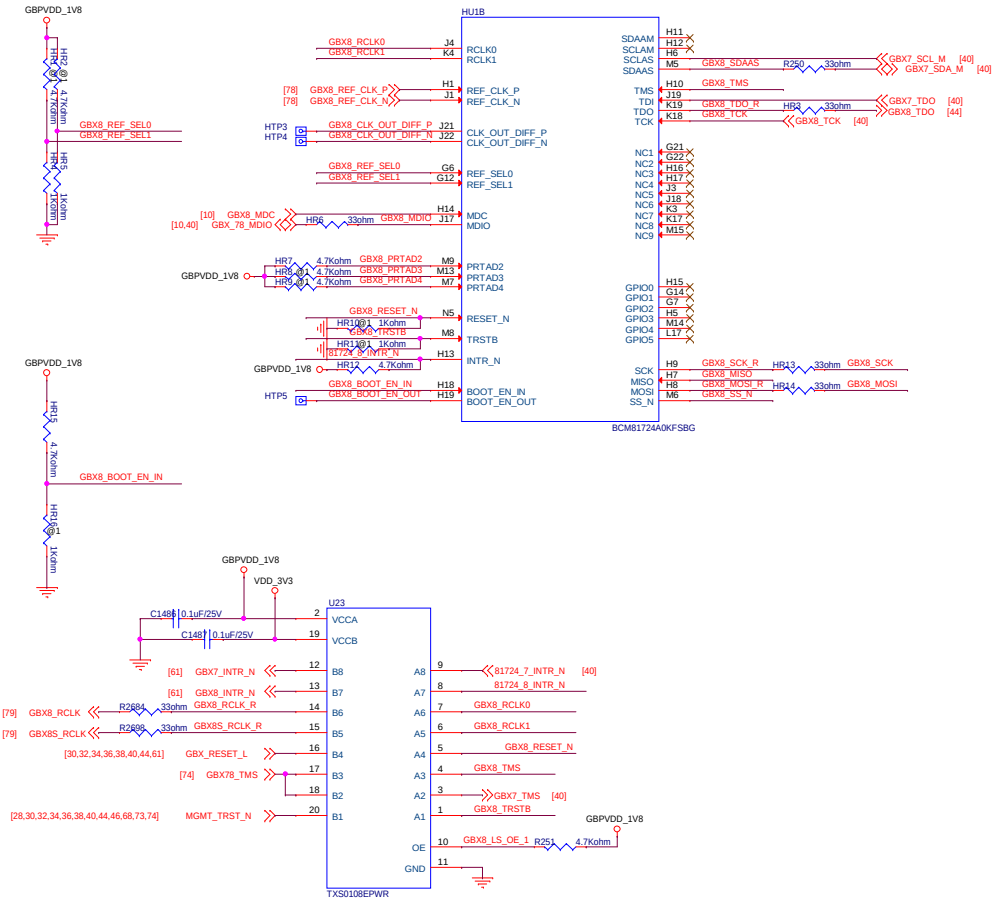
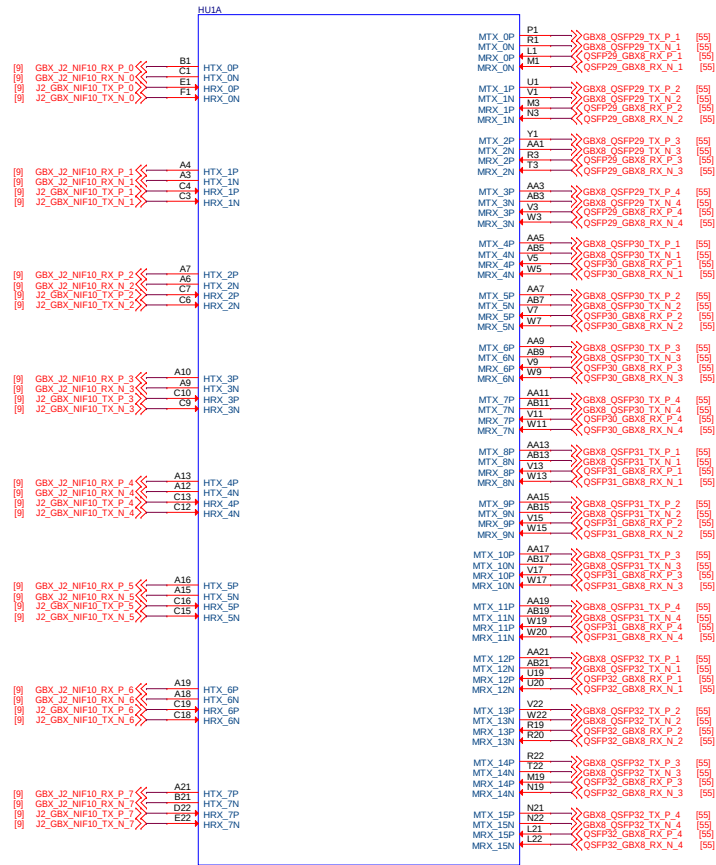










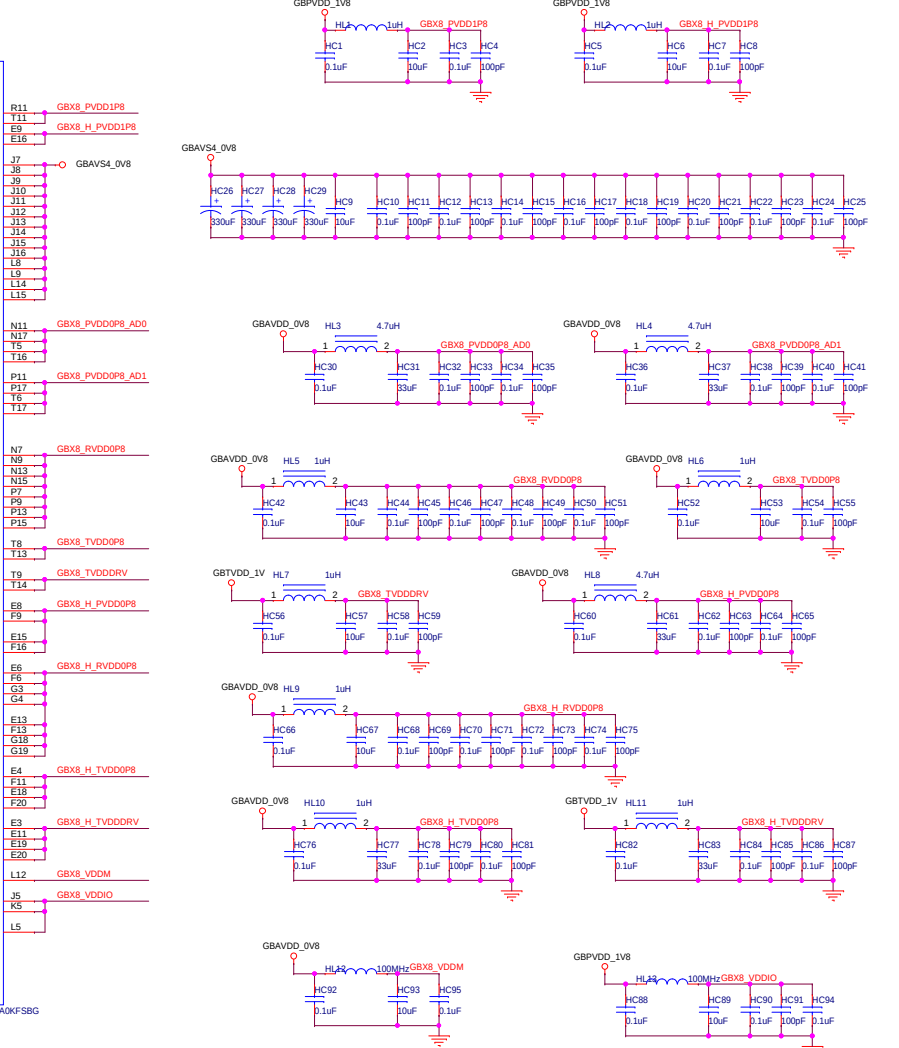
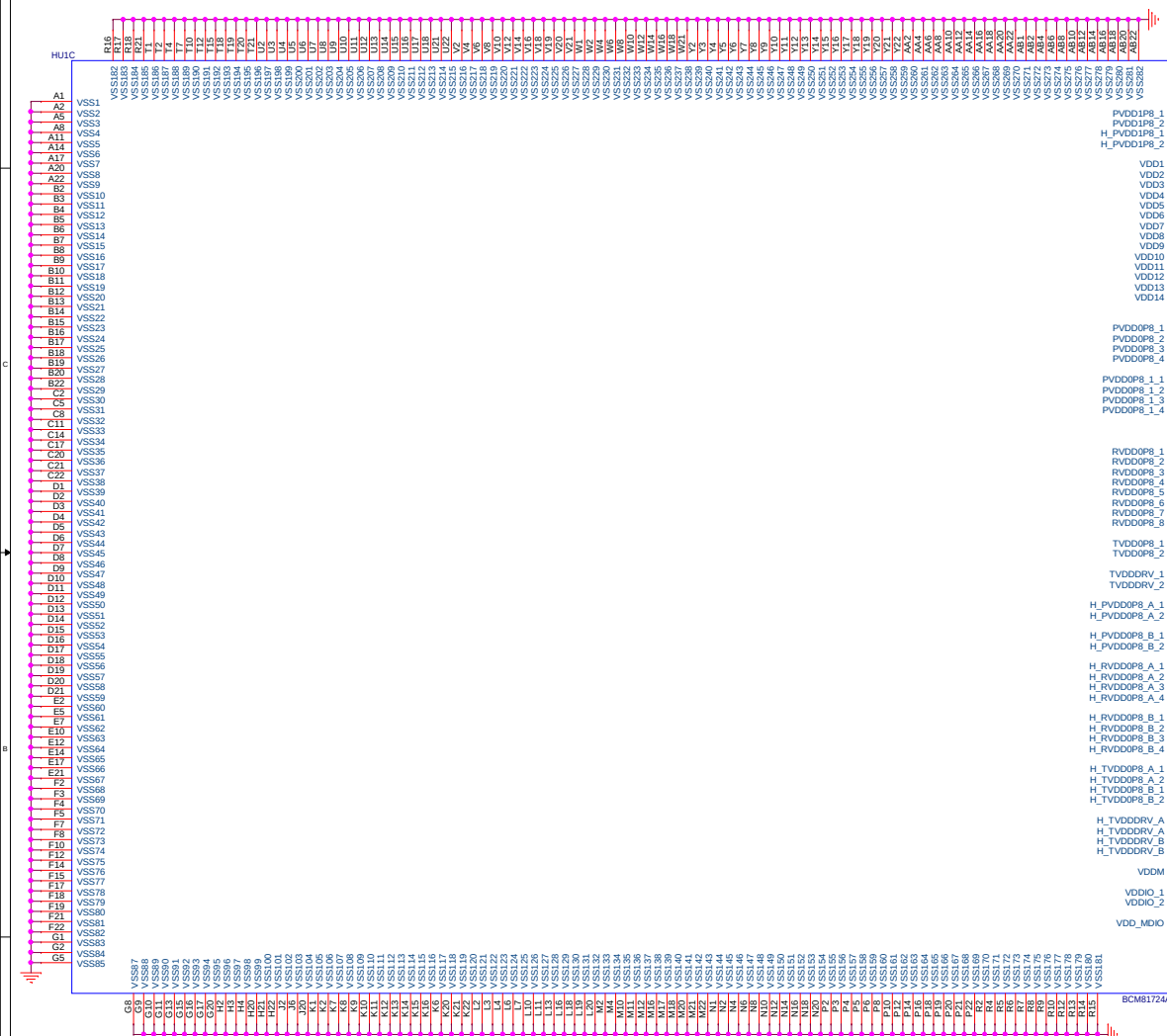


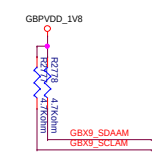
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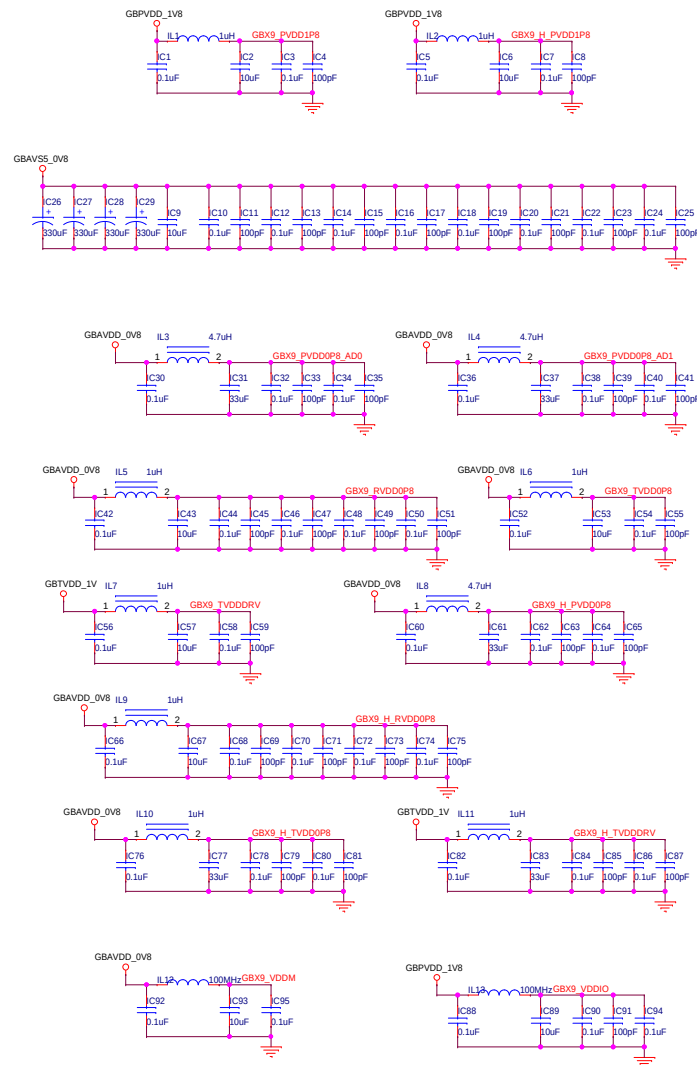
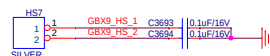
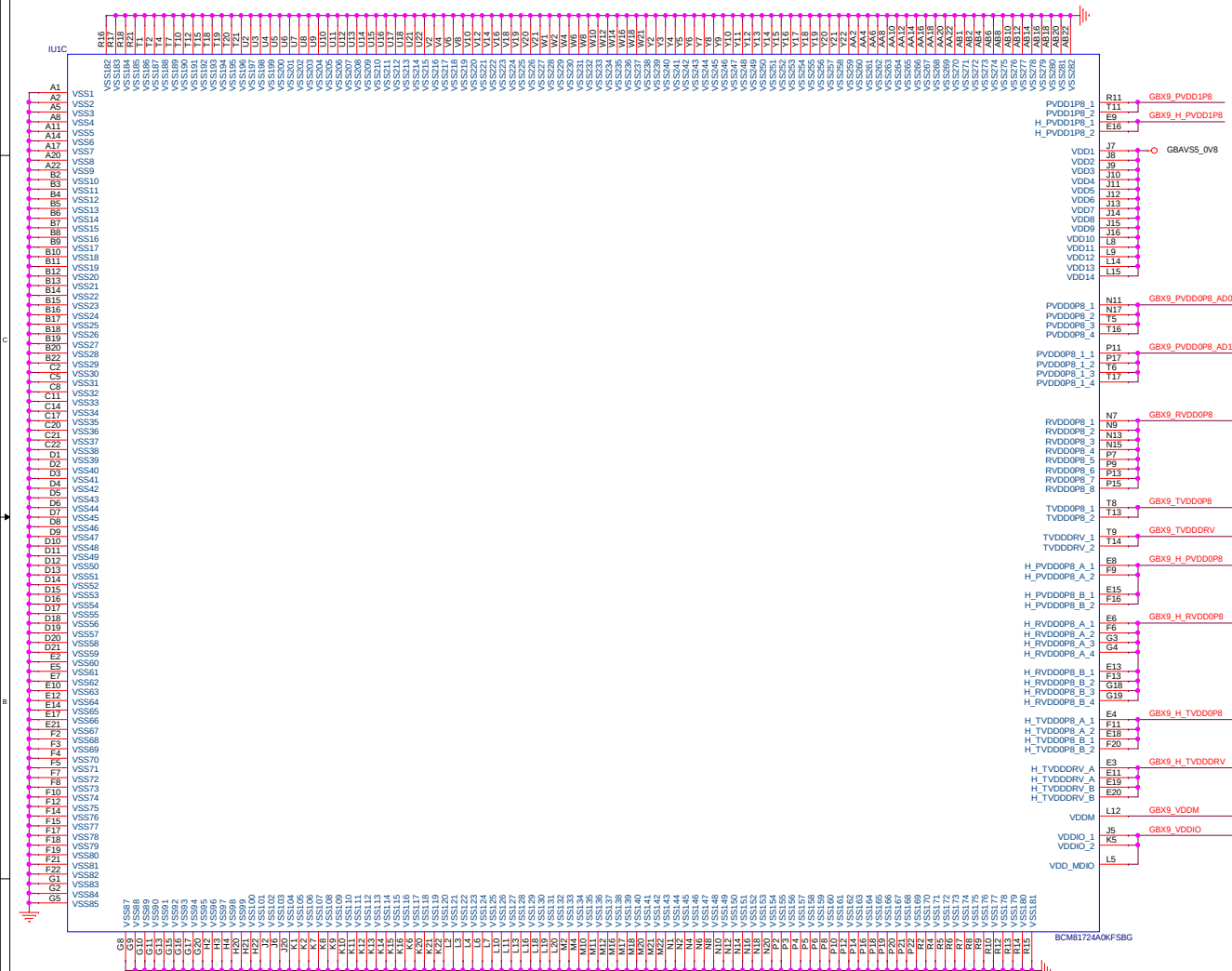
**Accton Inc.**
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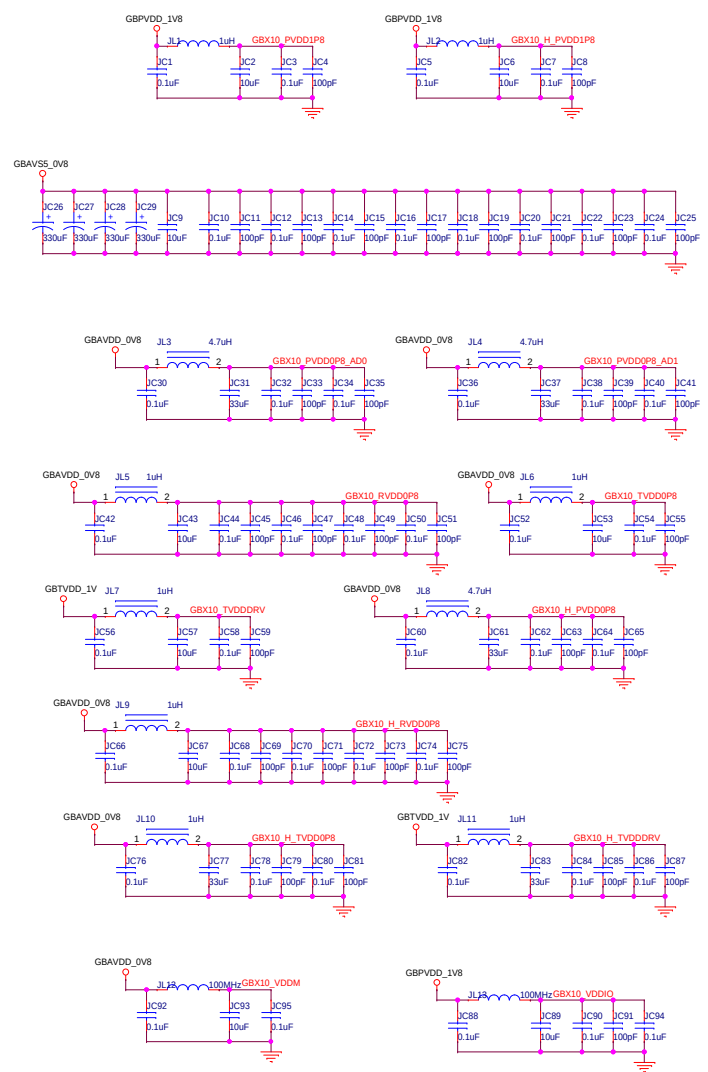
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Size	Page Name	P042_GPX8 SerDes & MISC		
C		Engineer		
		Chungyu Kuo		
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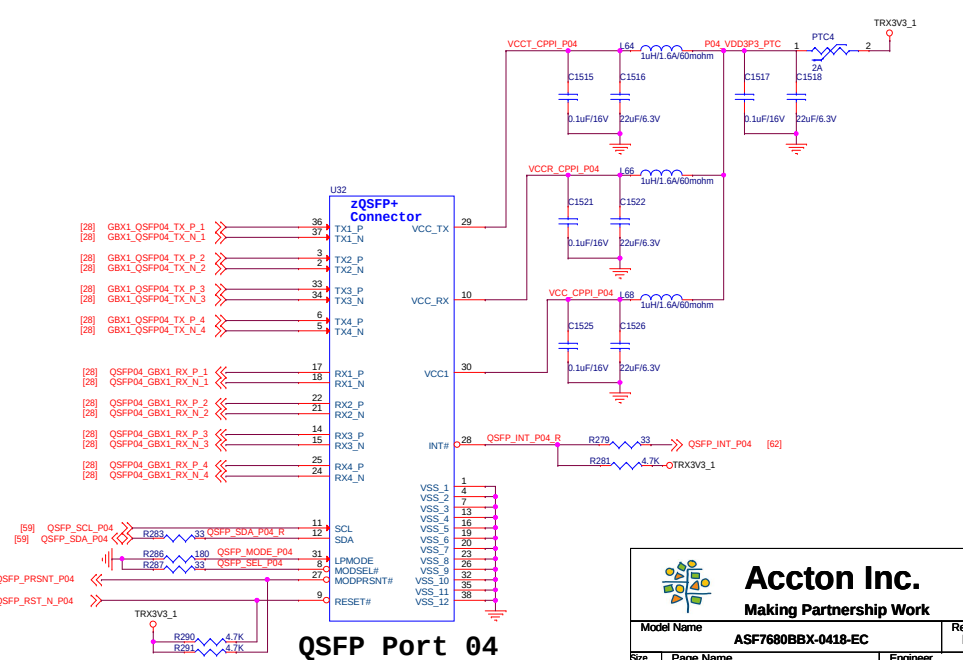
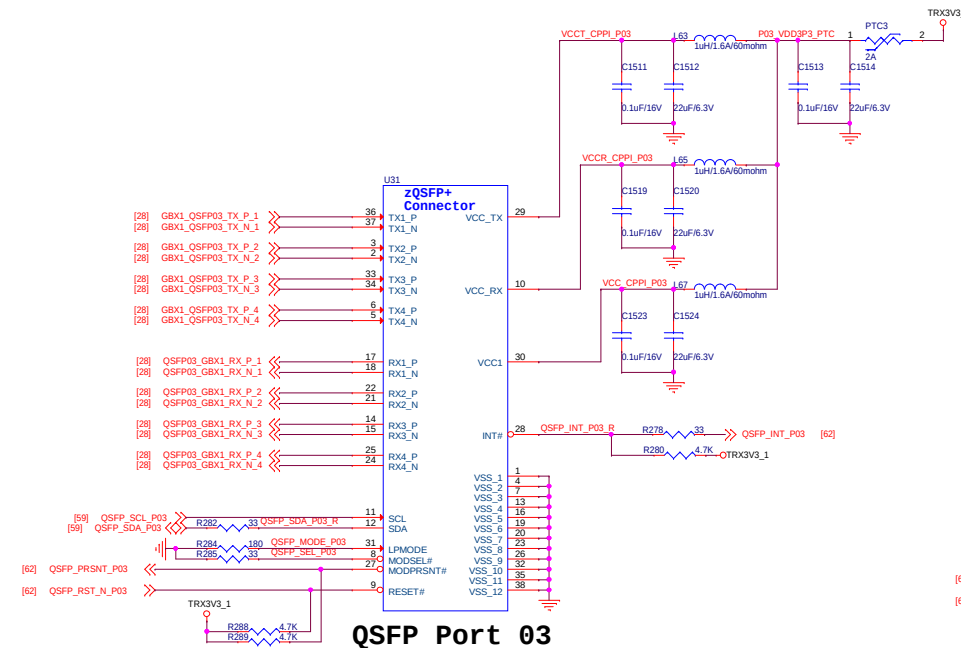
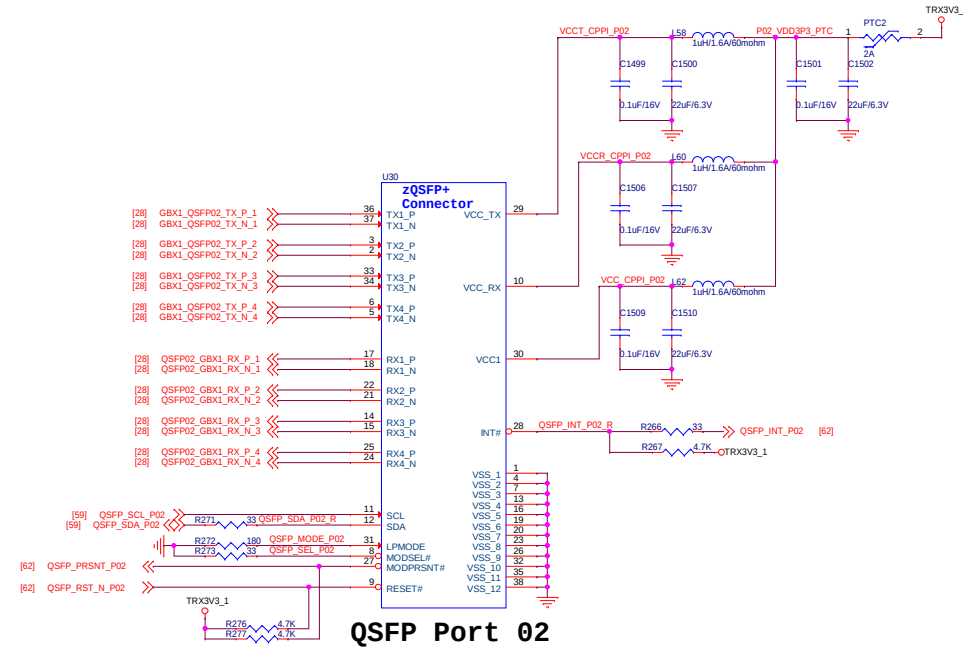
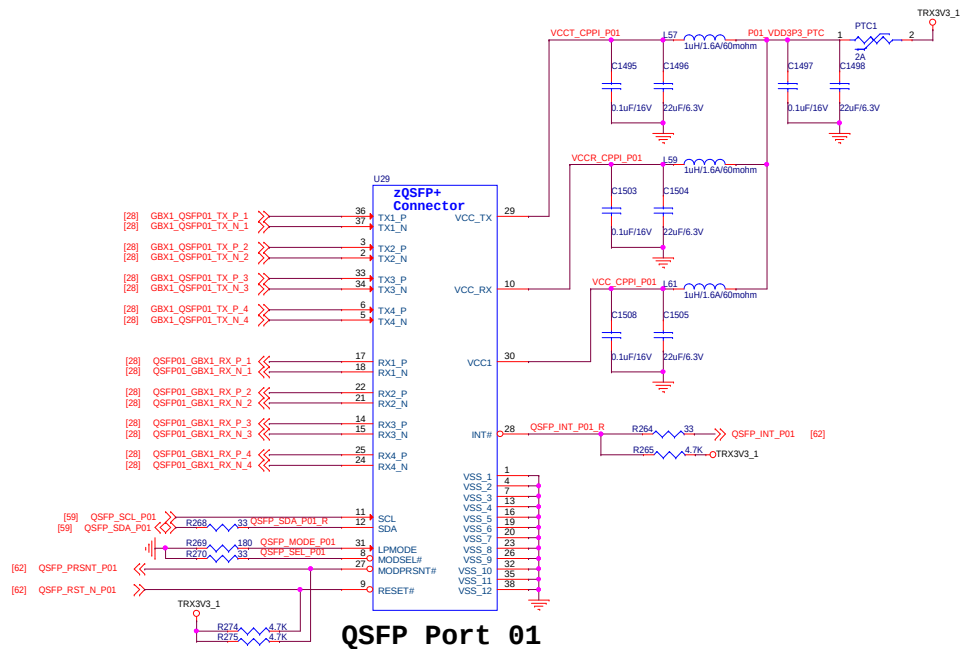


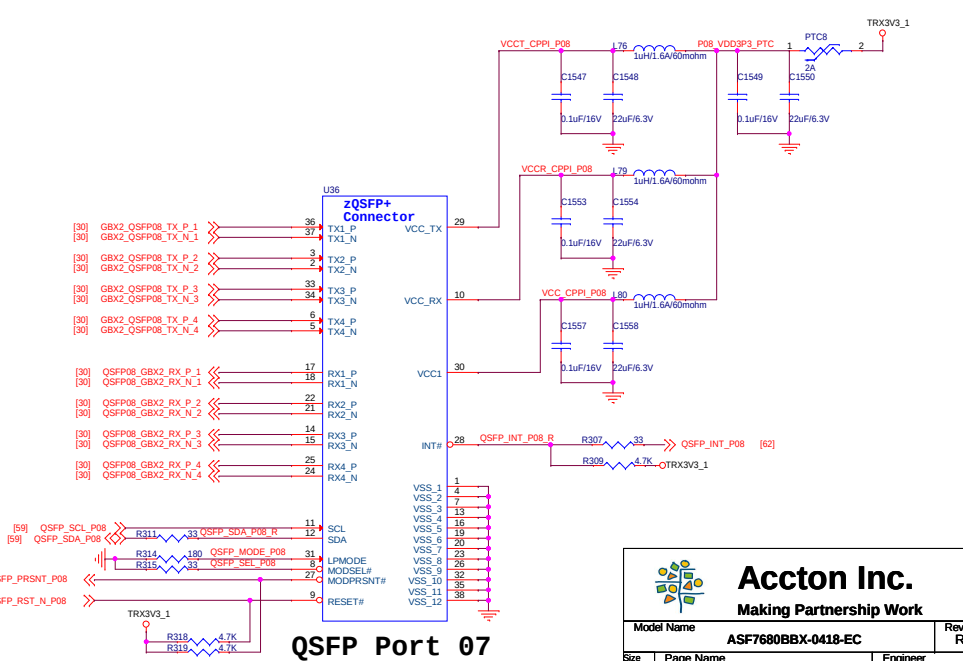
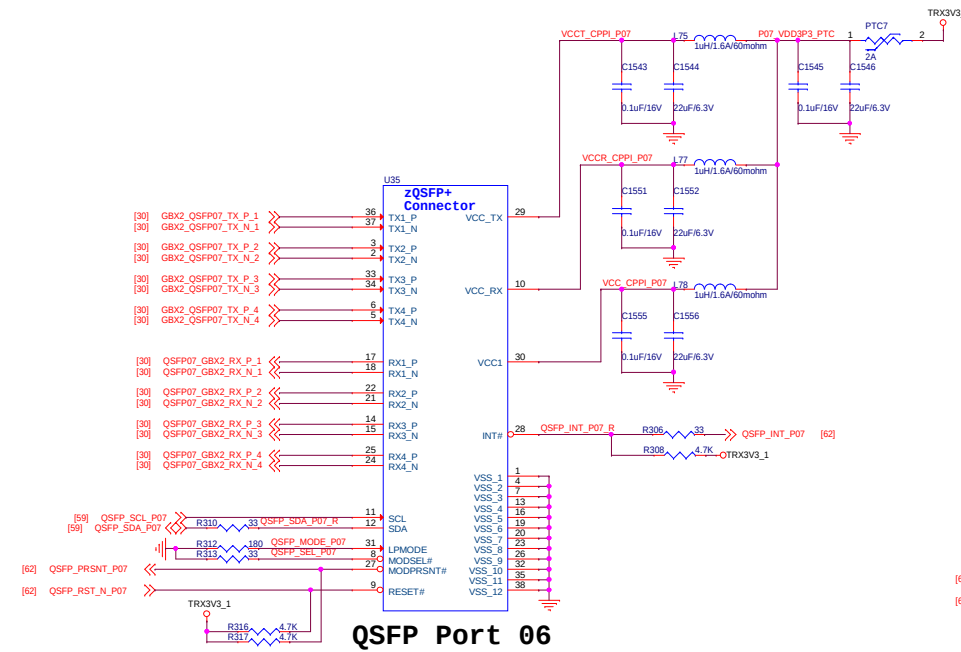
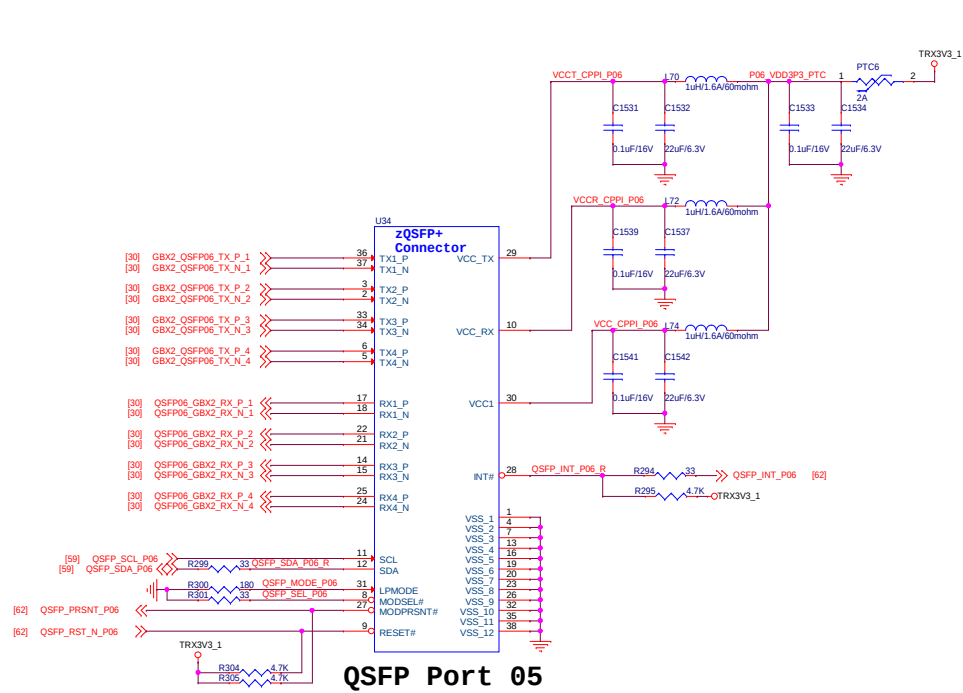
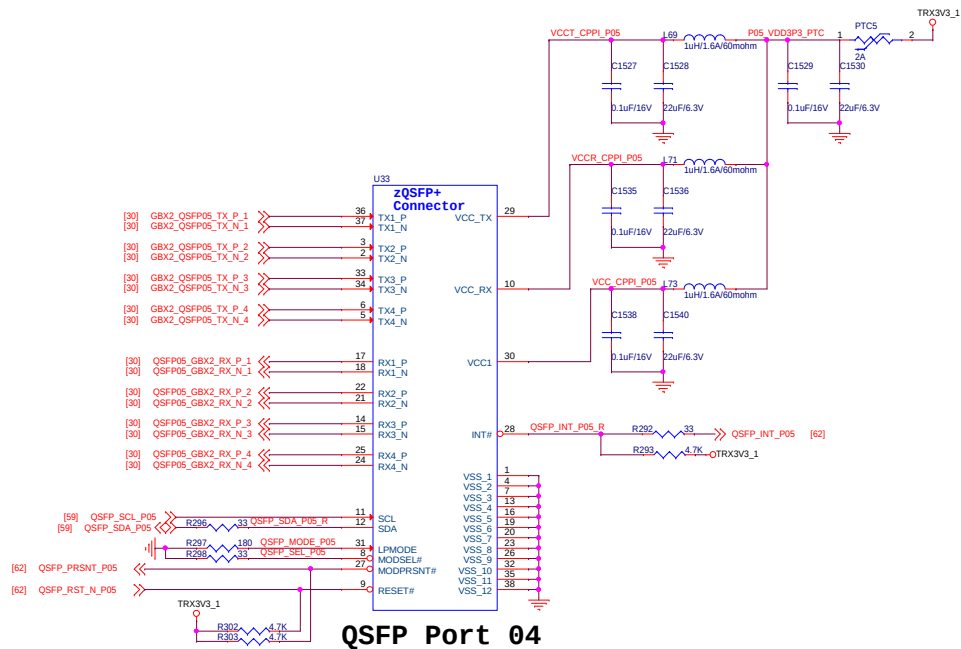


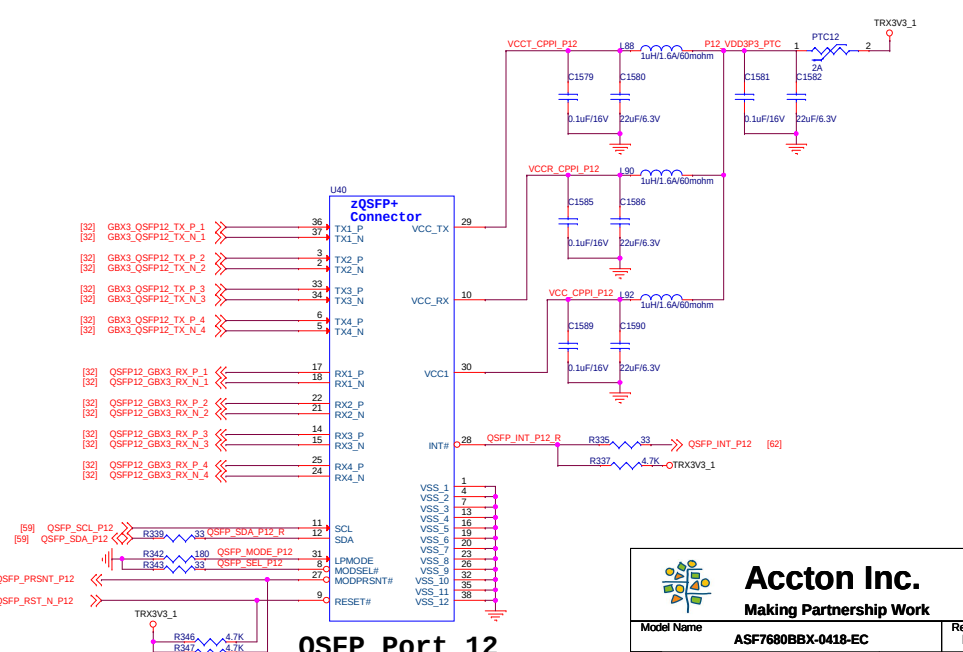
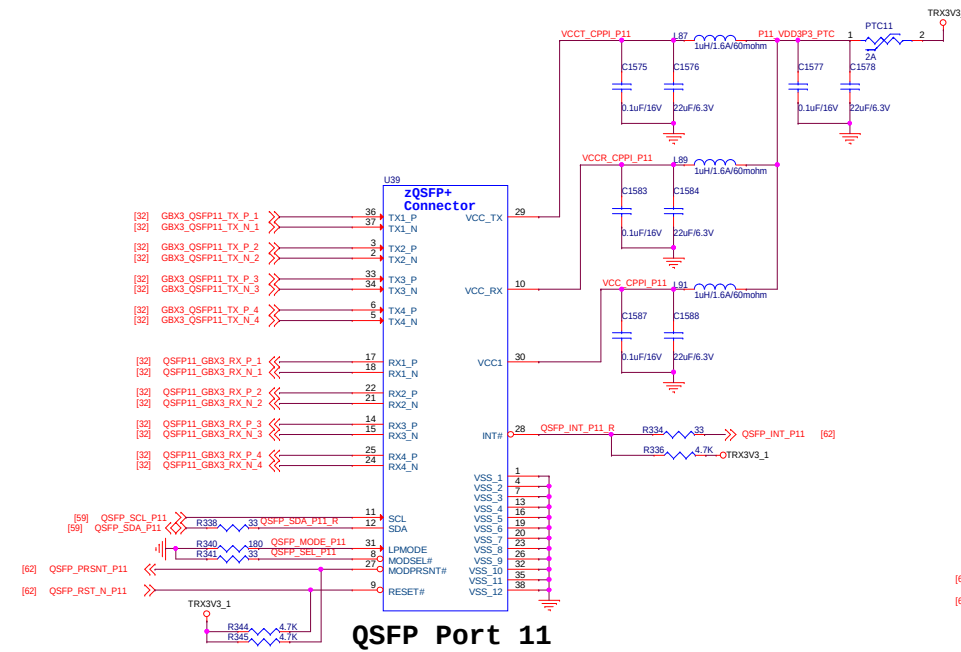
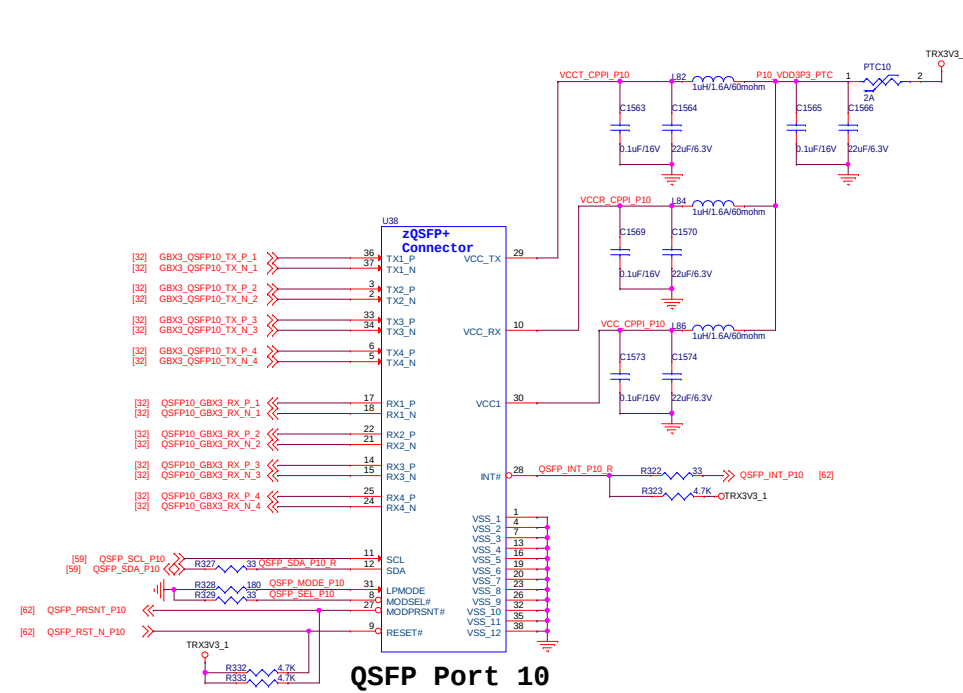
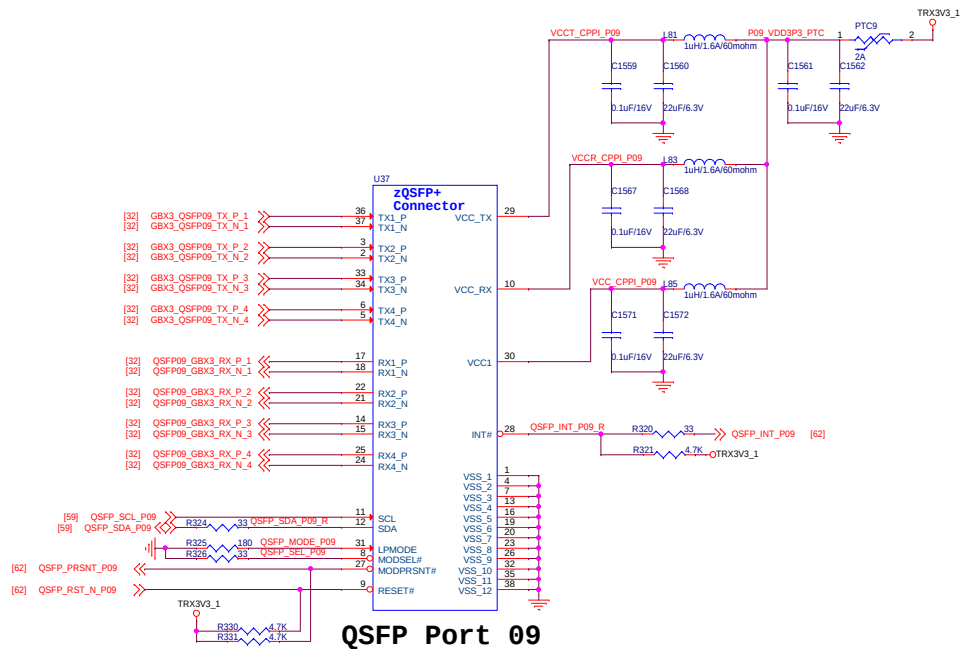
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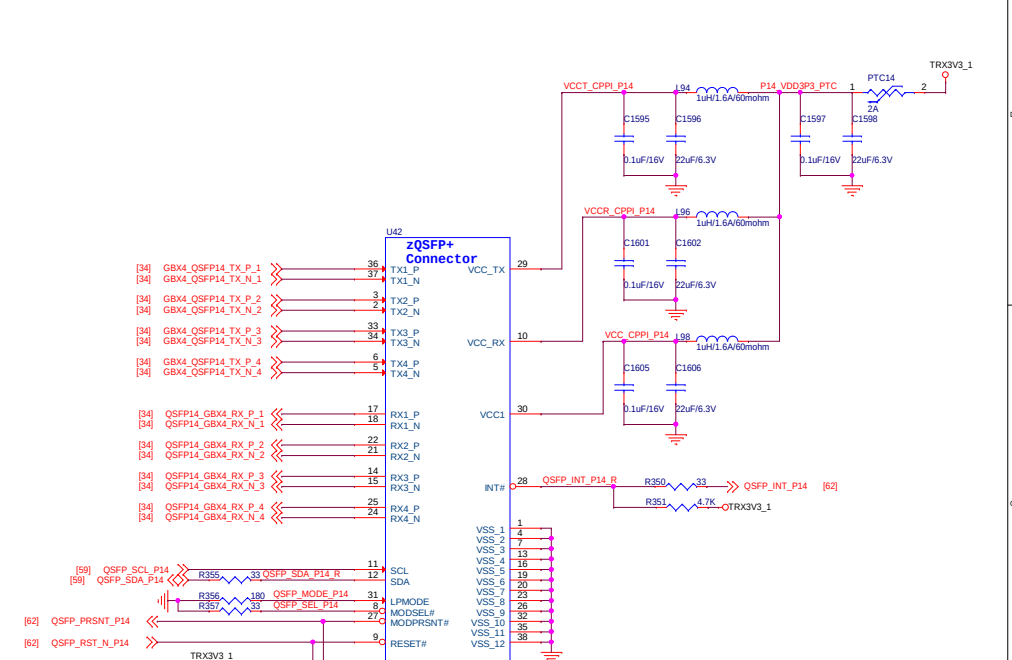




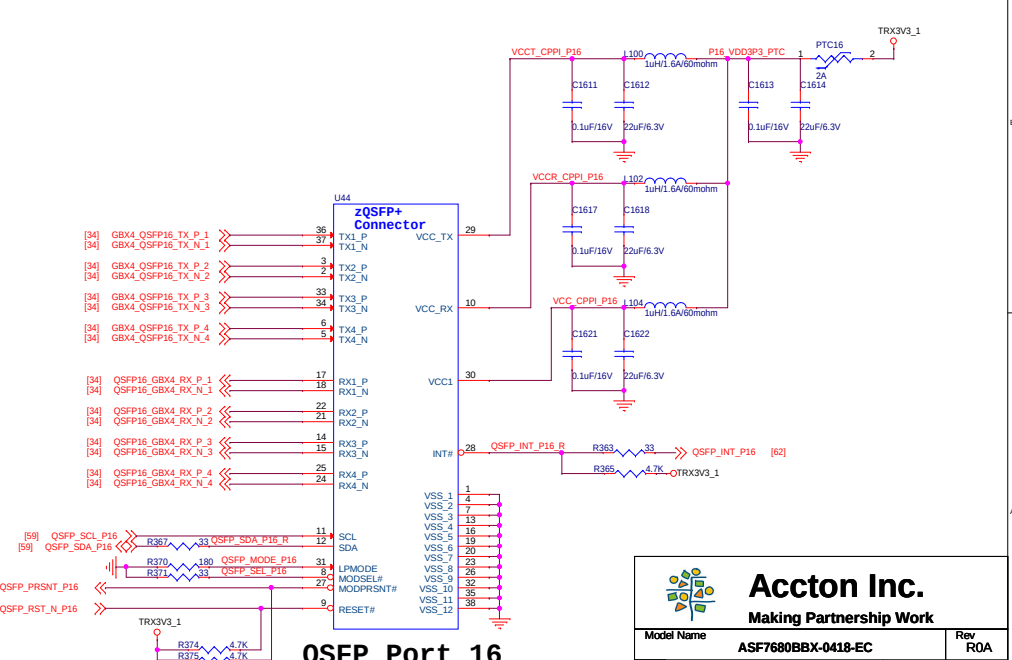




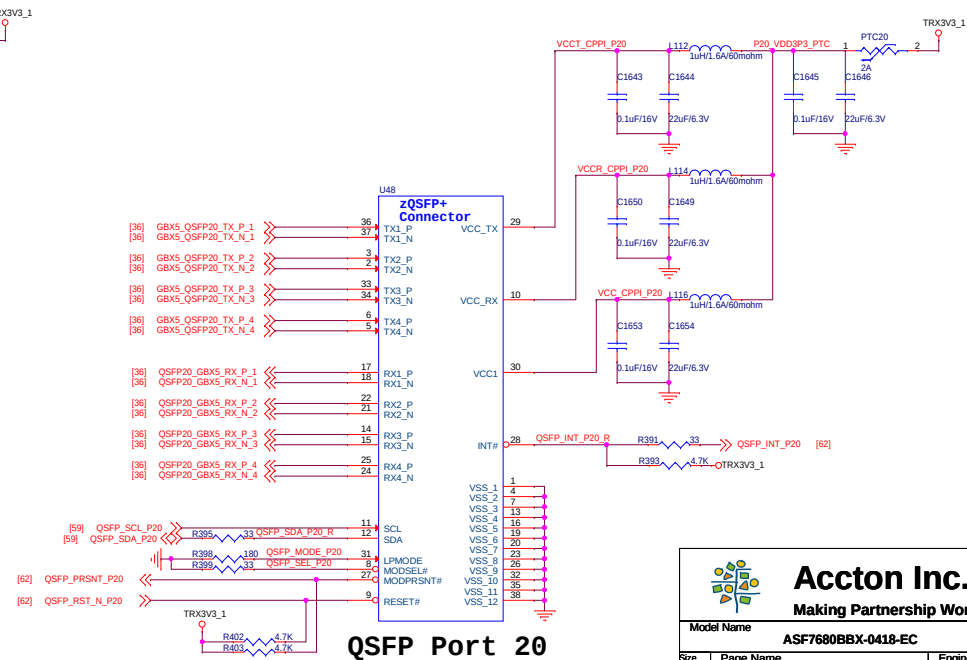
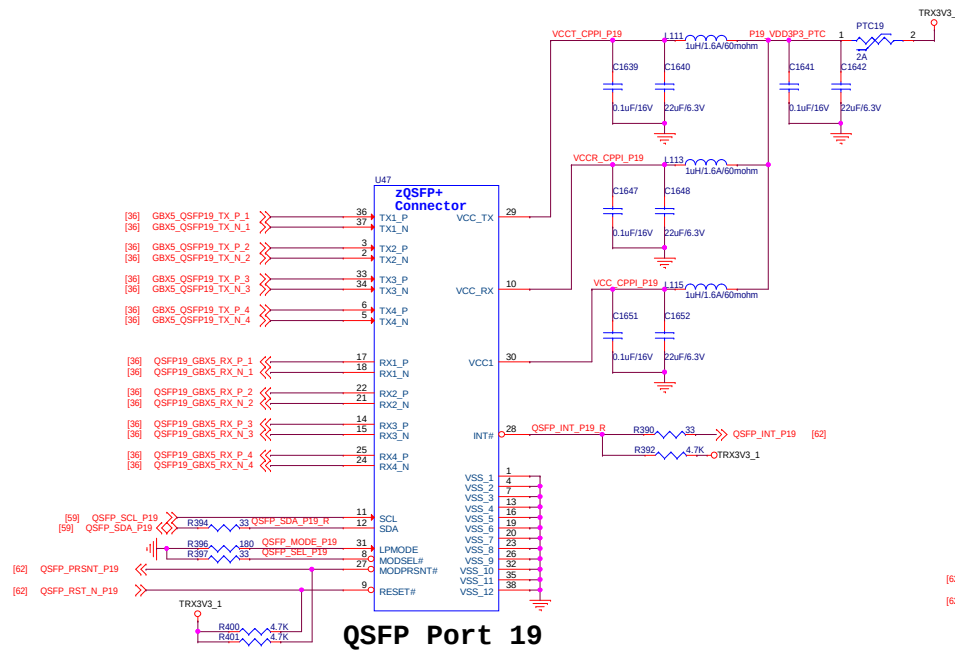
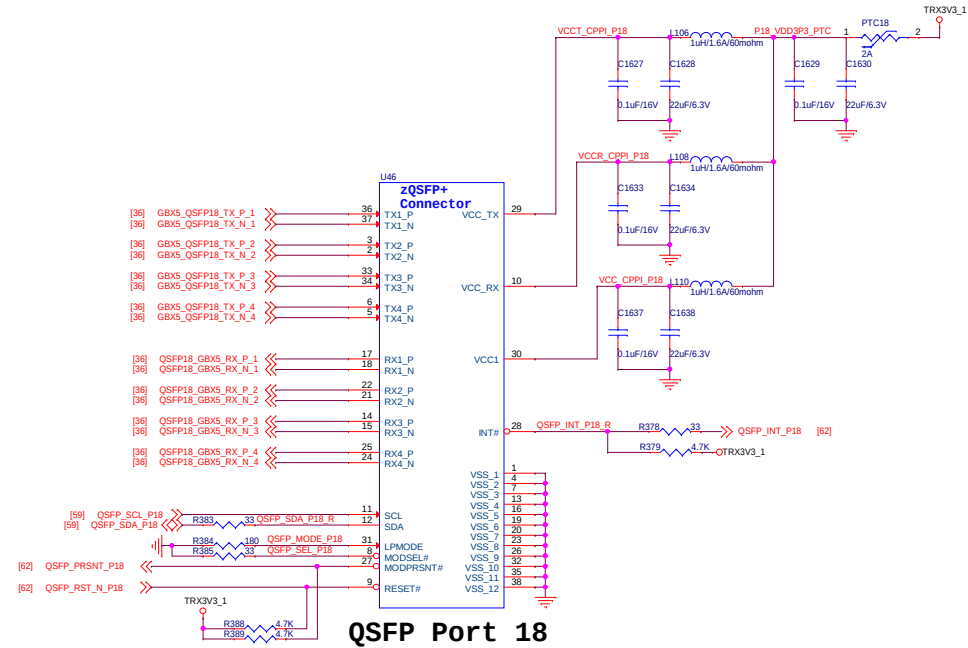
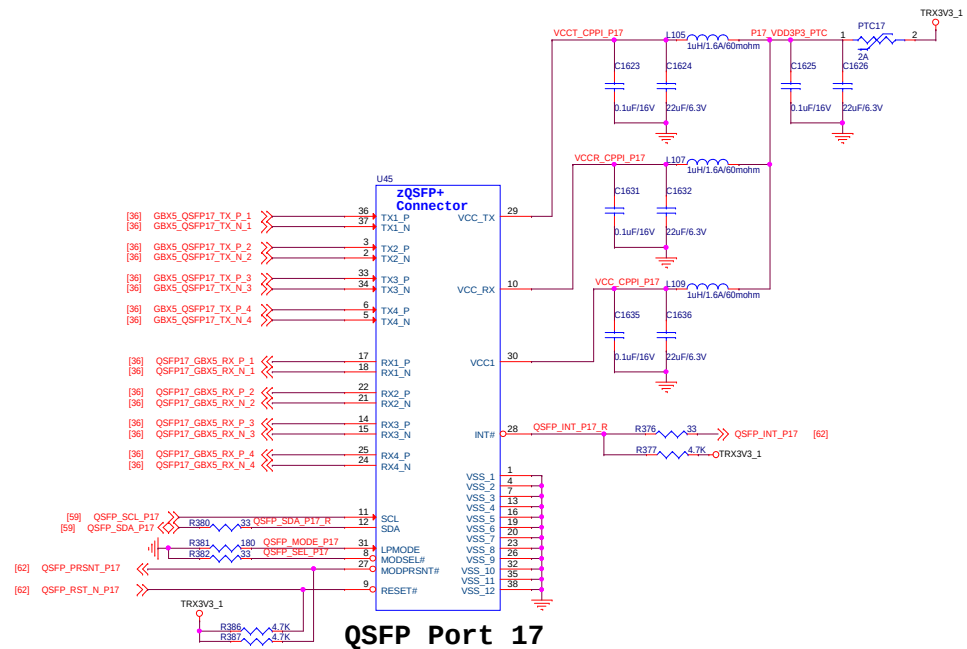




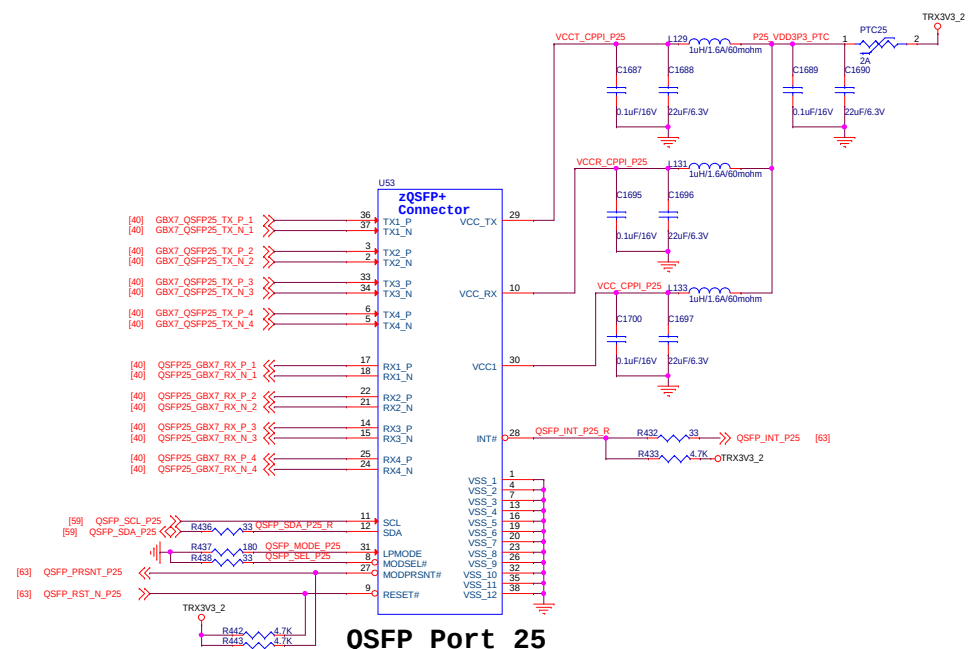
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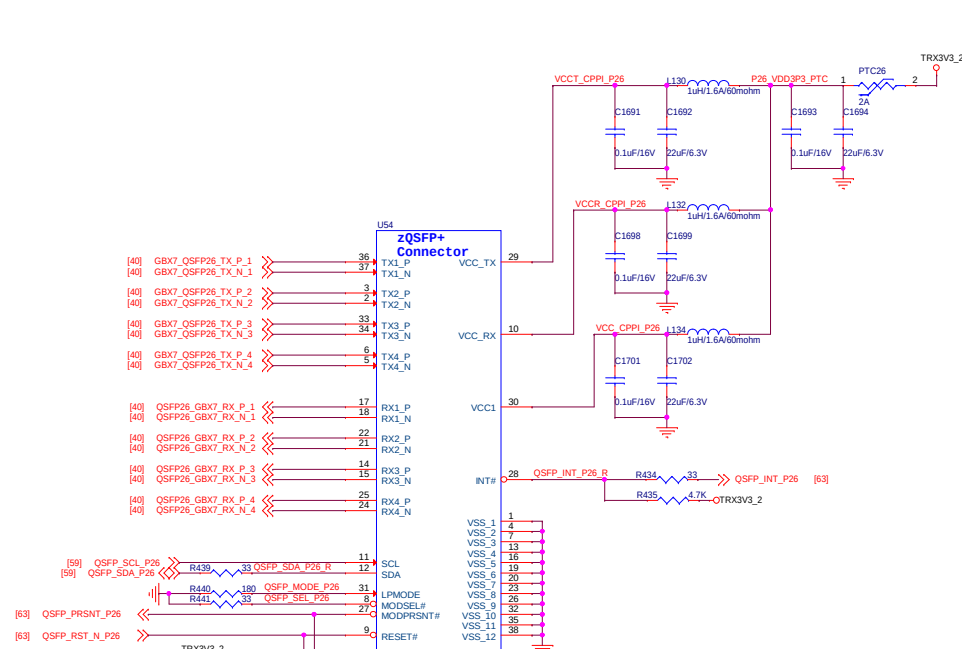
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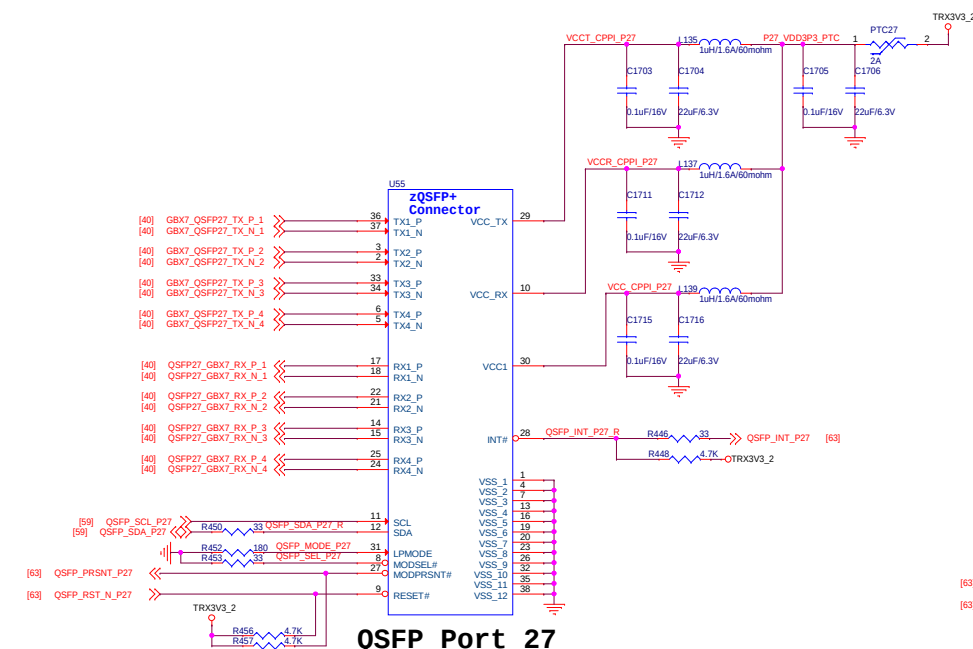




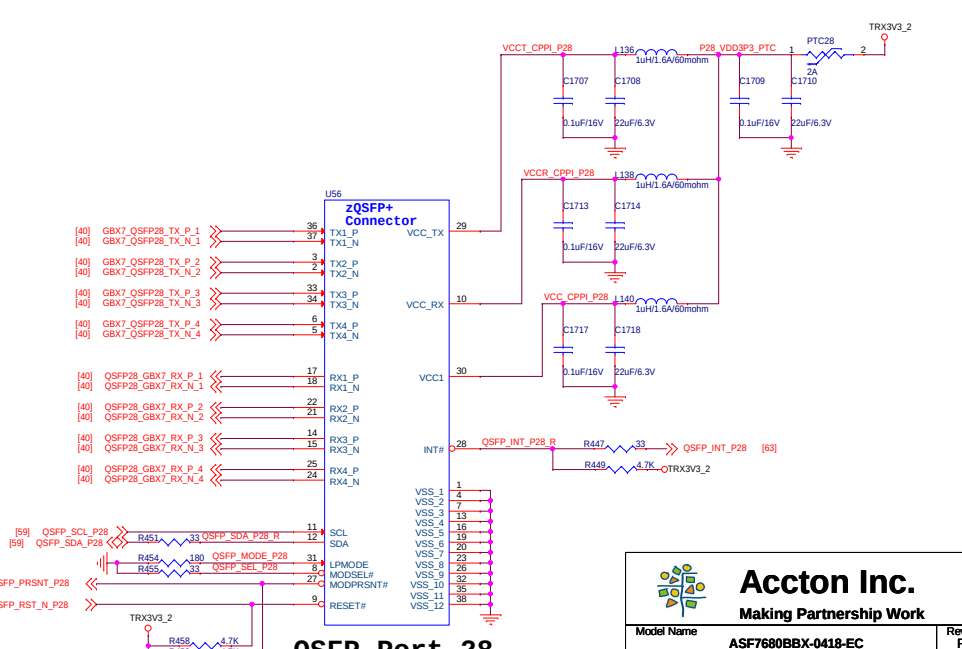
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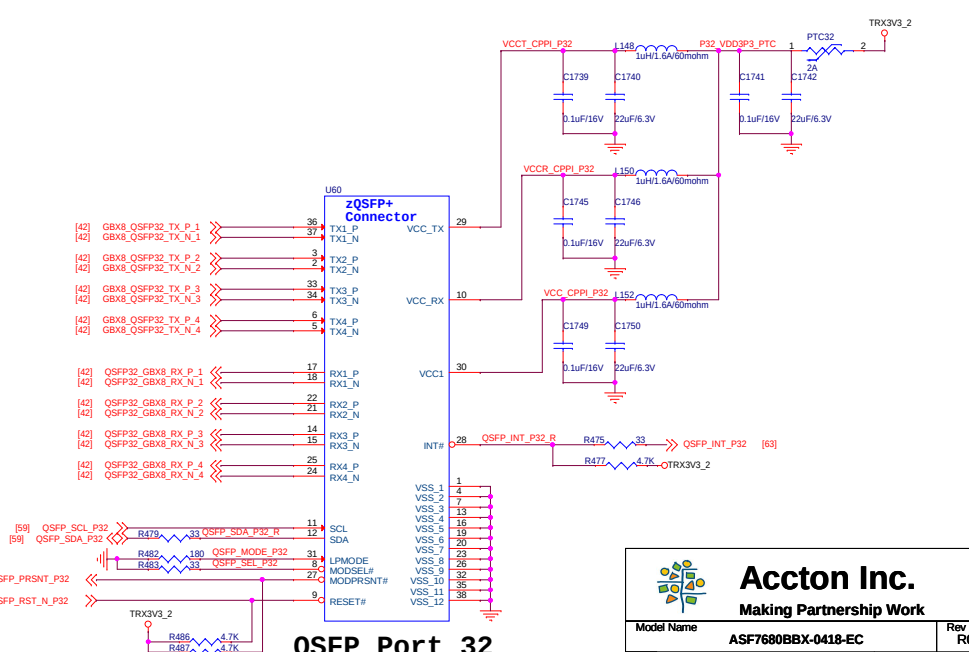
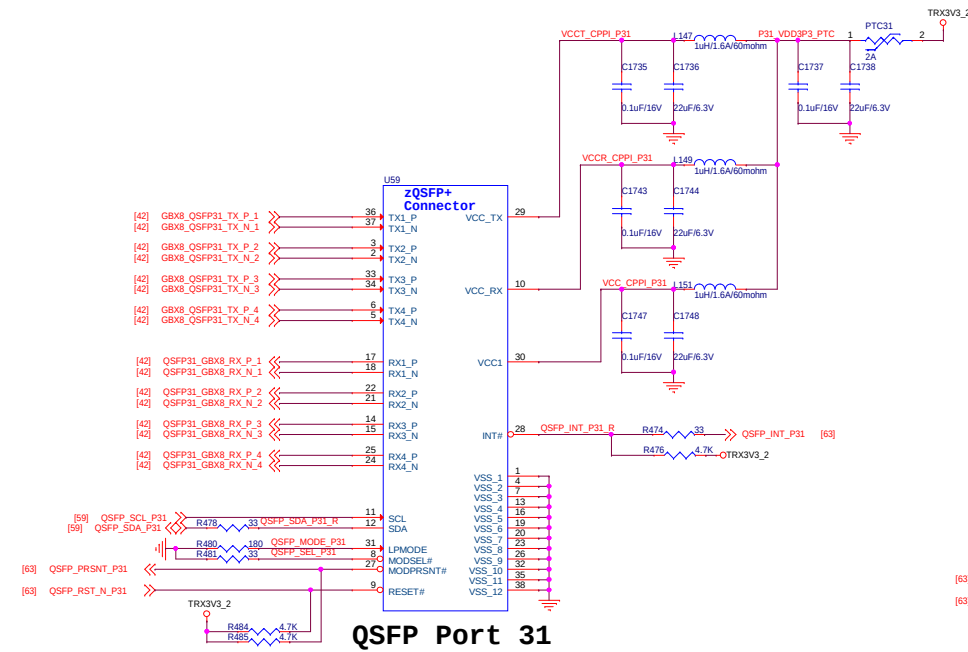
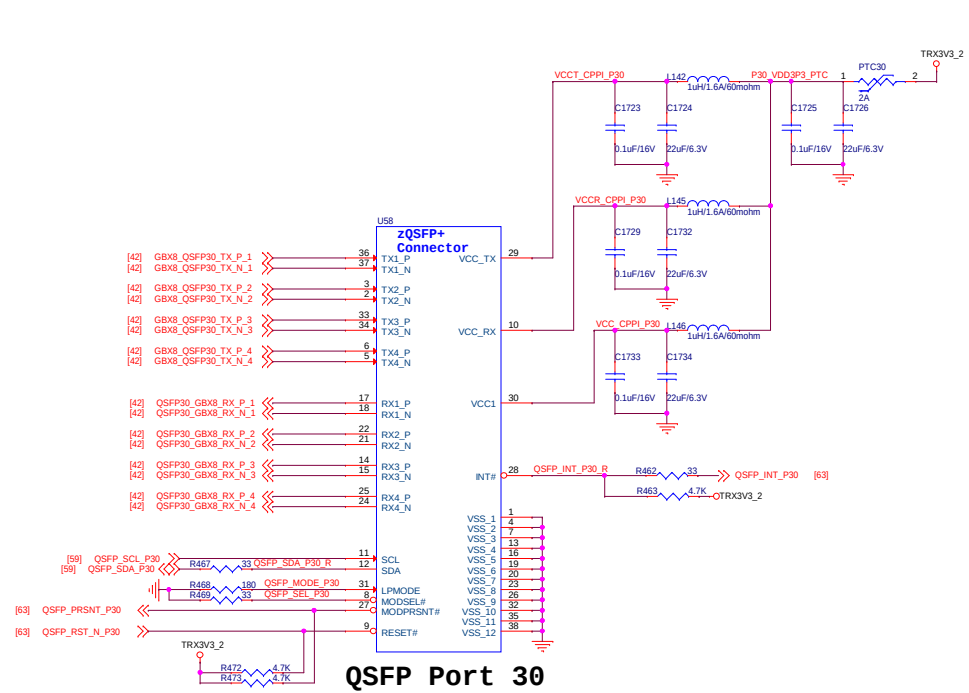
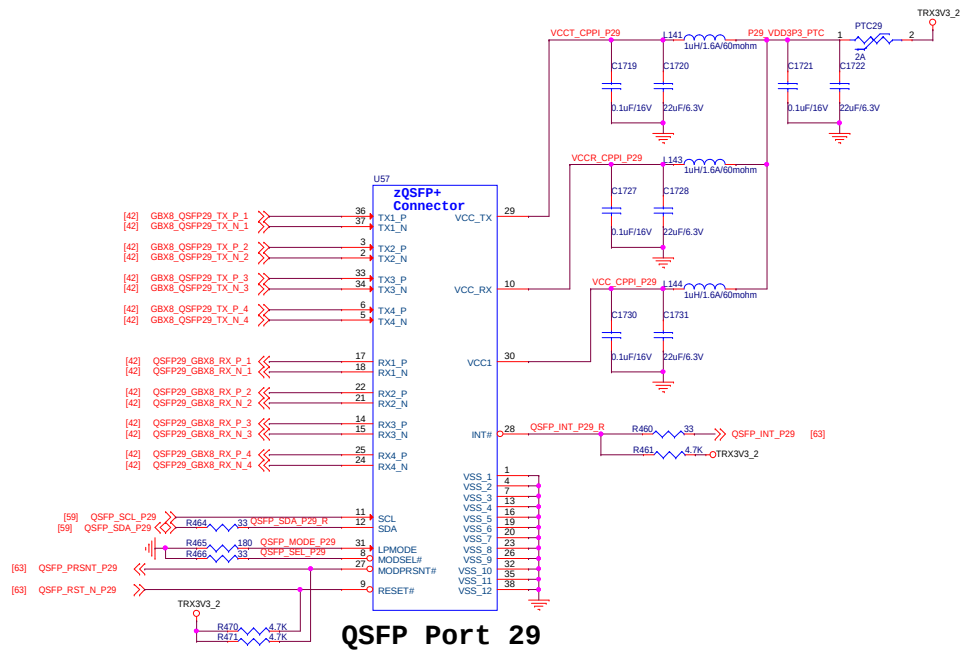
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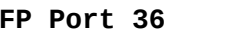


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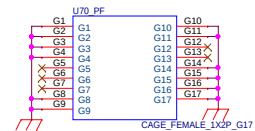
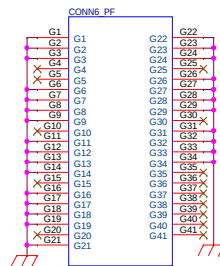
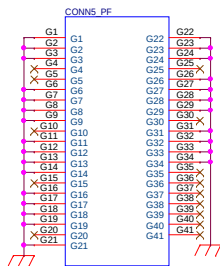
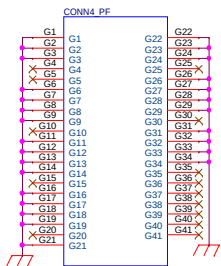
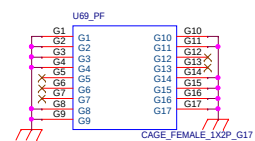
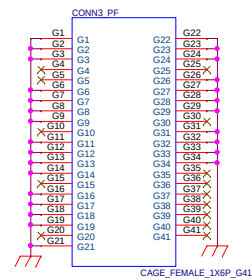
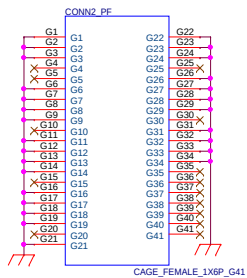
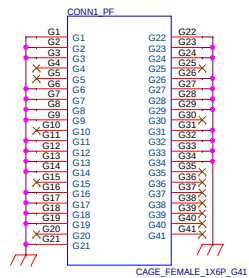


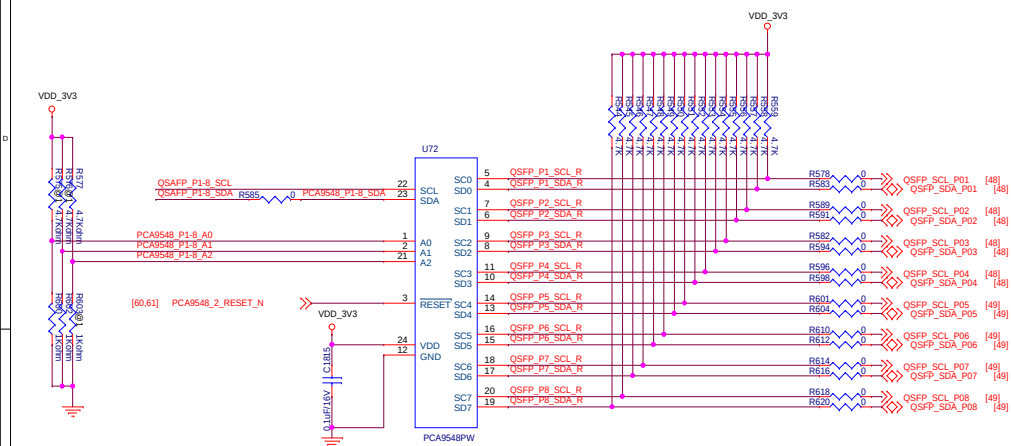
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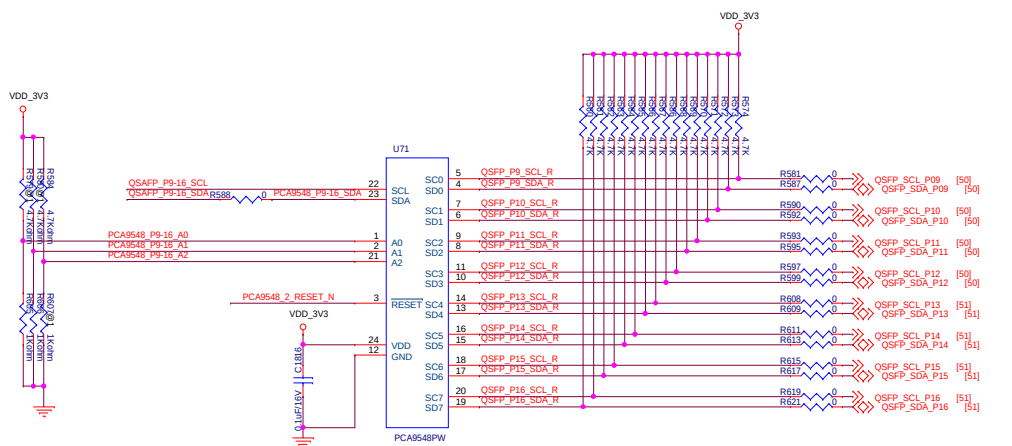






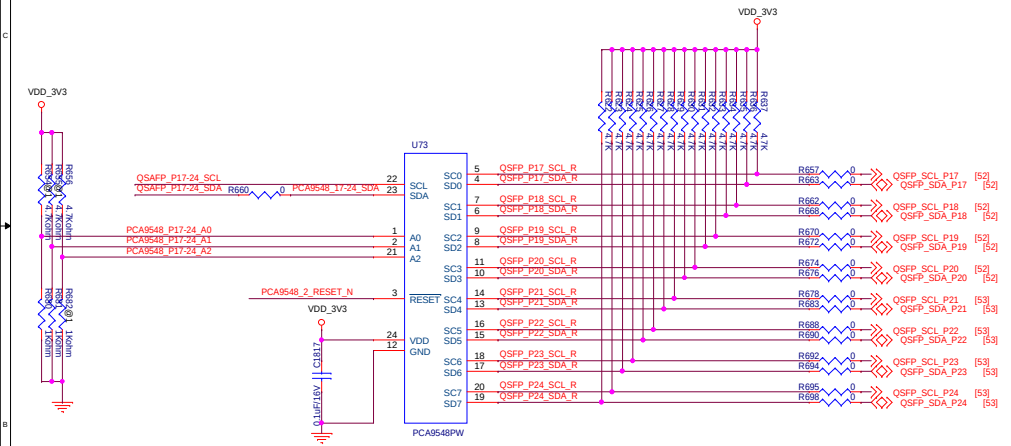
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7bit I2C address : 0x74



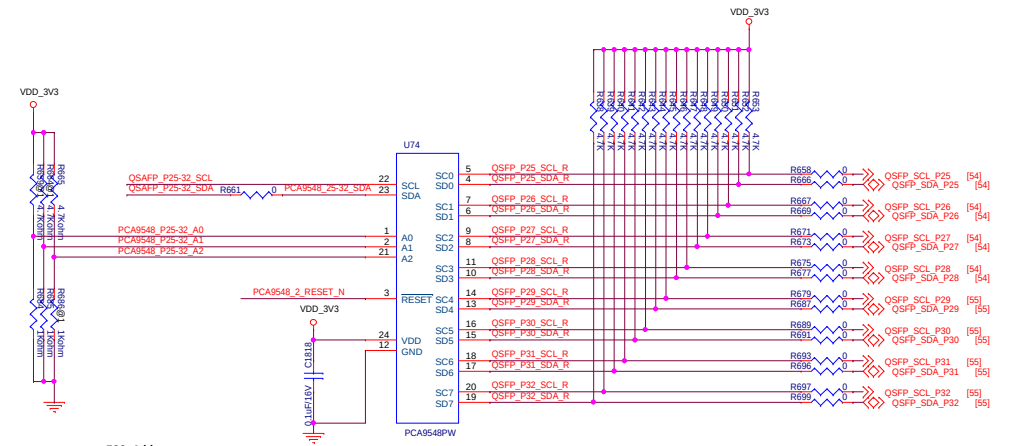
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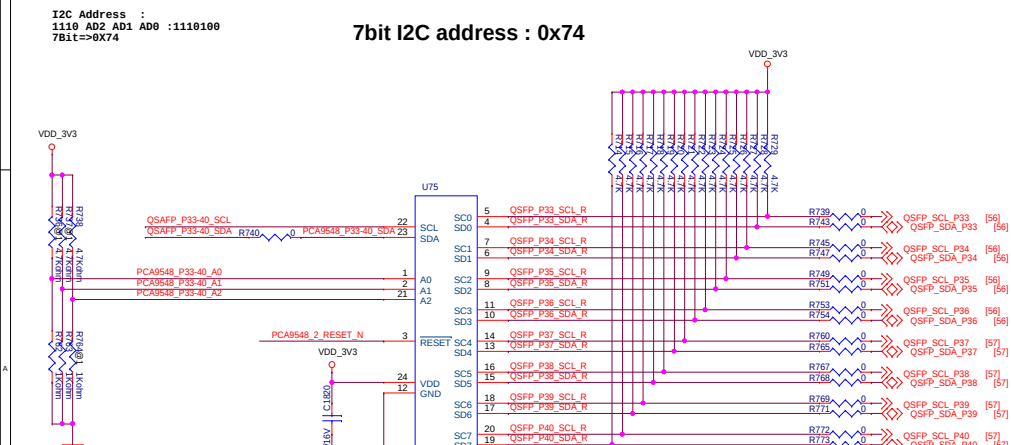
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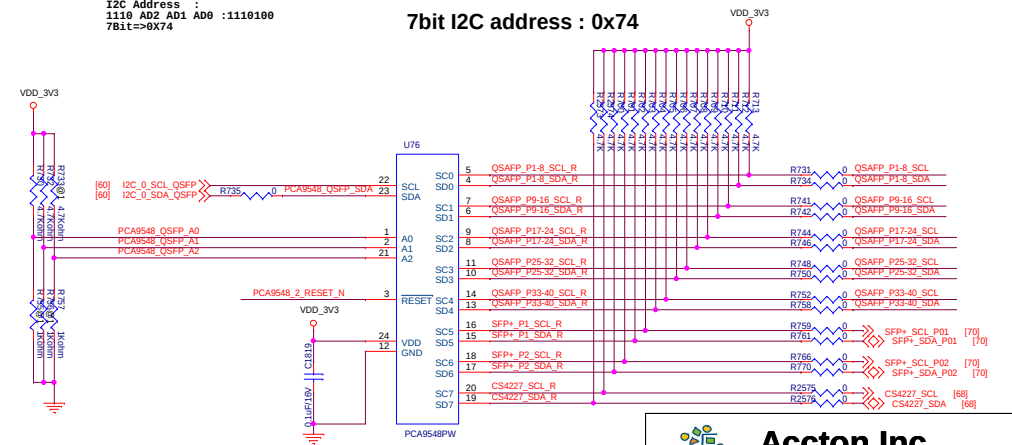
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7Bit=>0x74

7bit I2C address : 0x74



I2C Address :
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7Bit=>0x74

7bit I2C address : 0x74



I2C Address :
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7Bit=>0x73

7bit I2C address : 0x73

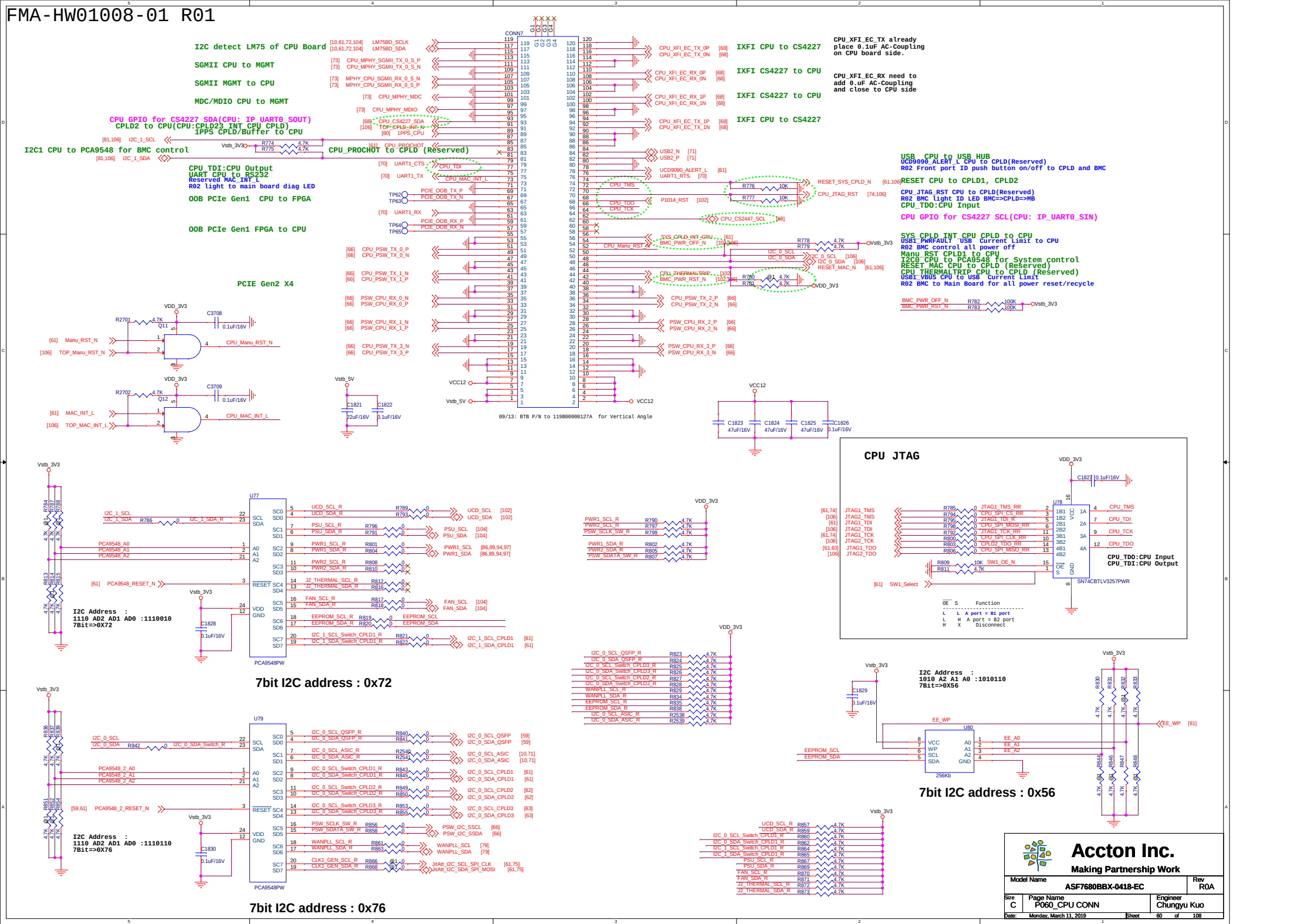


Accton Inc.

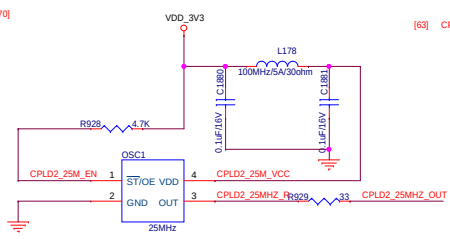
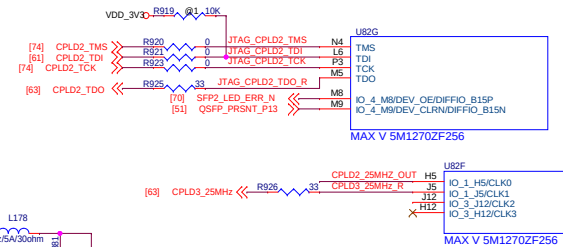
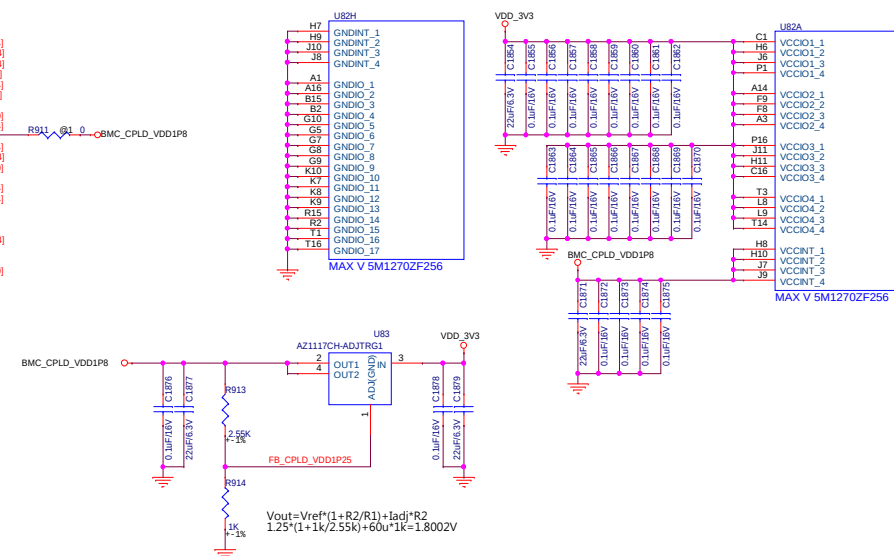
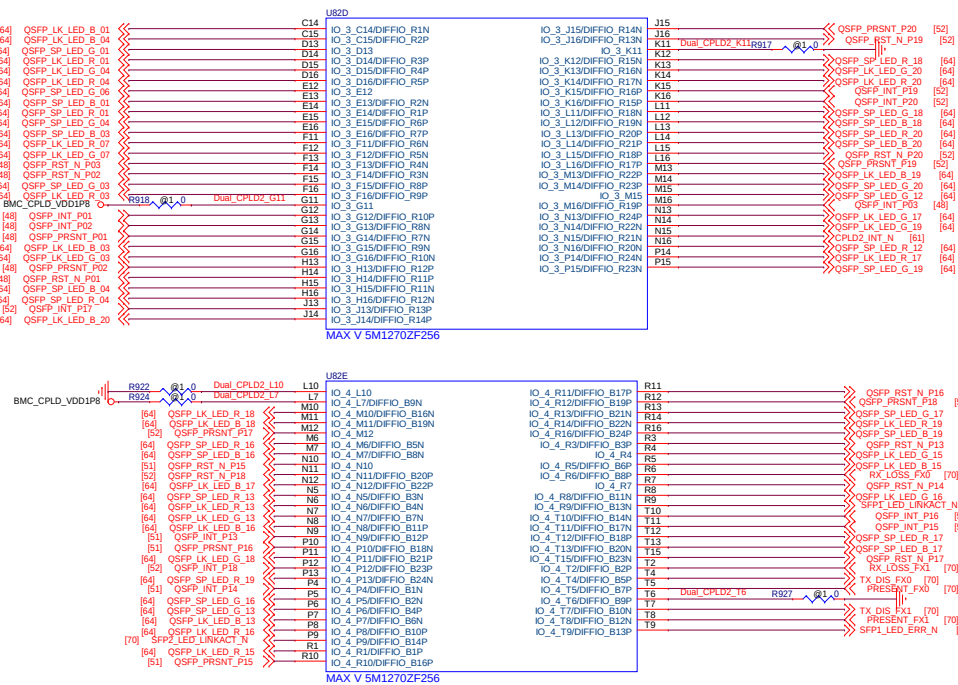
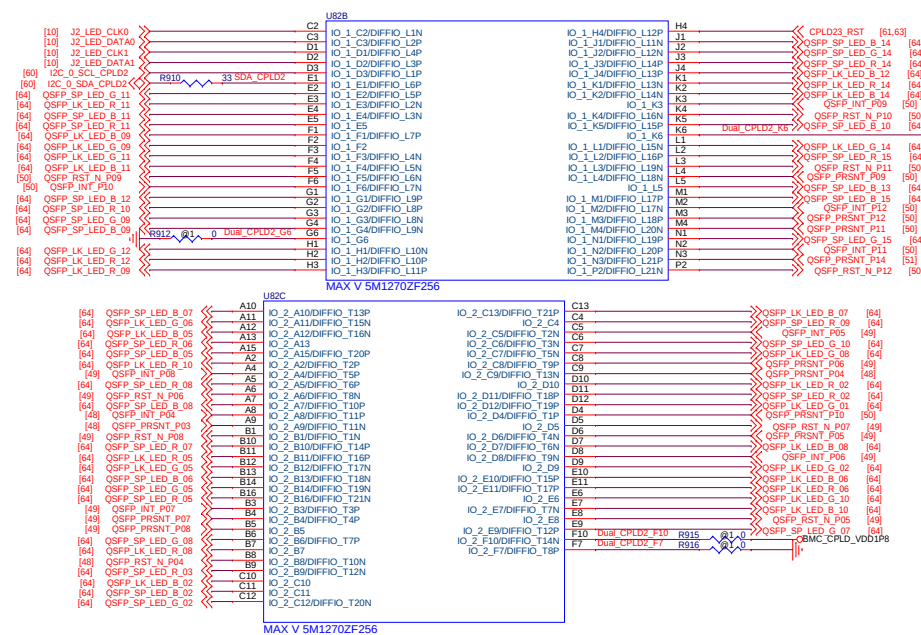
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Size	Page Name	P059_QSFP I2C MUX		Engineer
C				Chungyu Kuo
Date:	Monday, March 11, 2019	Sheet	99	of 108

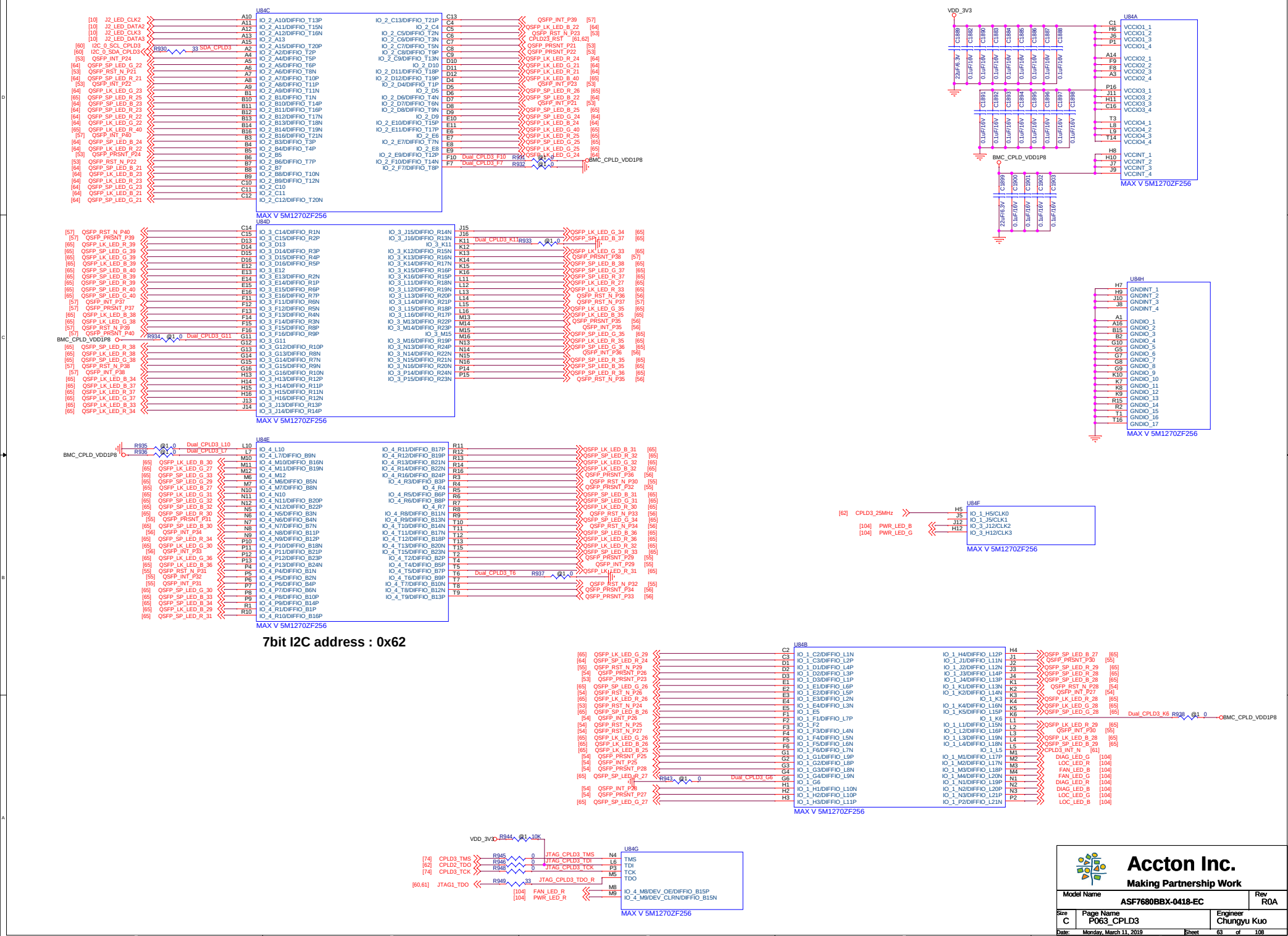
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FMA-HW01008-01 R01

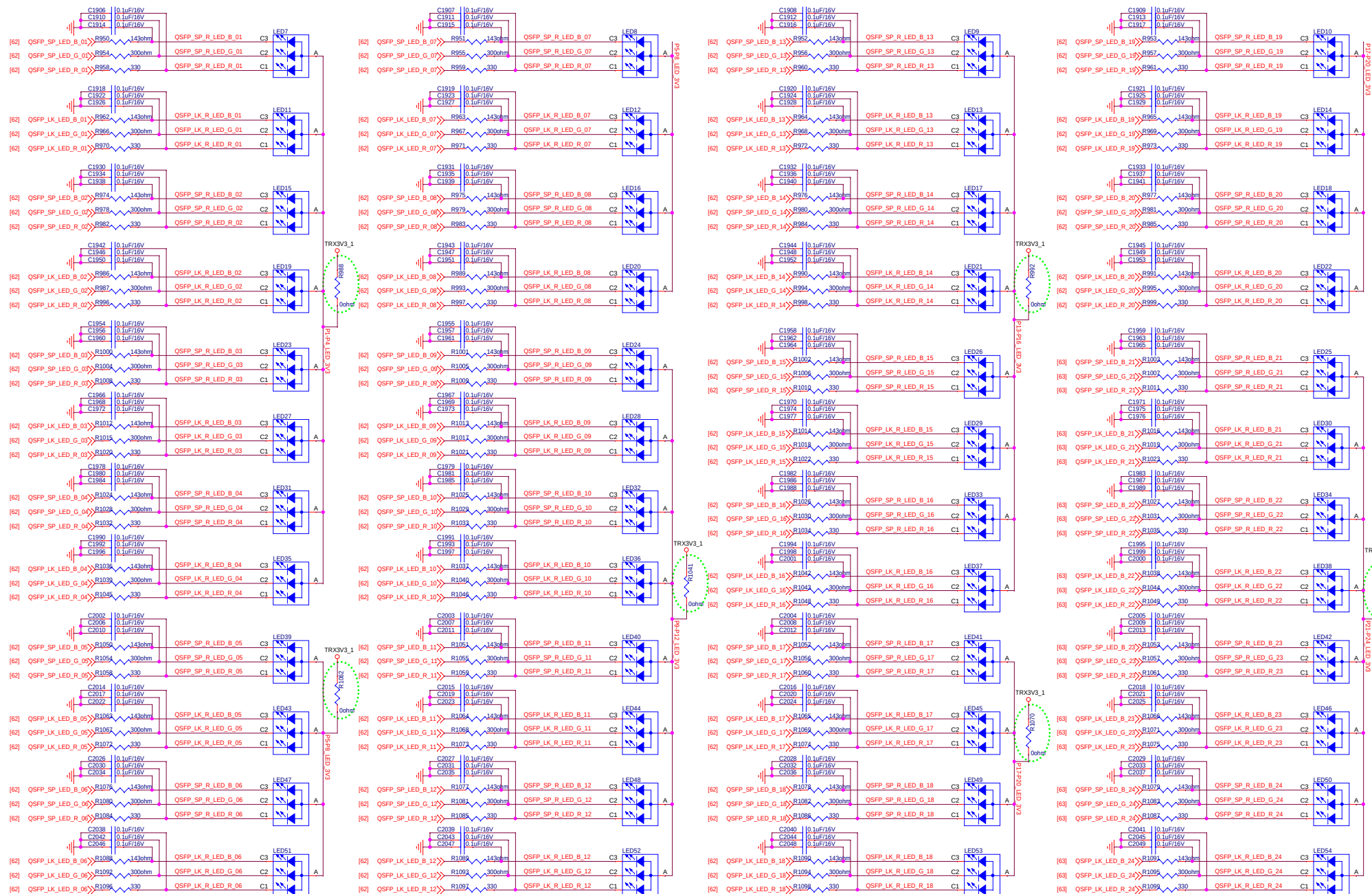






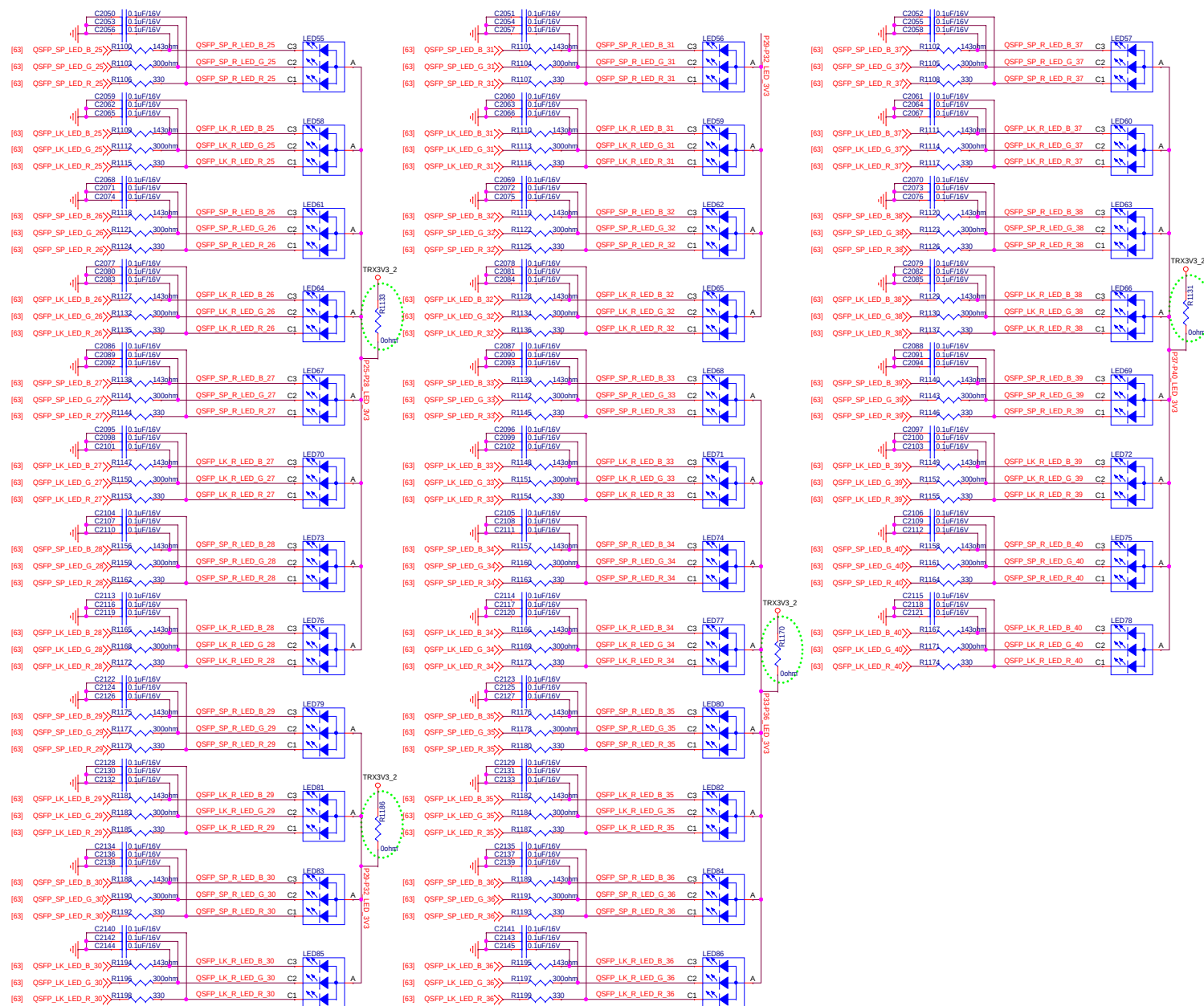
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FMA-HW01008-01 R01



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Model Name			Rev
ASF7680BBX-0418-EC			ROA
Size	Page Name	Engineer	
C	P064_QSFP_LED 1- 24	Chungyu Kuo	
Date:	Monday, March 11, 2019	Sheet	64 of 108

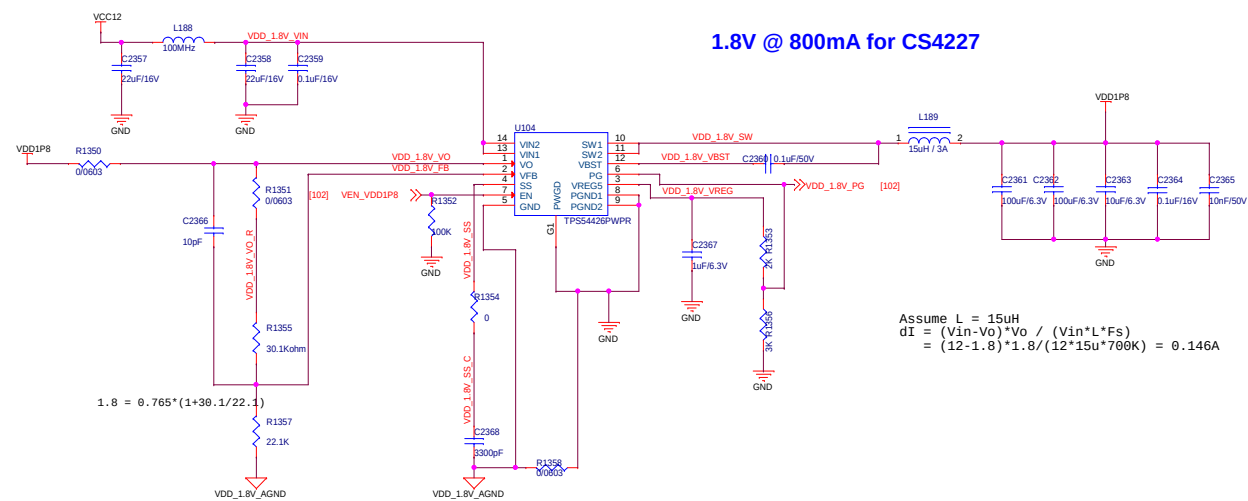
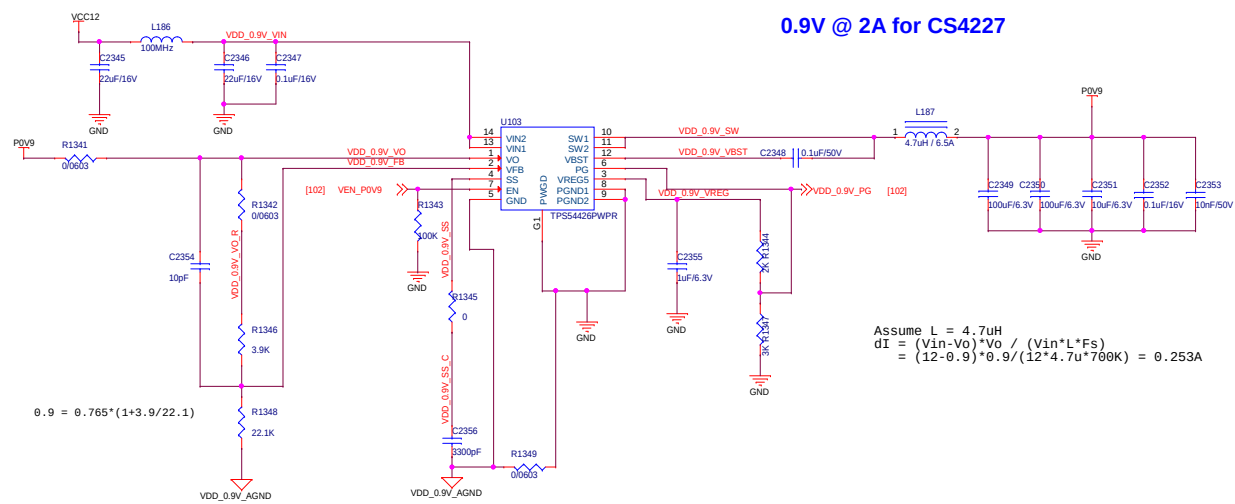


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Size	Page Name	P065_QSFP LED 25-40	Engineer	
C			Chungyu Kuo	
Date:	Monday, March 11, 2019	Sheet	65	of 108

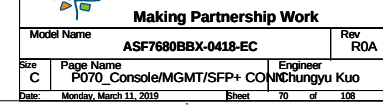




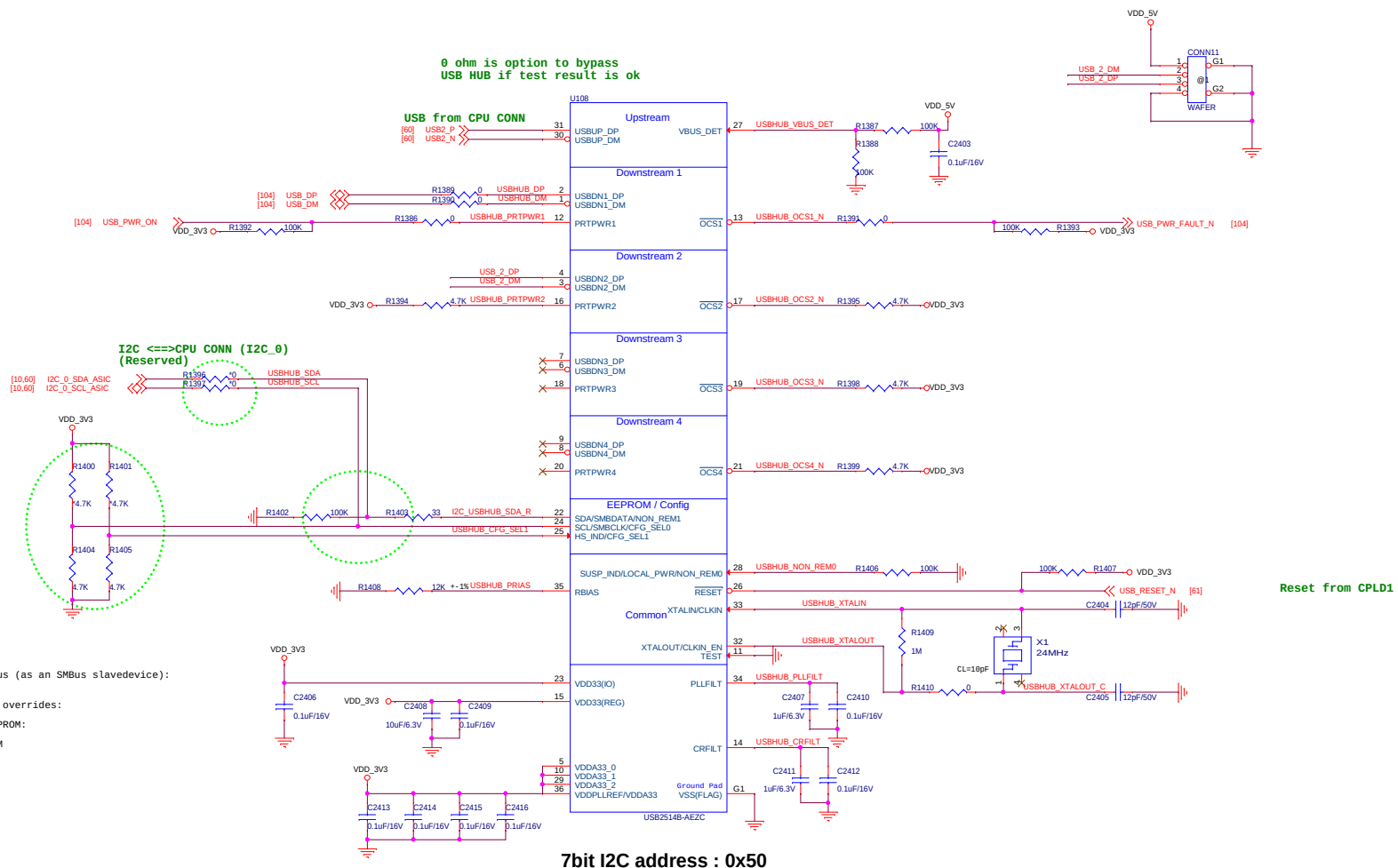


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Size	Page Name	P069_CS4227 1.8V & 0.9V	Engineer	Chungyu Kuo
C	Date:	Monday, March 11, 2019	Sheet	69 of 108



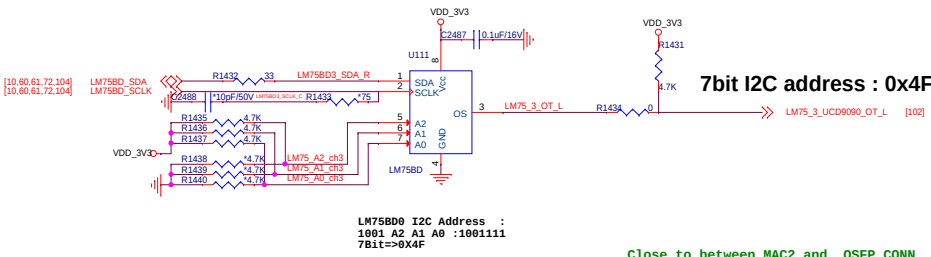
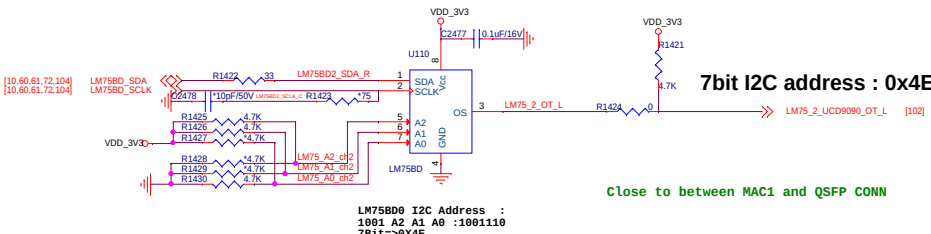
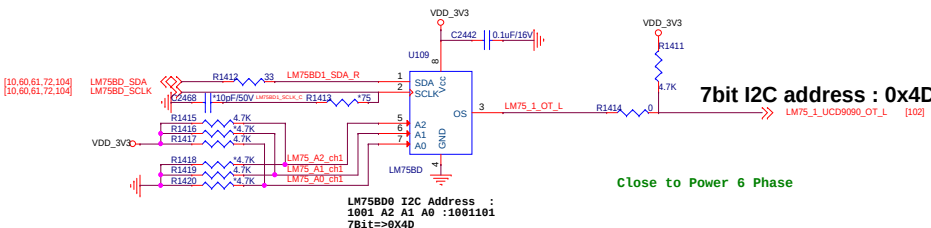
Move USB connector to mini I/O board



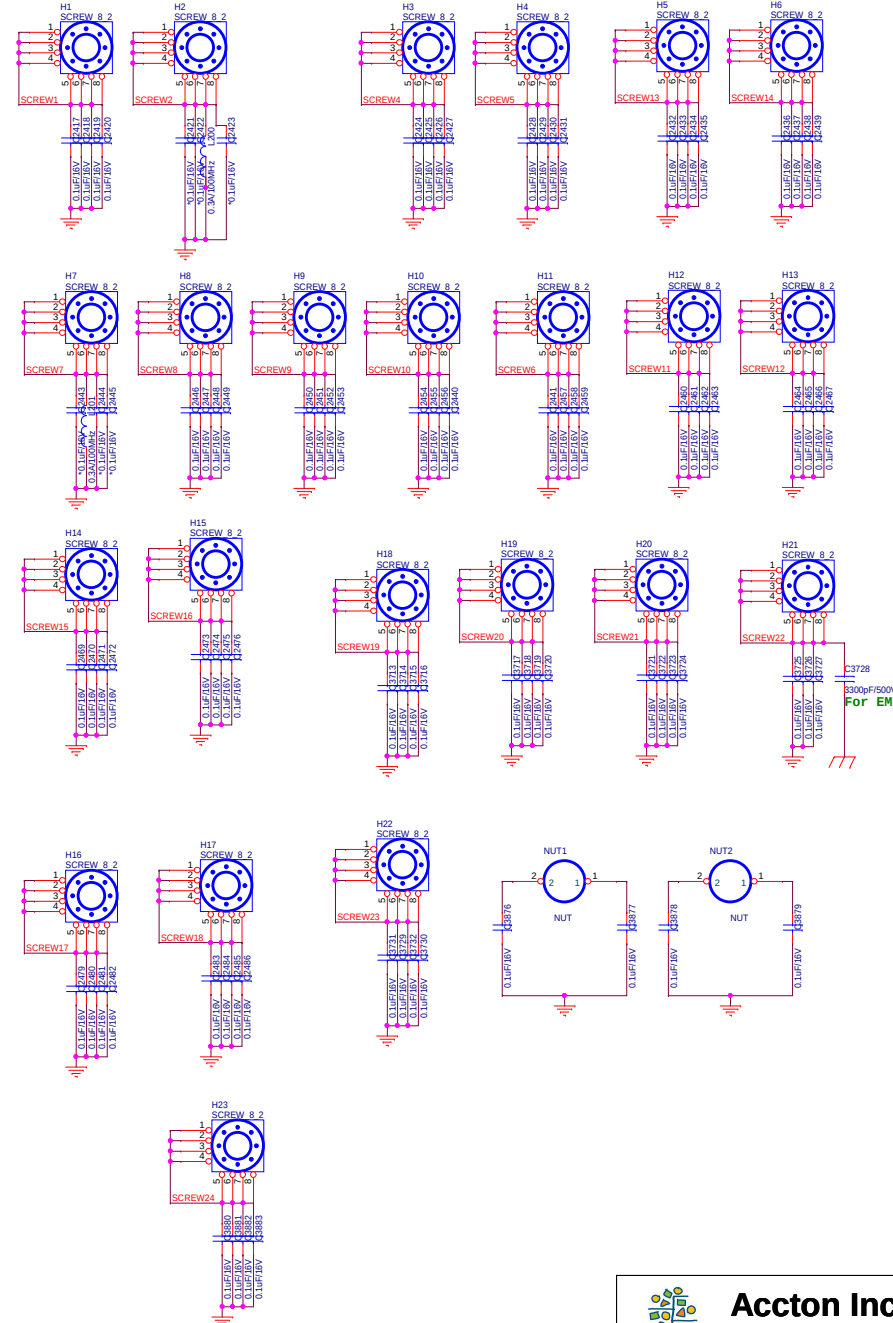
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Initial Interface
CFG_SEL[1:0]
  00 = default configuration:
    Strap options enabled
    Self-powered operation enabled
    Individual power switching
    Individual over-current sensing
01 = The hub is configured externally over SMBus (as an SMBus slave device):
    Strap options disabled
    All registers configured over SMBus
10 = Default configuration with the following overrides:
    Bus-powered operation
11 = The hub is configured over 2-wire I2C EEPROM:
    Strap options disabled
    All registers configured by I2C EEPROM
```

7bit I2C address : 0x50

Thermal sensor



Mounting Hole

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Model Name	ASF7680BBX-0418-EC
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Size C	Page Name P072_Thermal Sensor & Screw	Engineer Chungyu Kuo
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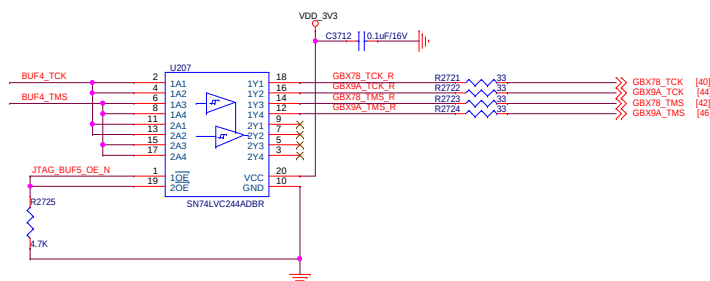
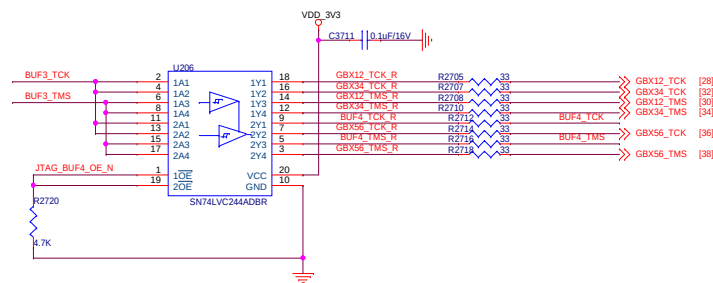
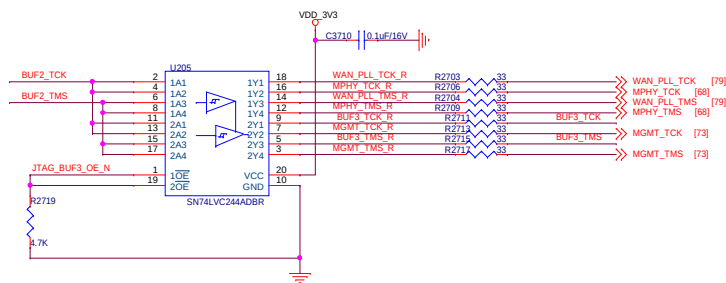
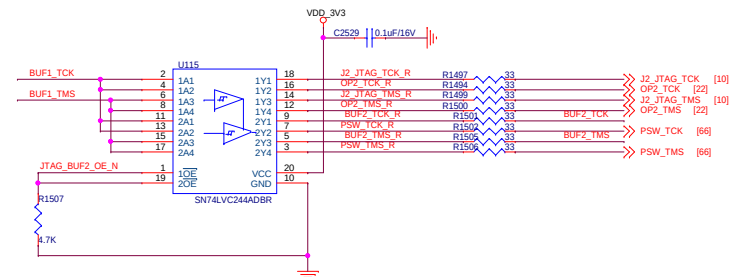
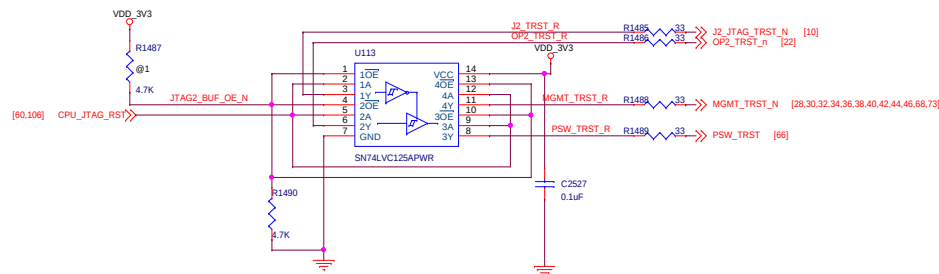
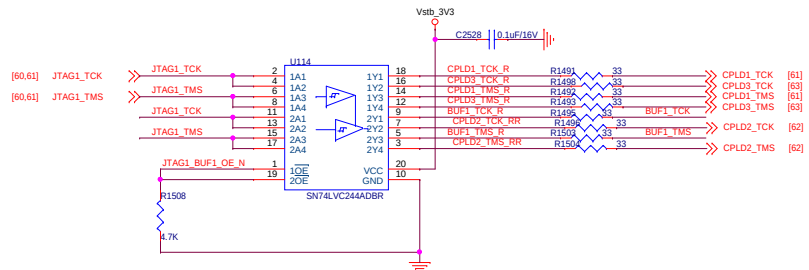
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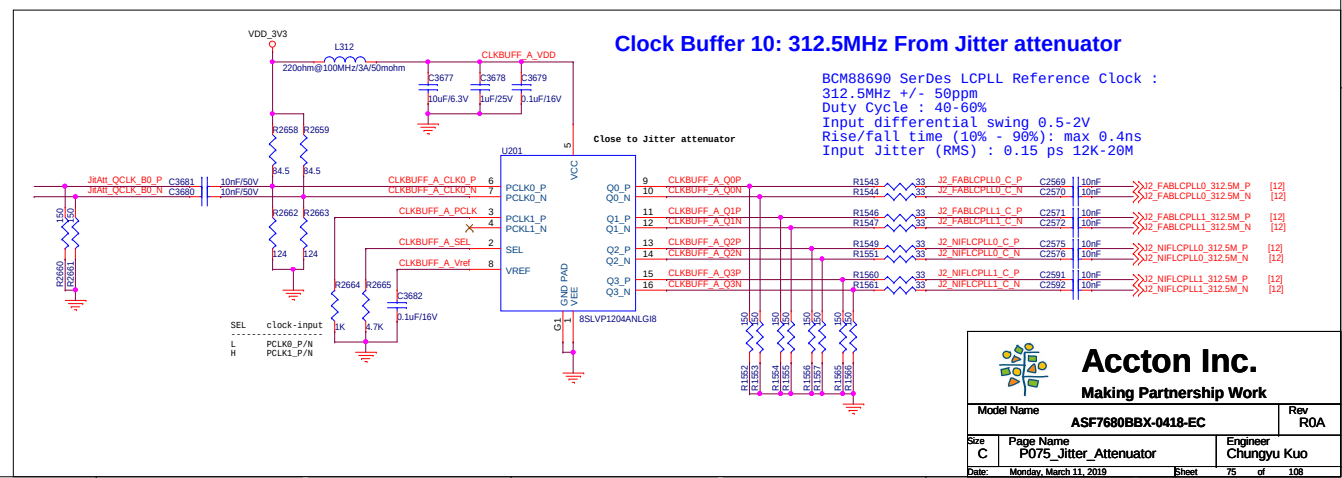
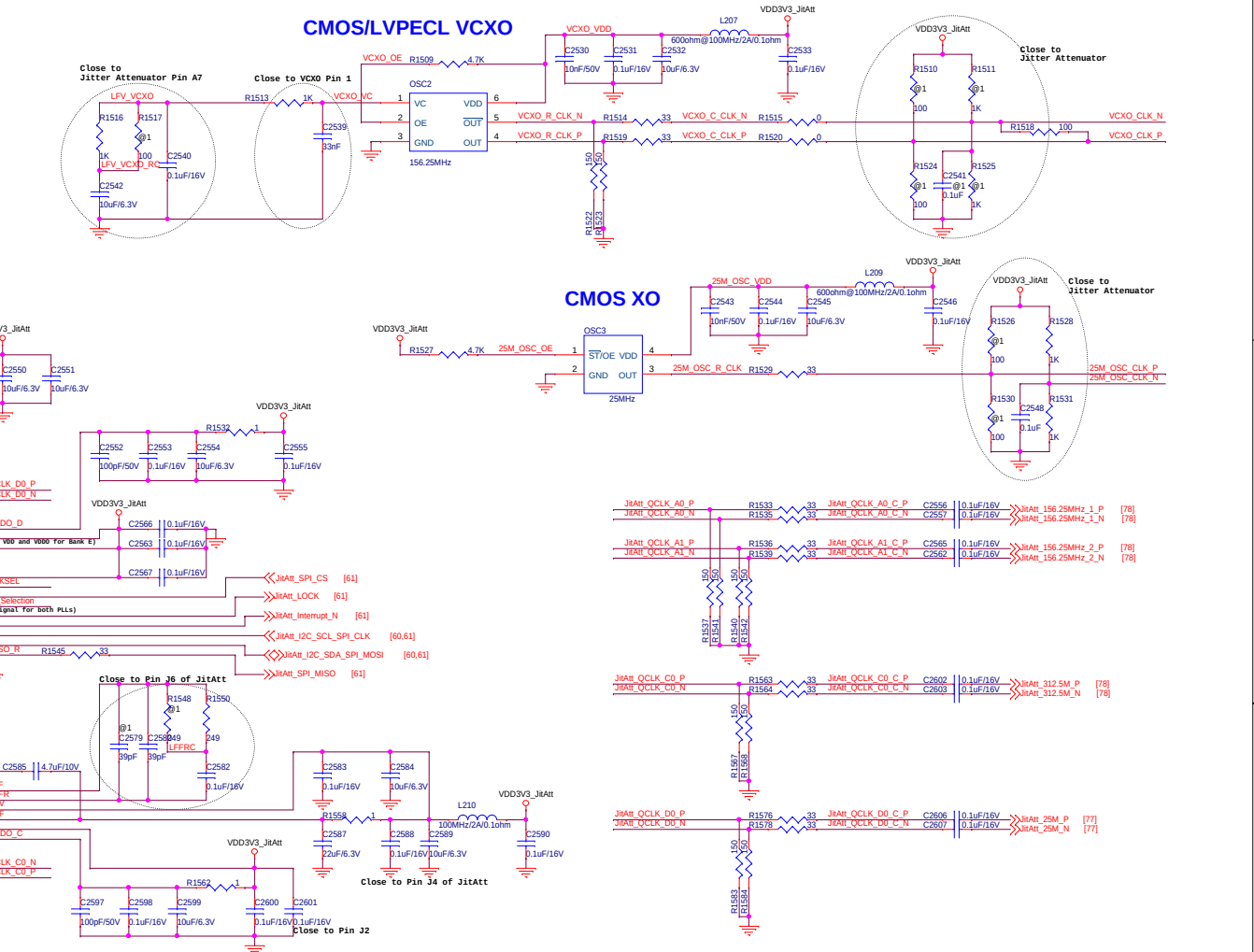
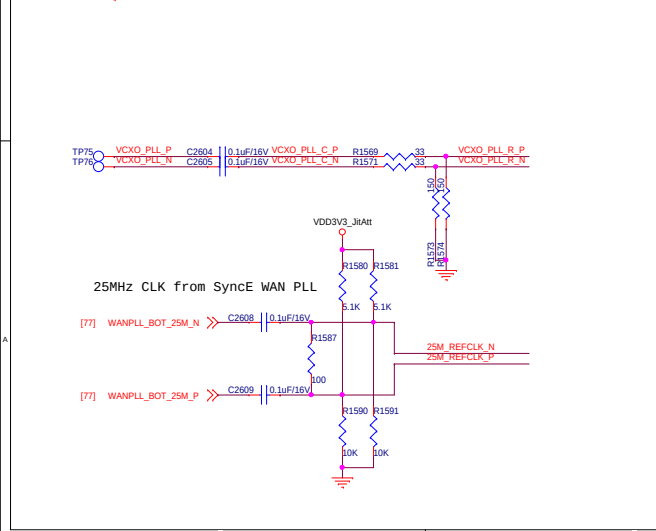
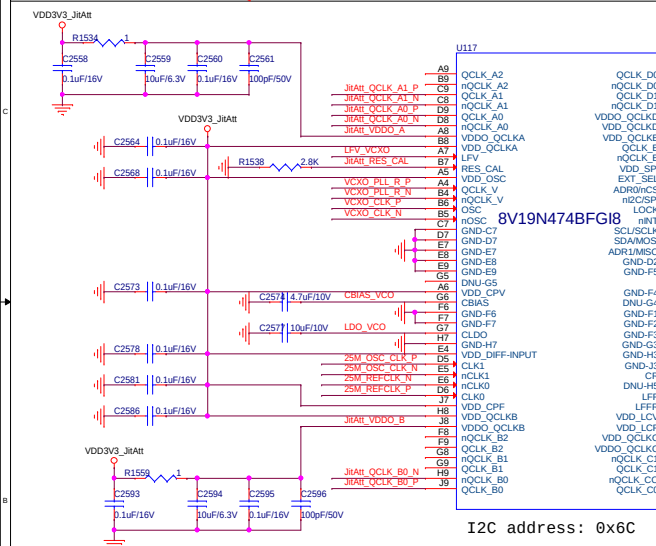
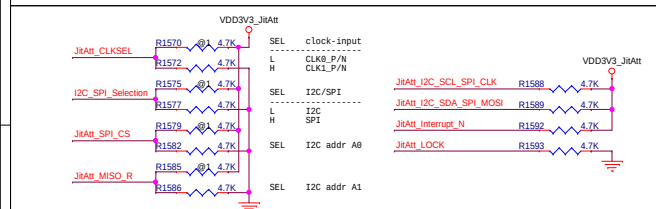
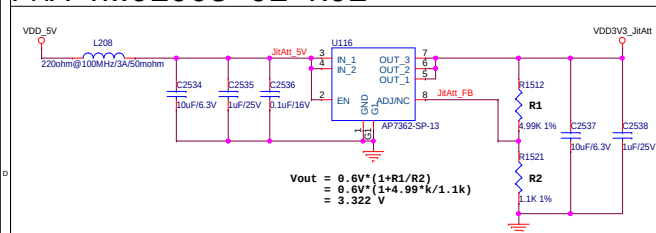
Pin	Bit[2:0]
VDDO	111
LED_LINK10	110
LED_LINK100	101
LED_LINK1000	100
LED_DUPLEX	011
LED_RX	010
LED_TX	001
VSS	000

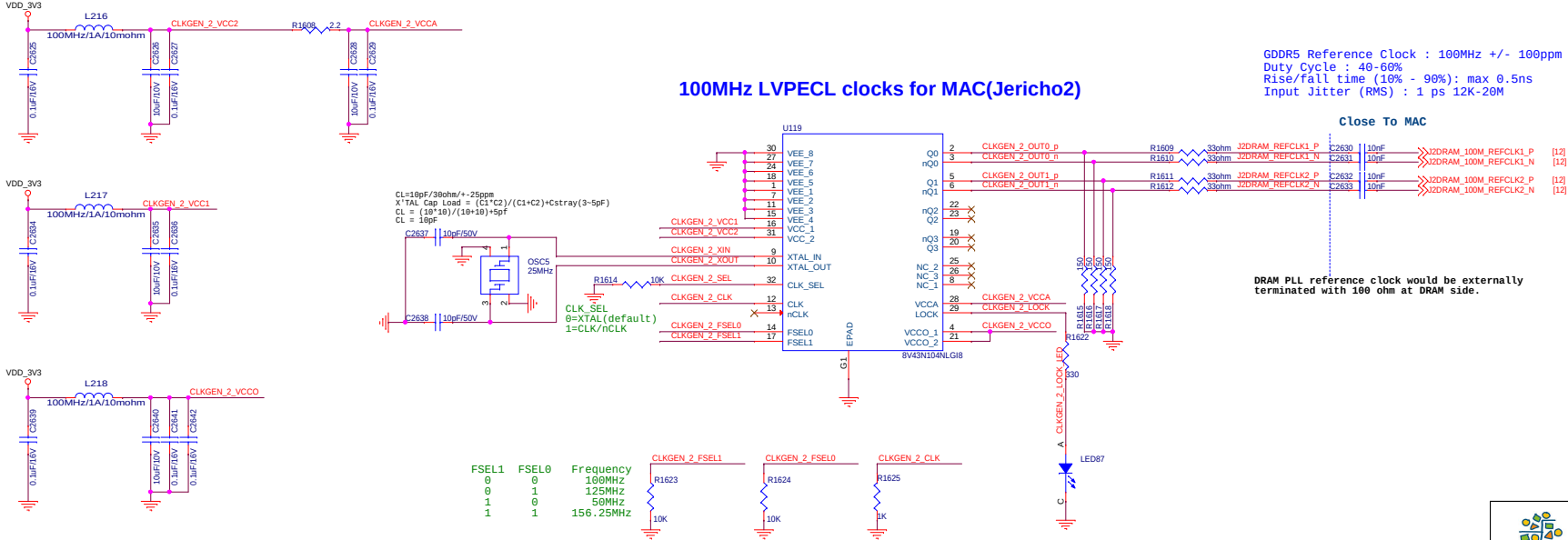
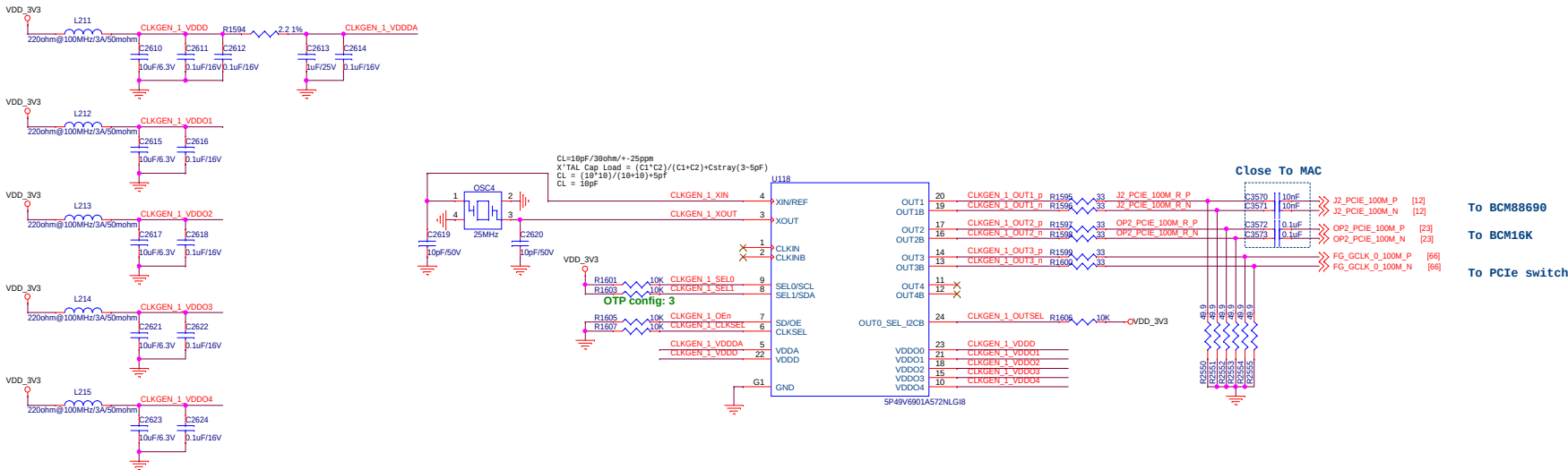
Pin	Bit[2]	Bit[1]	Bit[0]
CONFIG0	PHYADR[2] ¹	PHYADR[1] ¹	PHYADR[0] ¹
CONFIG1	ENA_PAUSE	PHYADR[4] ¹	PHYADR[3] ¹
CONFIG2	ANEG[3]	ANEG[2]	ANEG[1]
CONFIG3	ANEG[0]	ENA_XC	DIS_125
CONFIG4	HWCFG_MODE[2]	HWCFG_MODE[1]	HWCFG_MODE[0]
CONFIG5	DIS_FC	DIS_SLEEP	HWCFG_MODE[3]
CONFIG6	SEL_TWSI	INT_POL	75/50 OHM

1. For the TWSI device address, the lower 5 bits (PHYADR[4:0]), are latched during hardware reset, and the device address bits ([6:5]) are fixed at '10'.



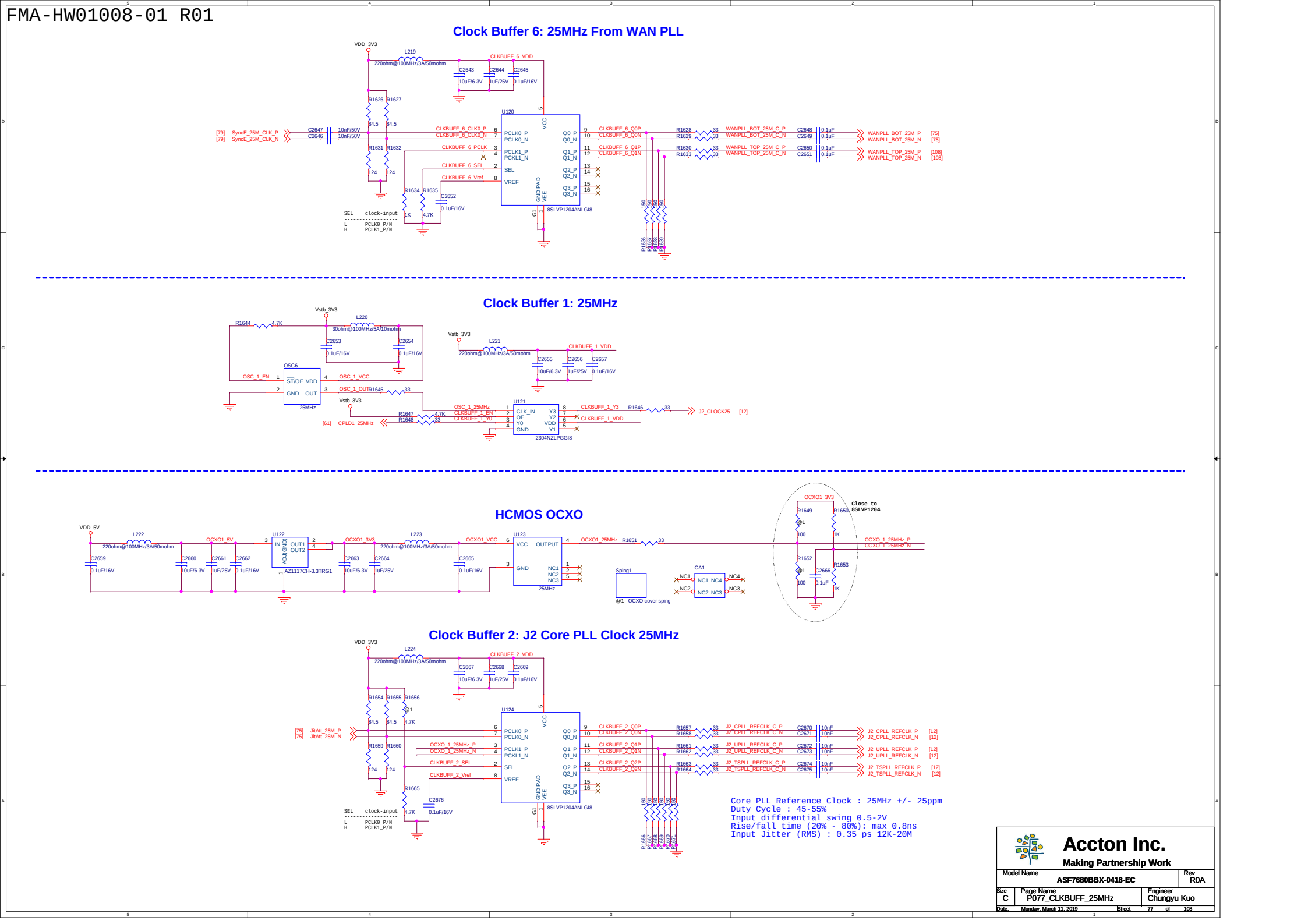
5
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FMA-HW01008-01 R01

Clock Buffer 6: 25MHz From WAN PLL



FMA-HW01008-01 R01

Clock Buffer 6: 25MHz From WAN PLL

Clock Buffer 1: 25MHz

HCMOS OCXO

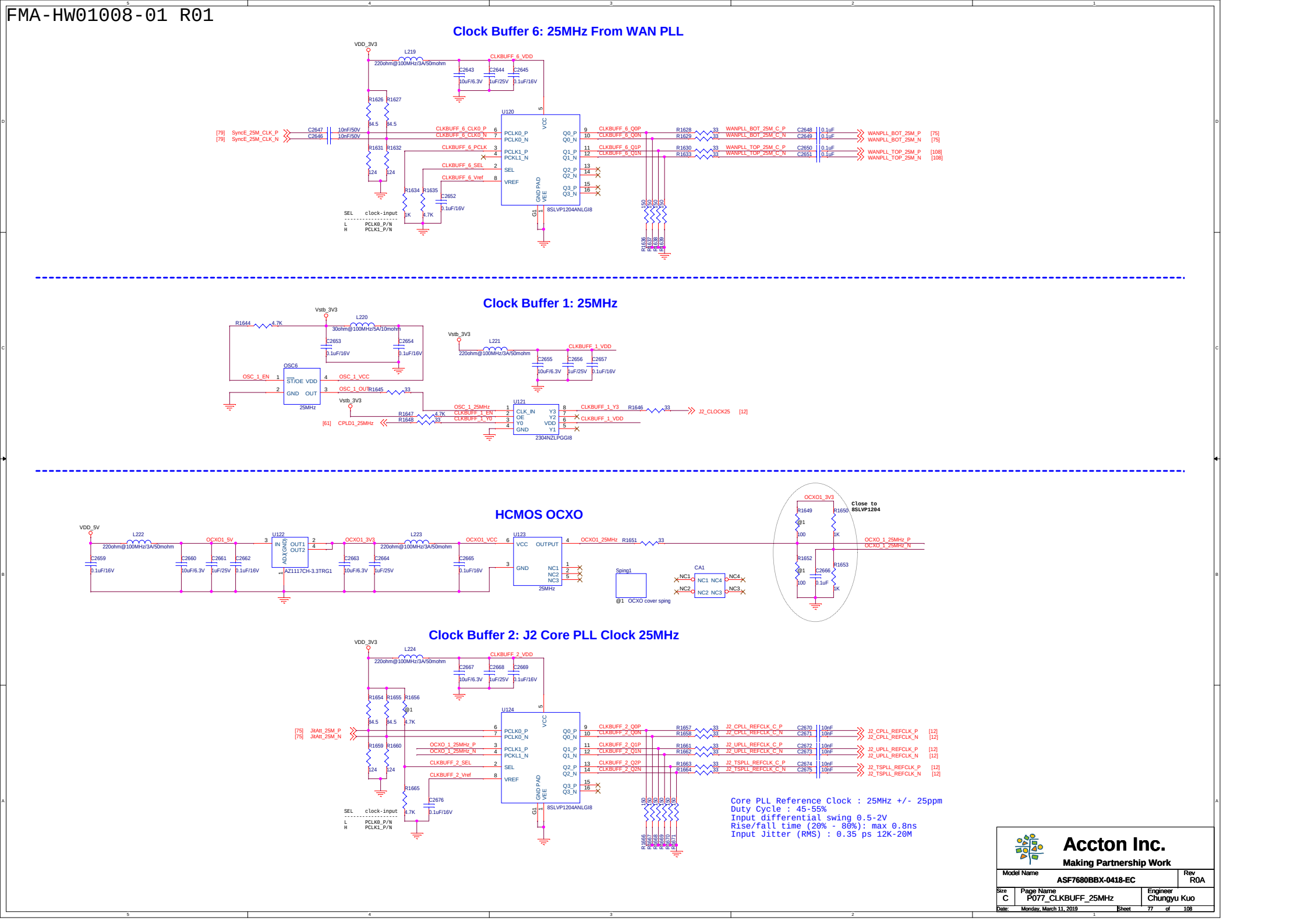
Clock Buffer 2: J2 Core PLL Clock 25MHz

Core PLL Reference Clock : 25MHz +/- 25ppm
Duty Cycle : 45-55%
Input differential swing 0.5-2V
Rise/fall time (20% - 80%) : max 0.8ns
Input Jitter (RMS) : 0.35 ps 12K-20M

Model Name		Rev
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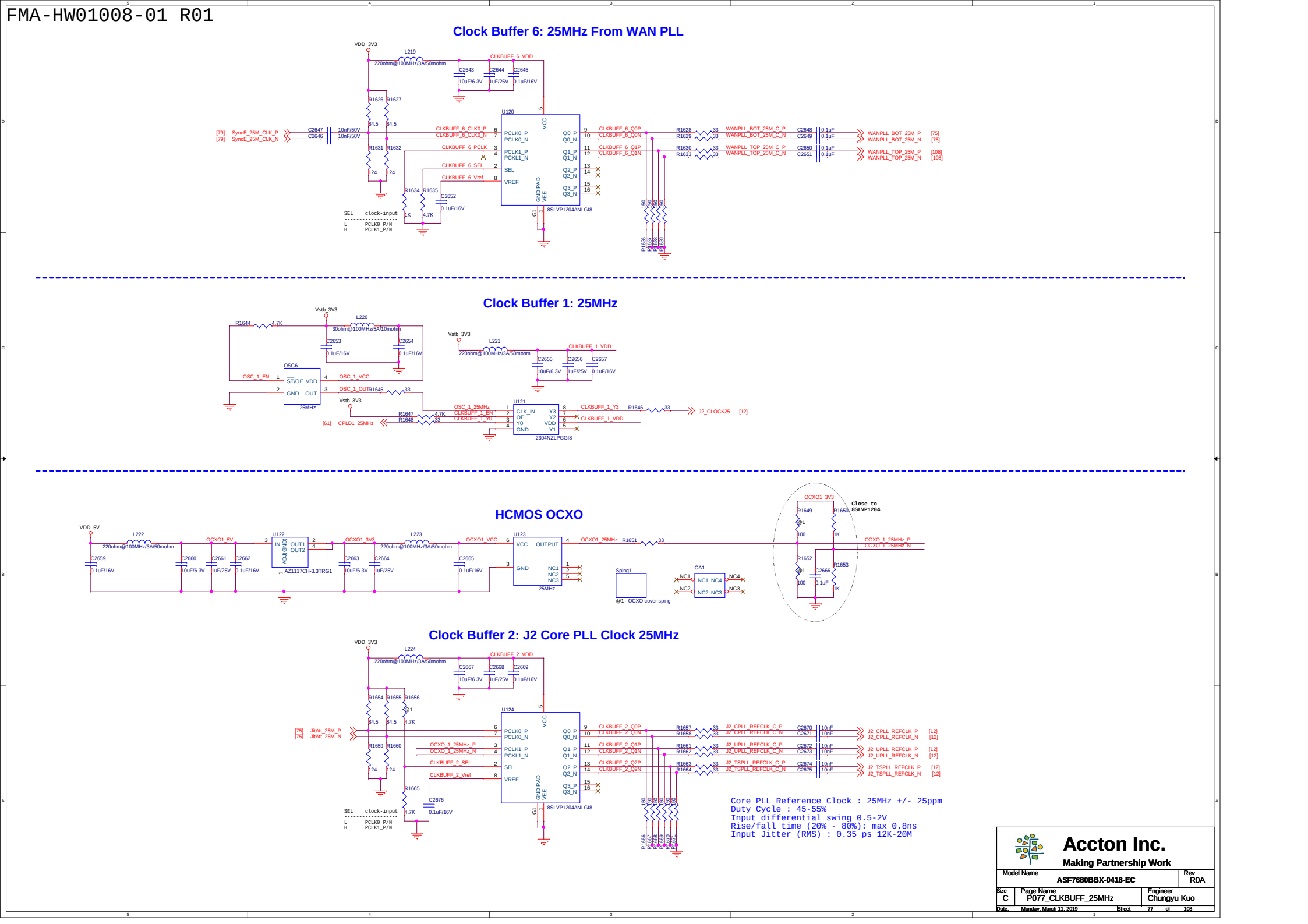
Size	Page Name	Engineer
C	P077_CLKBUFF_25MHz	Chungyu Kuo

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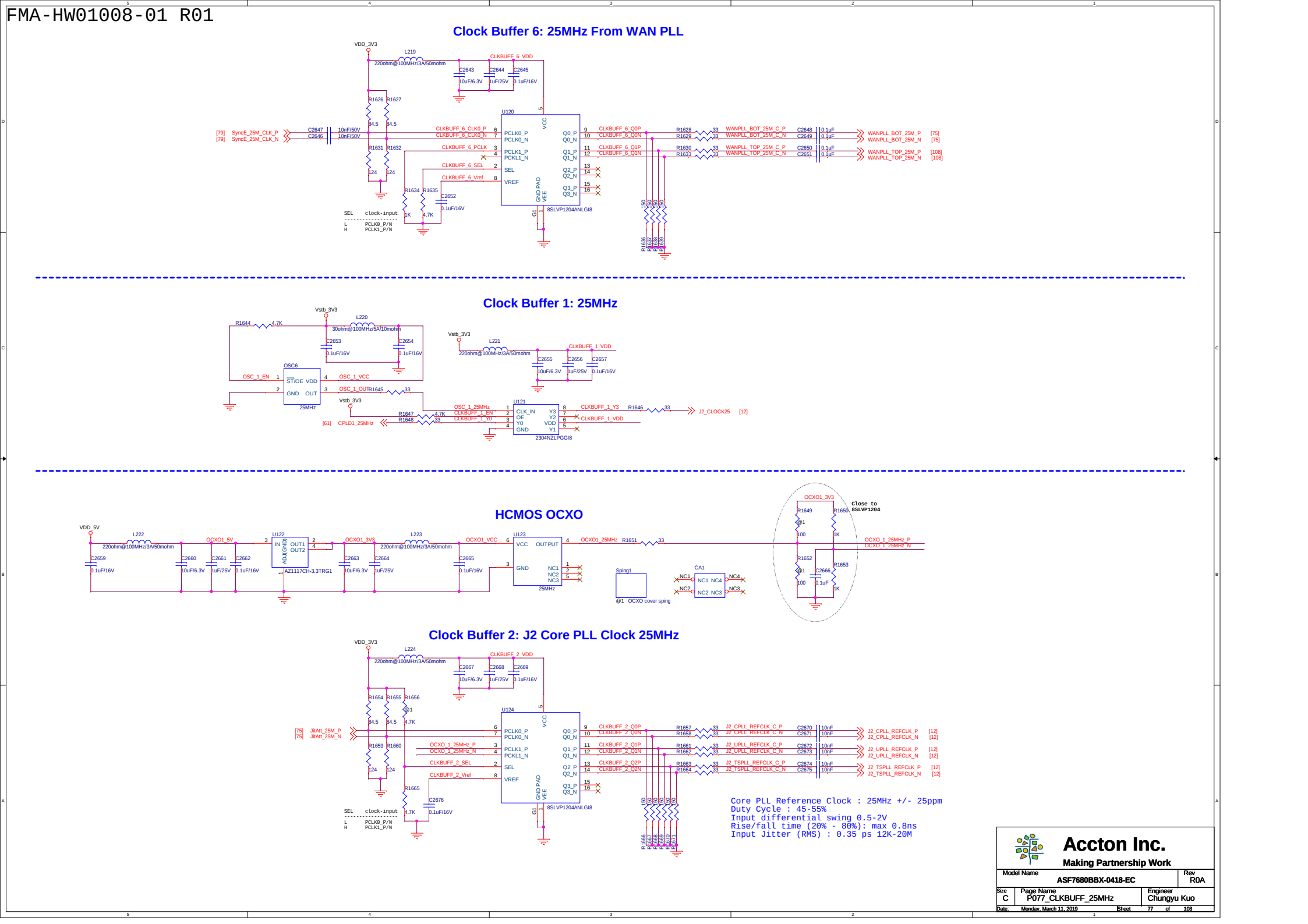
Clock Buffer 6: 25MHz From WAN PLL



FMA-HW01008-01 R01

Clock Buffer 6: 25MHz From WAN PLL

Diagram showing the circuit for Clock Buffer 6, which is a 25MHz clock buffer derived from the WAN PLL. The circuit includes a 25MHz clock input (CLKIN) connected to the buffer's input (QIN). The output (QOUT) is connected to the WAN PLL output (WANPLL_25M_P). The circuit also includes a 25MHz clock output (CLKOUT) connected to the WAN PLL output (WANPLL_25M_N). The circuit is powered by VDD_3V3 and includes various capacitors (C2647, C2648, C2649, C2650, C2651, C2652, C2653, C2654, C2655, C2656, C2657, C2658, C2659, C2660, C2661, C2662, C2663, C2664, C2665, C2666, C2667, C2668, C2669, C2670, C2671, C2672, C2673, C2674, C2675, C2676, C2677, C2678, C2679, C2680, C2681, C2682, C2683, C2684, C2685, C2686, C2687, C2688, C2689, C2690, C2691, C2692, C2693, C2694, C2695, C2696, C2697, C2698, C2699, C2700, C2701, C2702, C2703, C2704, C2705, C2706, C2707, C2708, C2709, C2710, C2711, C2712, C2713, C2714, C2715, C2716, C2717, C2718, C2719, C2720, C2721, C2722, C2723, C2724, C2725, C2726, C2727, C2728, C2729, C2730, C2731, C2732, C2733, C2734, C2735, C2736, C2737, C2738, C2739, C2740, C2741, C2742, C2743, C2744, C2745, C2746, C2747, C2748, C2749, C2750, C2751, C2752, C2753, C2754, C2755, C2756, C2757, C2758, C2759, C2760, C2761, C2762, C2763, C2764, C2765, C2766, C2767, C2768, C2769, C2770, C2771, C2772, C2773, C2774, C2775, C2776, C2777, C2778, C2779, C2780, C2781, C2782, C2783, C2784, C2785, C2786, C2787, C2788, C2789, C2790, C2791, C2792, C2793, C2794, C2795, C2796, C2797, C2798, C2799, C2800, C2801, C2802, C2803, C2804, C2805, C2806, C2807, C2808, C2809, C2810, C2811, C2812, C2813, C2814, C2815, C2816, C2817, C2818, C2819, C2820, C2821, C2822, C2823, C2824, C2825, C2826, C2827, C2828, C2829, C2830, C2831, C2832, C2833, C2834, C2835, C2836, C2837, C2838, C2839, C2840, C2841, C2842, C2843, C2844, C2845, C2846, C2847, C2848, C2849, C2850, C2851, C2852, C2853, C2854, C2855, C2856, C2857, C2858, C2859, C2860, C2861, C2862, C2863, C2864, C2865, C2866, C2867, C2868, C2869, C2870, C2871, C2872, C2873, C2874, C2875, C2876, C2877, C2878, C2879, C2880, C2881, C2882, C2883, C2884, C2885, C2886, C2887, C2888, C2889, C2890, C2891, C2892, C2893, C2894, C2895, C2896, C2897, C2898, C2899, C2900, C2901, C2902, C2903, C2904, C2905, C2906, C2907, C2908, C2909, C2910, C2911, C2912, C2913, C2914, C2915, C2916, C2917, C2918, C2919, C2920, C2921, C2922, C2923, C2924, C2925, C2926, C2927, C2928, C2929, C2930, C2931, C2932, C2933, C2934, C2935, C2936, C2937, C2938, C2939, C2940, C2941, C2942, C2943, C2944, C2945, C2946, C2947, C2948, C2949, C2950, C2951, C2952, C2953, C2954, C2955, C2956, C2957, C2958, C2959, C2960, C2961, C2962, C2963, C2964, C2965, C2966, C2967, C2968, C2969, C2970, C2971, C2972, C2973, C2974, C2975, C2976, C2977, C2978, C2979, C2980, C2981, C2982, C2983, C2984, C2985, C2986, C2987, C2988, C2989, C2990, C2991, C2992, C2993, C2994, C2995, C2996, C2997, C2998, C2999, C3000, C3001, C3002, C3003, C3004, C3005, C3006, C3007, C3008, C3009, C3010, C3011, C3012, C3013, C3014, C3015, C3016, C3017, C3018, C3019, C3020, C3021, C3022, C3023, C3024, C3025, C3026, C3027, C3028, C3029, C3030, C3031, C3032, C3033, C3034, C3035, C3036, C3037, C3038, C3039, C3040, C3041, C3042, C3043, C3044, C3045, C3046, C3047, C3048, C3049, C3050, C3051, C3052, C3053, C3054, C3055, C3056, C3057, C3058, C3059, C3060, C3061, C3062, C3063, C3064, C3065, C3066, C3067, C3068, C3069, C3070, C3071, C3072, C3073, C3074, C3075, C3076, C3077, C3078, C3079, C3080, C3081, C3082, C3083, C3084, C3085, C3086, C3087, C3088, C3089, C3090, C3091, C3092, C3093, C3094, C3095, C3096, C3097, C3098, C3099, C3100, C3101, C3102, C3103, C3104, C3105, C3106, C3107, C3108, C3109, C3110, C3111, C3112, C3113, C3114, C3115, C3116, C3117, C3118, C3119, C3120, C3121, C3122, C3123, C3124, C3125, C3126, C3127, C3128, C3129, C3130, C3131, C3132, C3133, C3134, C3135, C3136, C3137, C3138, C3139, C3140, C3141, C3142, C3143, C3144, C3145, C3146, C3147, C3148, C3149, C3150, C3151, C3152, C3153, C3154, C3155, C3156, C3157, C3158, C3159, C3160, C3161, C3162, C3163, C3164, C3165, C3166, C3167, C3168, C3169, C3170, C3171, C3172, C3173, C3174, C3175, C3176, C3177, C3178, C3179, C3180, C3181, C3182, C3183, C3184, C3185, C3186, C3187, C3188, C3189, C3190, C3191, C3192, C3193, C3194, C3195, C3196, C3197, C3198, C3199, C3200, C3201, C3202, C3203, C3204, C3205, C3206, C3207, C3208, C3209, C3210, C3211, C3212, C3213, C3214, C3215, C3216, C3217, C3218, C3219, C3220, C3221, C3222, C3223, C3224, C3225, C3226, C3227, C3228, C3229, C3230, C3231, C3232, C3233, C3234, C3235, C3236, C3237, C3238, C3239, C3240, C3241, C3242, C3243, C3244, C3245, C3246, C3247, C3248, C3249, C3250, C3251, C3252, C3253, C3254, C3255, C3256, C3257, C3258, C3259, C3260, C3261, C3262, C3263, C3264, C3265, C3266, C3267, C3268, C3269, C3270, C3271, C3272, C3273, C3274, C3275, C3276, C3277, C3278, C3279, C3280, C3281, C3282, C3283, C3284, C3285, C3286, C3287, C3288, C3289, C3290, C3291, C3292, C3293, C3294, C3295, C3296, C3297, C3298,



FMA-HW01008-01 R01

Clock Buffer 6: 25MHz From WAN PLL

Clock Buffer 1: 25MHz

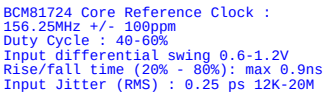
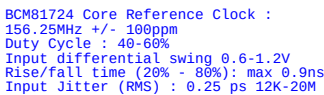
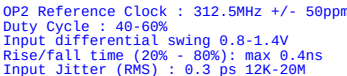
HCMOS OCXO

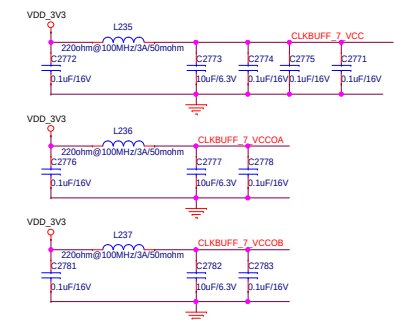
Clock Buffer 2: J2 Core PLL Clock 25MHz

Core PLL Reference Clock : 25MHz +/- 25ppm
Duty Cycle : 45-55%
Input differential swing 0.5-2V
Rise/fall time (20% - 80%): max 0.8ns
Input Jitter (RMS) : 0.35 ps 12K-20M

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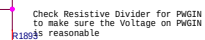
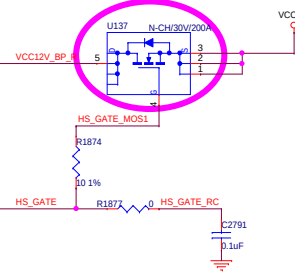
Model Name	ASF7680BBX-0418-EC	Rev	R0A
Size	Page Name	Engineer	
C	P077_CLKBUFF_25MHz	Chungyu Kuo	
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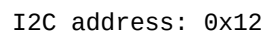


Clock Buffer 9: 1PPS

The schematic shows the internal components of the clock buffer. It includes a 74VHC123 timer (U134) and an 830154AGH-08LFT clock buffer. The circuit is powered by VDD_3V3 and includes a 220ohm resistor, a 100MHz/34750mohm capacitor, and a 0.1uF/16V capacitor. The output of the timer is connected to the CLK_IN pin of the clock buffer. The clock buffer has three outputs: CLKBUFF_9_Q1, CLKBUFF_9_Q2, and CLKBUFF_9_Q3, which are connected to the FRONT_1PPS_CLK, 1PPS_CPU, and J2BOT_1PPS_CLK pins, respectively. The circuit also includes a 1.5k resistor (R1857) and a 4.7k resistor (R1858) connected to the CLK_IN pin.

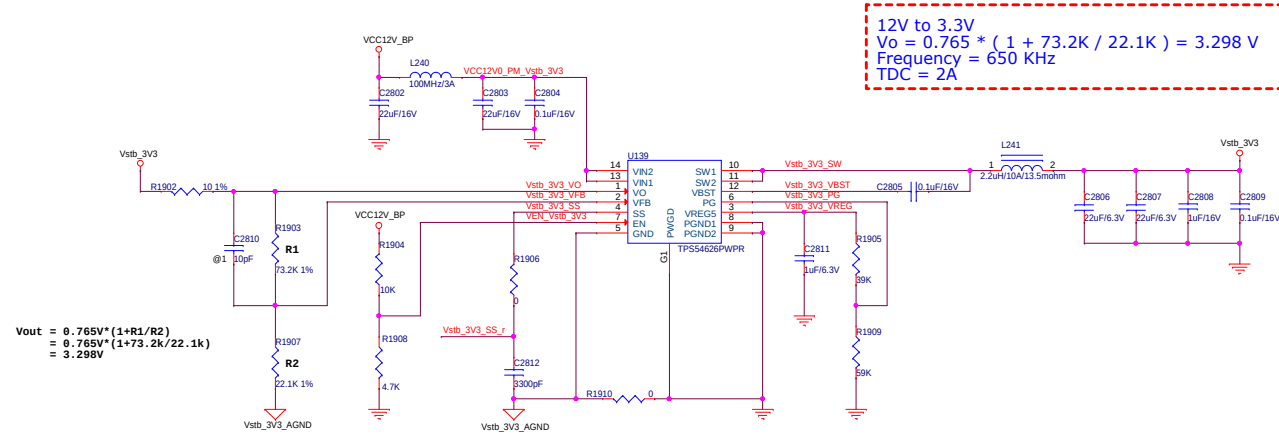


ADR2 State	ADR1 State	Device Address (Hex)
Low	Low	0x10
Low	Resistor	0x11
Low	High-Z	0x12
Low	High	0x13
Resistor	Low	0x40
Resistor	Resistor	0x41
Resistor	High-Z	0x42
Resistor	High	0x43
High-Z	Low	0x44
High-Z	Resistor	0x45
High-Z	High-Z	0x46
High-Z	High	0x47
High	Low	0x50
High	Resistor	0x51
High	High-Z	0x52
High	High	0x53

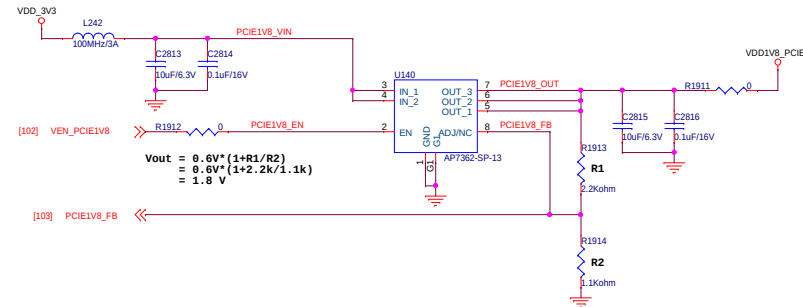


ADRx State	ADRx Pin Connection
Low	Connect to GND
Resistor	150 k Ω resistor to GND
High-Z	No connection (floating)
High	Connect to VCAP

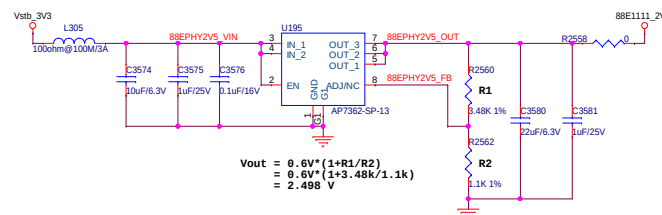
12V convert to 3.3V @2A For Standby Voltage (Power Monitor/CPLD1/MGMT/UCD)



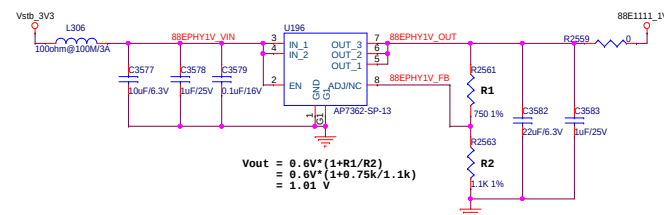
VDD3V3 convert to 1.8V @0.6A For PCIE Switch



Standby 3V3 convert to 2.5V@0.32A For Management PHY

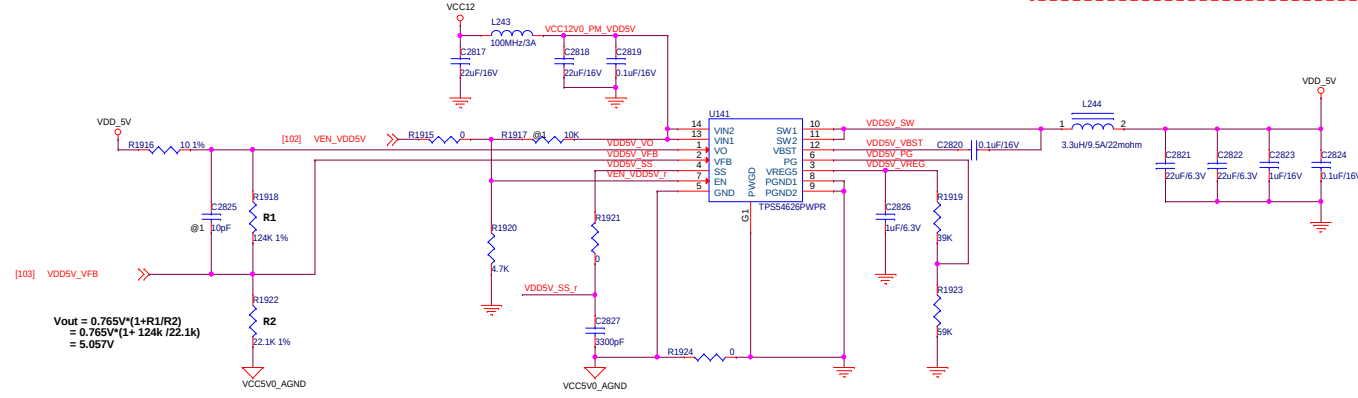


Standby 3V3 convert to 1V@0.21A For Management PHY

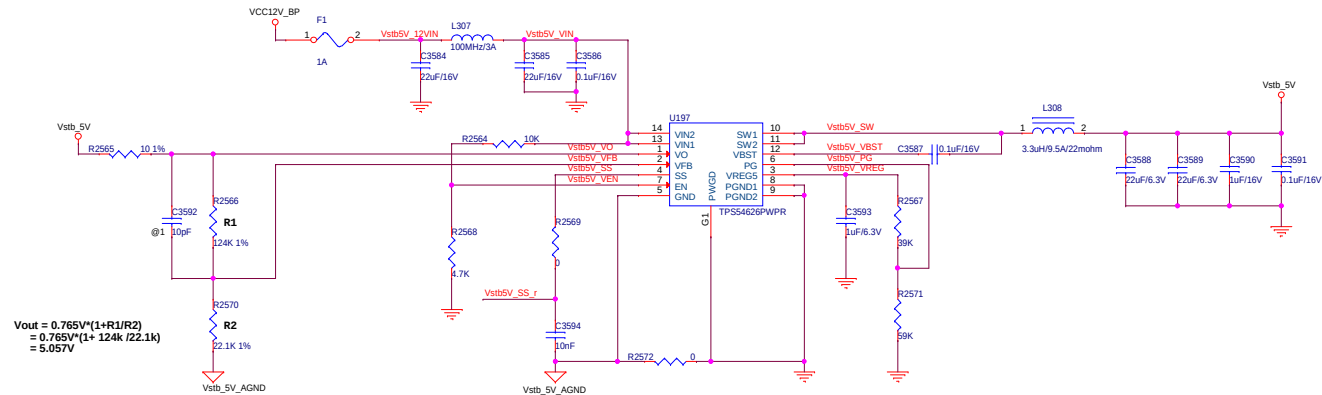


12V convert to 5V @3A For MOS Driver and USB

12V to 5V
 $V_o = 0.765 * (1 + 124k / 22.1k) = 5.057V$
 Frequency = 650 KHz
 TDC = 3A

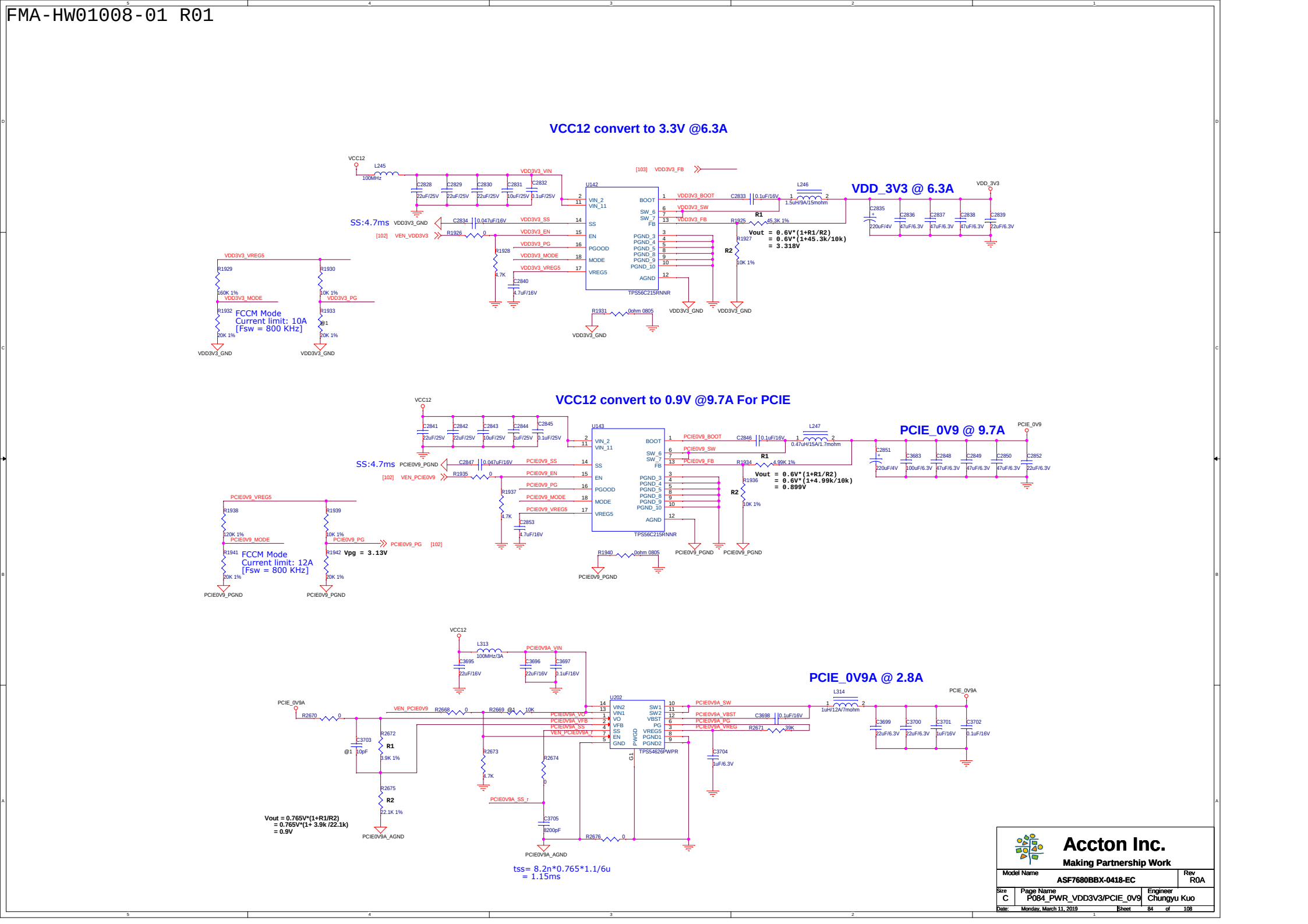


12V convert to 5V @1A For CPU BMC



FMA-HW01008-01 R01

VCC12 convert to 3.3V @6.3A



FMA-HW01008-01 R01

VCC12 convert to 3.3V @6.3A

VDD 3V3 @ 6.3A

VCC12 convert to 0.9V @9.7A For PCIE

PCIE_0V9 @ 9.7A

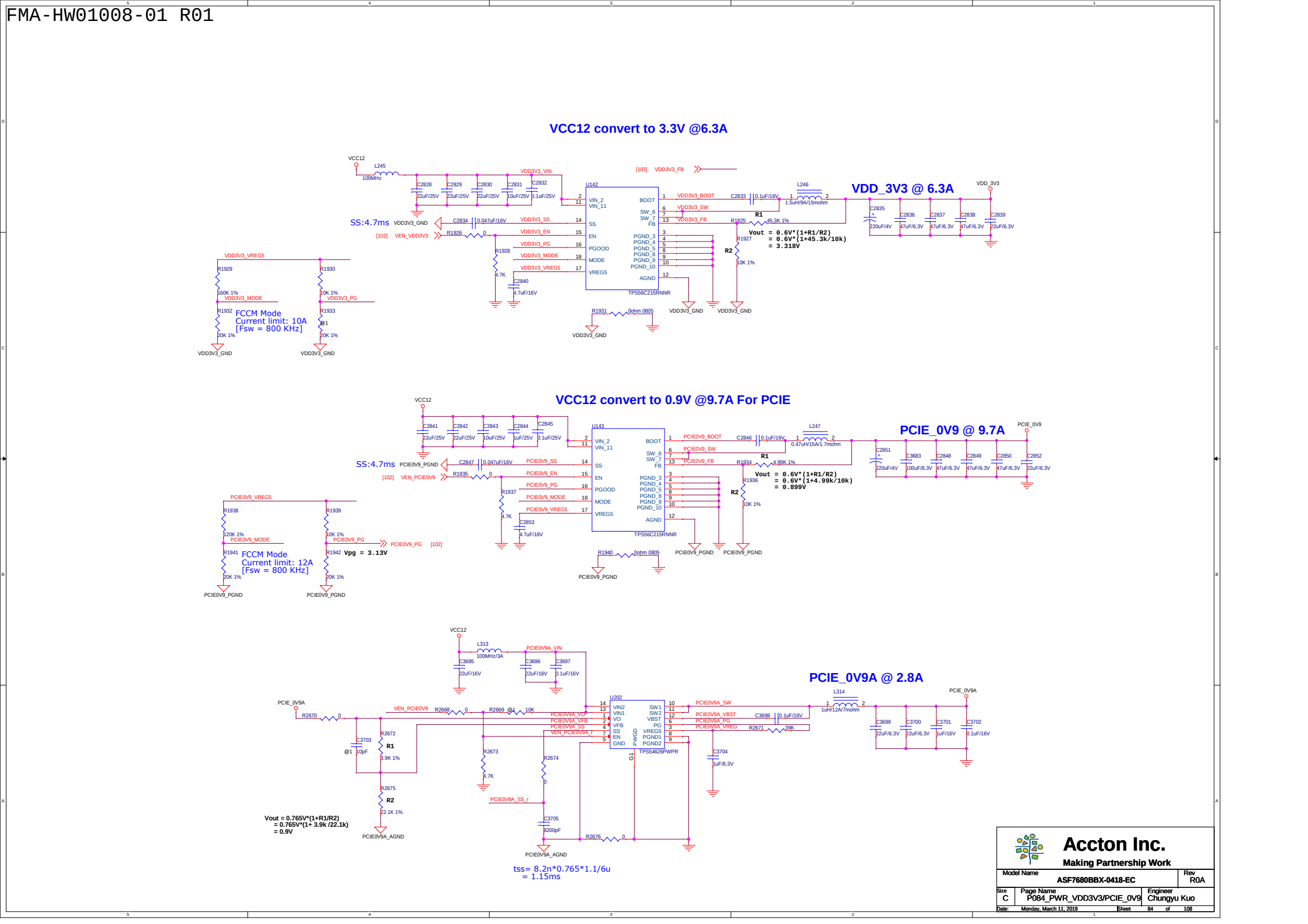
PCIE_0V9A @ 2.8A

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Model Name: ASF7680BBX-0418-EC Rev: R0A

Size: C Page Name: P084_PWR_VDD3V3/PCIE_0V9 Engineer: Chungyu Kuo

Date: Monday, March 11, 2019 Sheet: 84 of 108



FMA-HW01008-01 R01

VCC12 convert to 3.3V @6.3A

VDD 3V3 @ 6.3A

VCC12 convert to 0.9V @9.7A For PCIE

PCIE_0V9 @ 9.7A

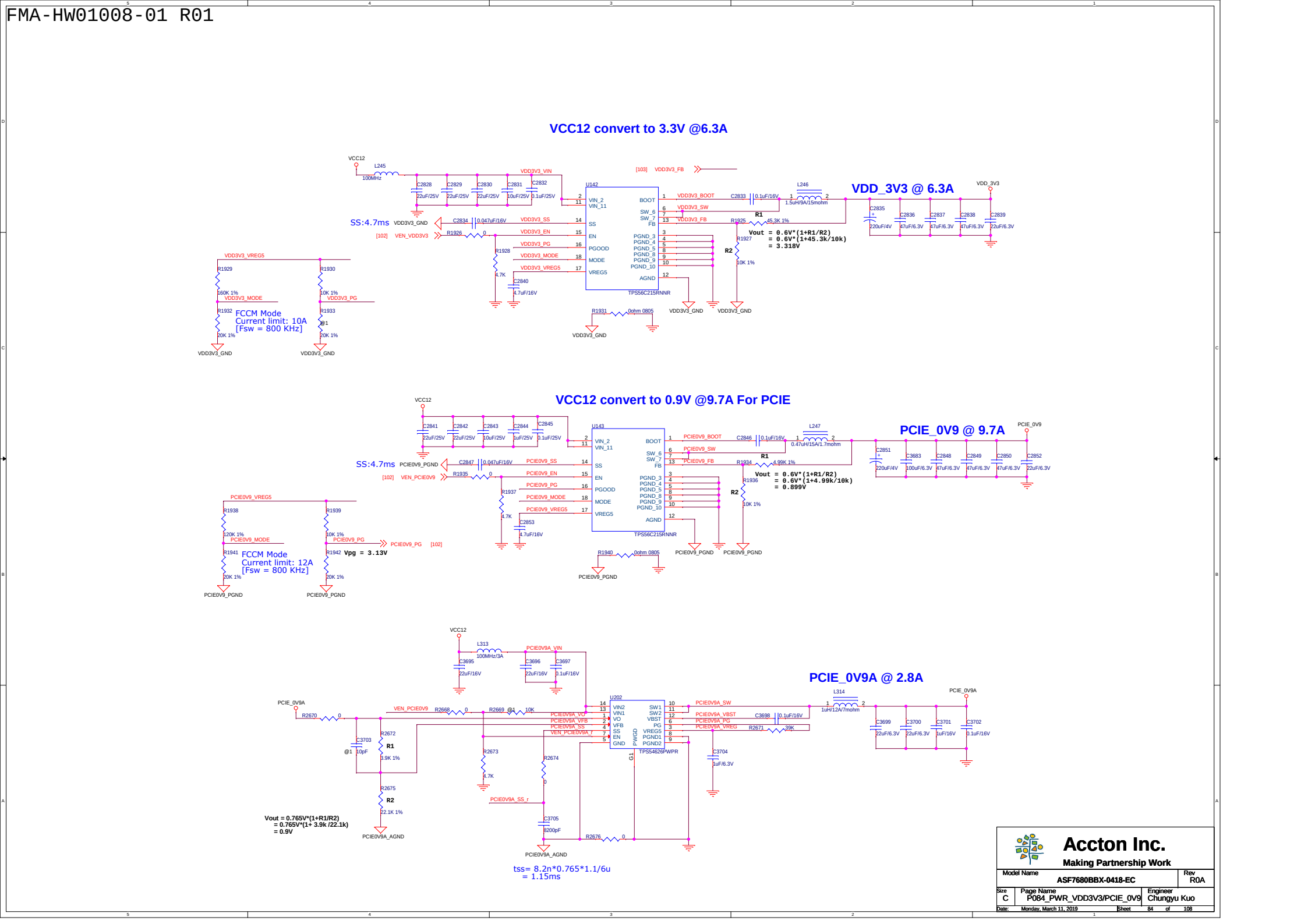
PCIE_0V9A @ 2.8A

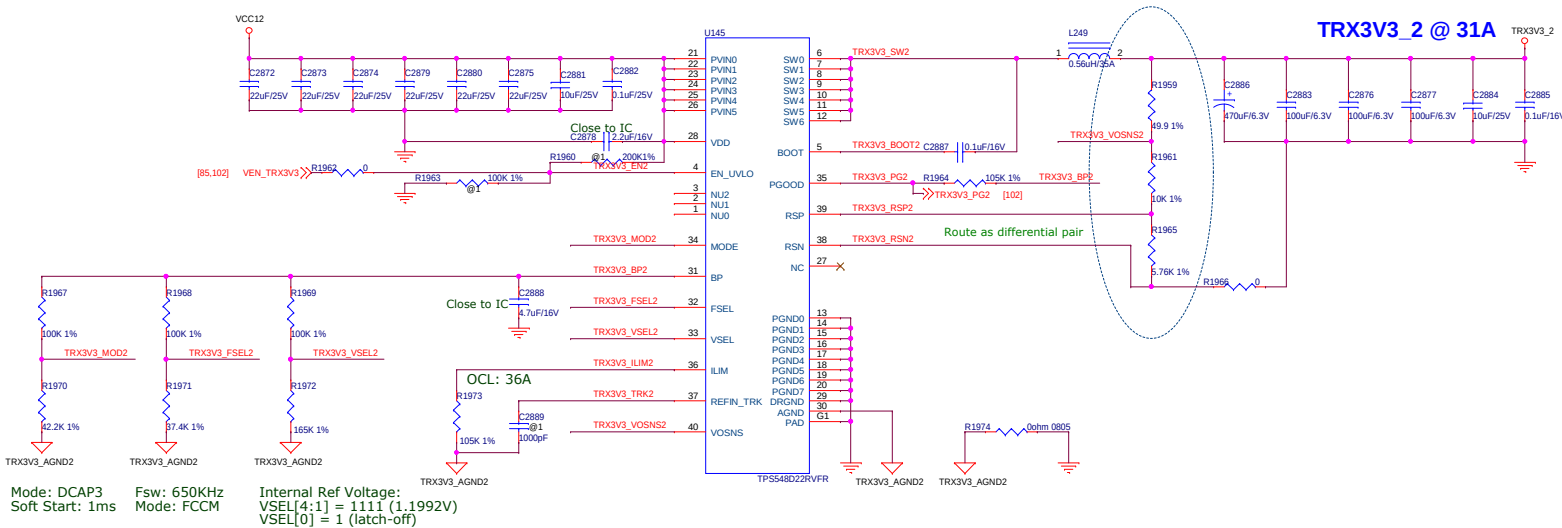
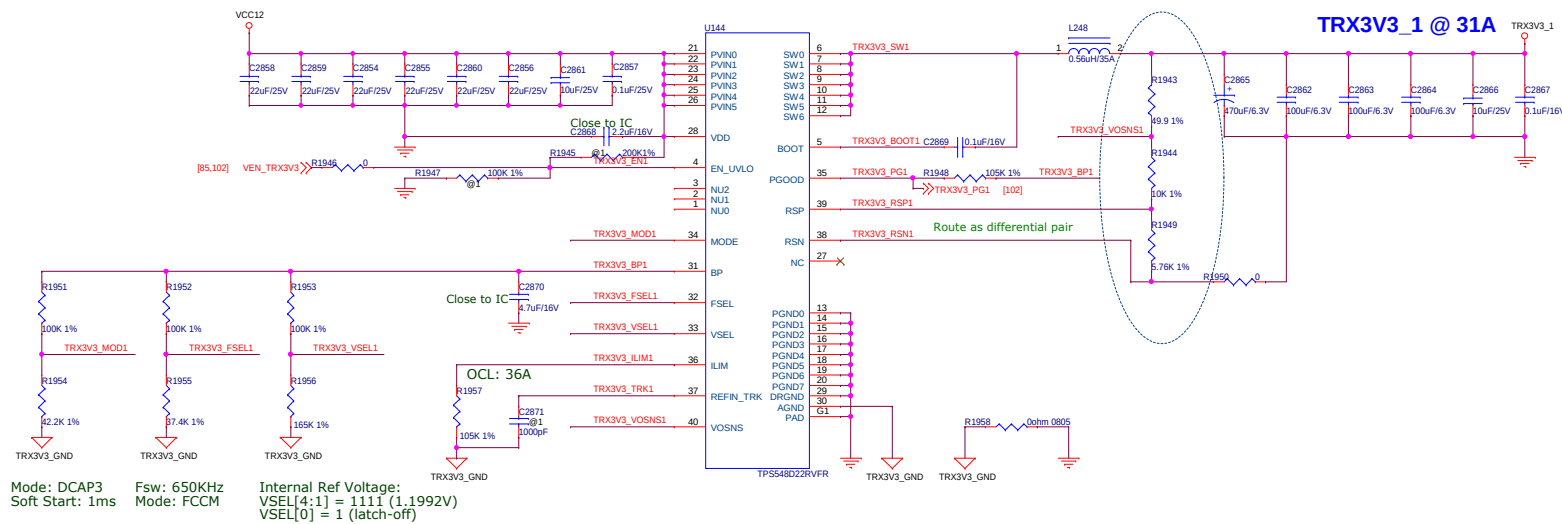
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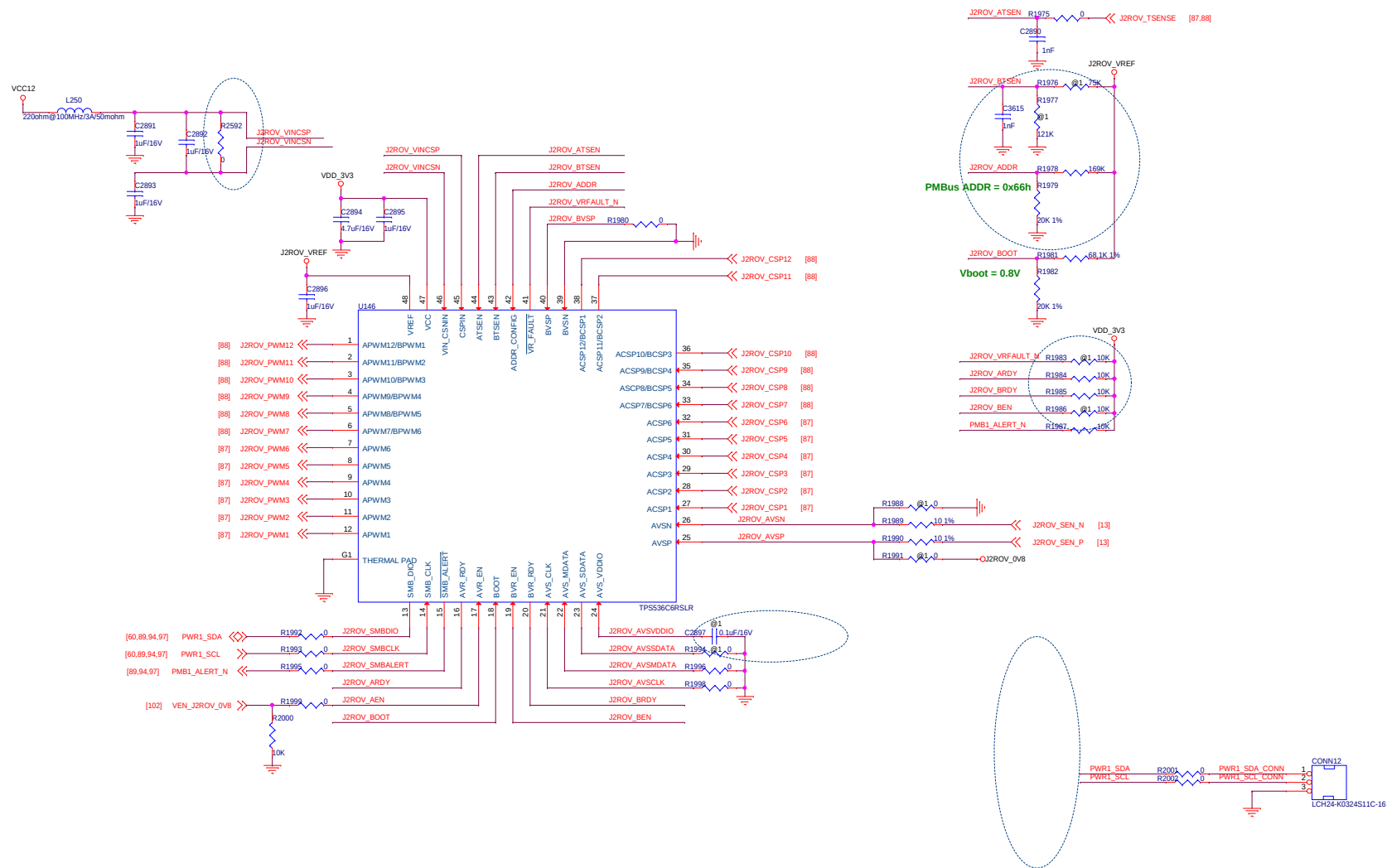
Model Name: ASF7680BBX-0418-EC Rev: R0A

Size: C Page Name: P084_PWR_VDD3V3/PCIE_0V9 Engineer: Chungyu Kuo

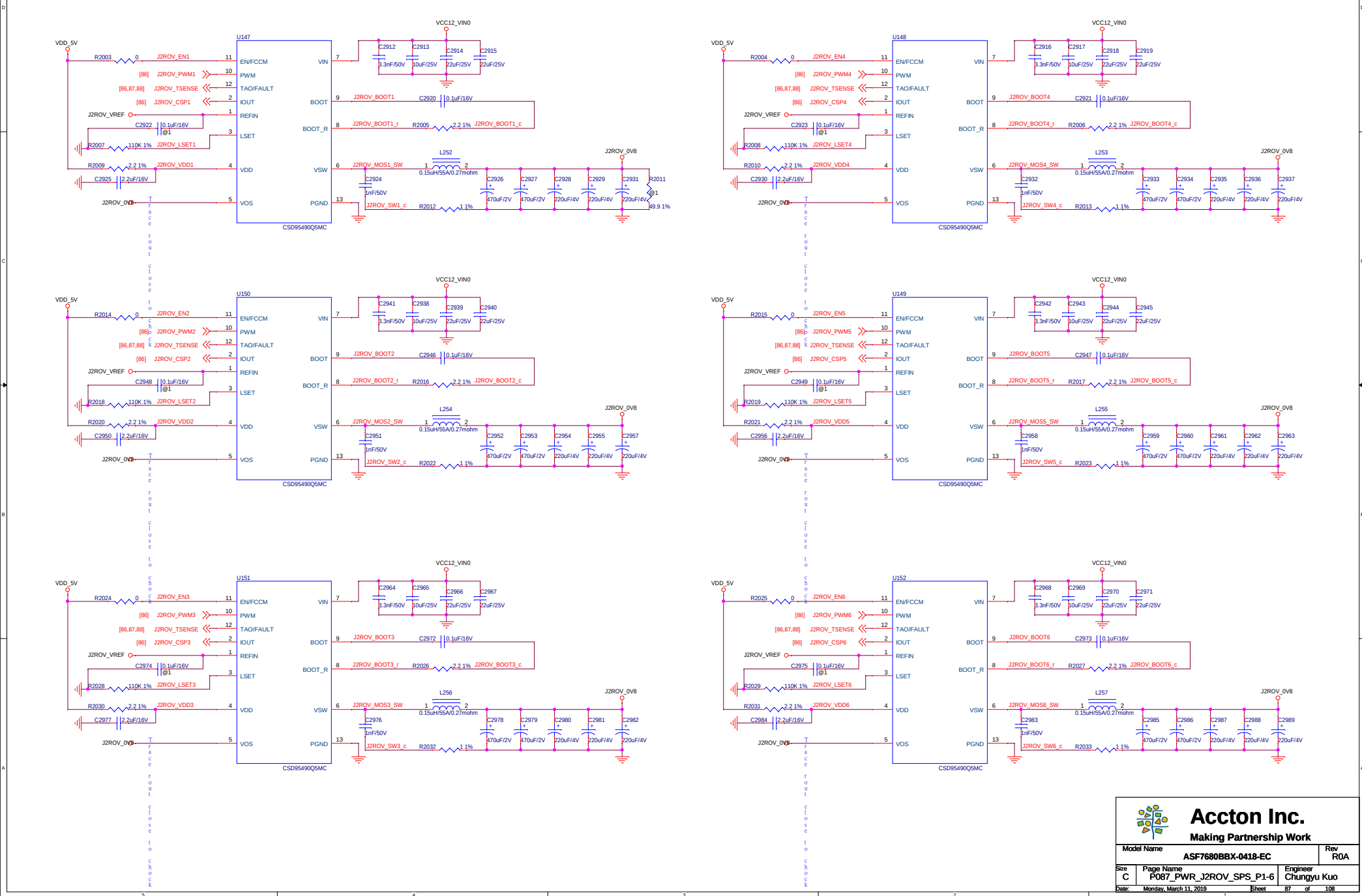
Date: Monday, March 11, 2019 Sheet: 84 of 108

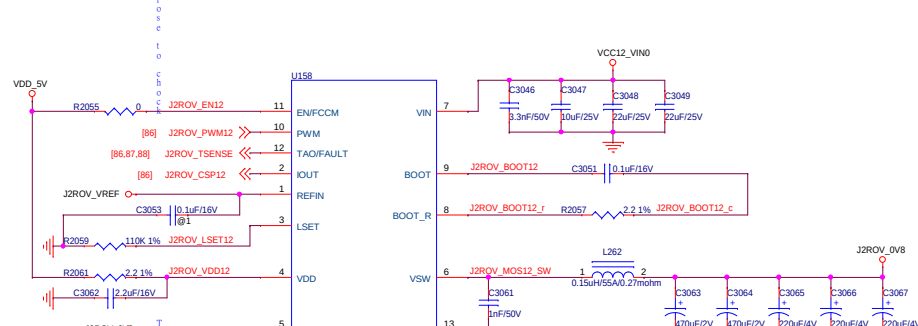
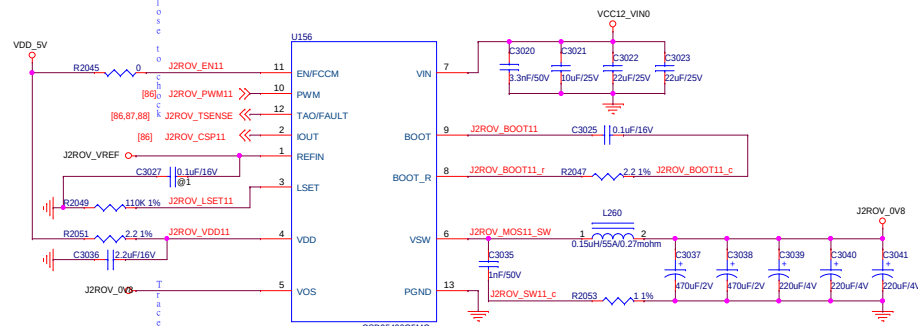
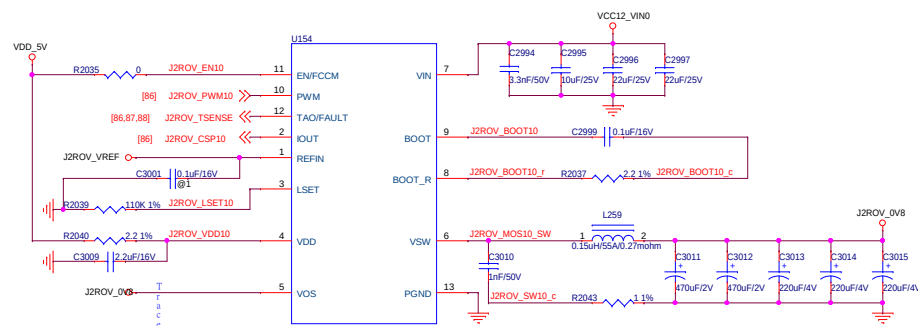
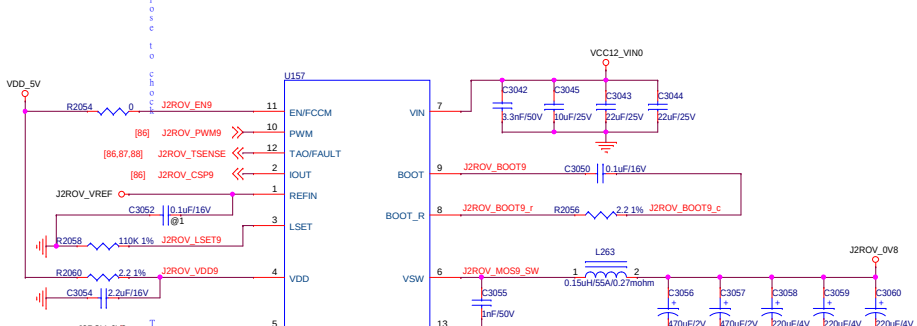
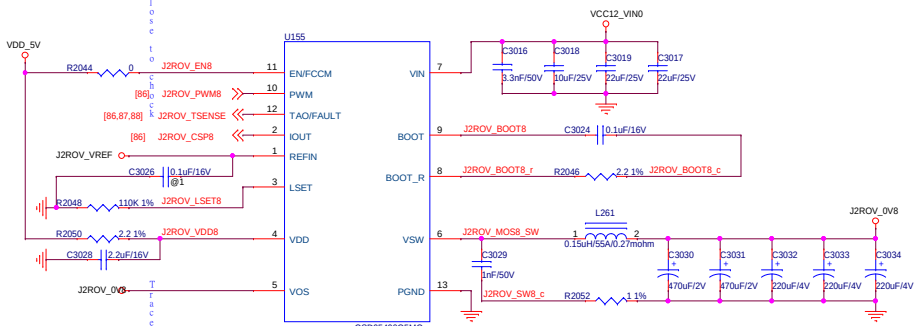
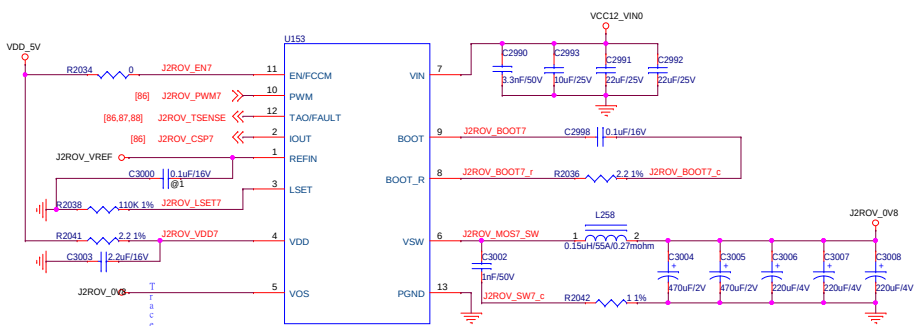




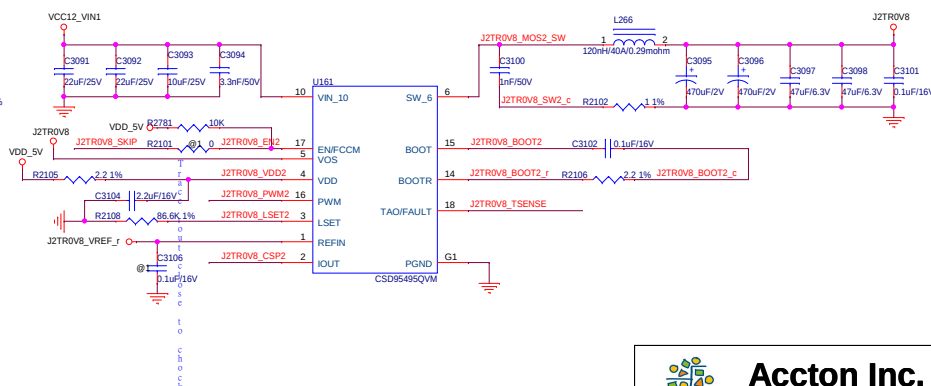
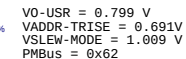


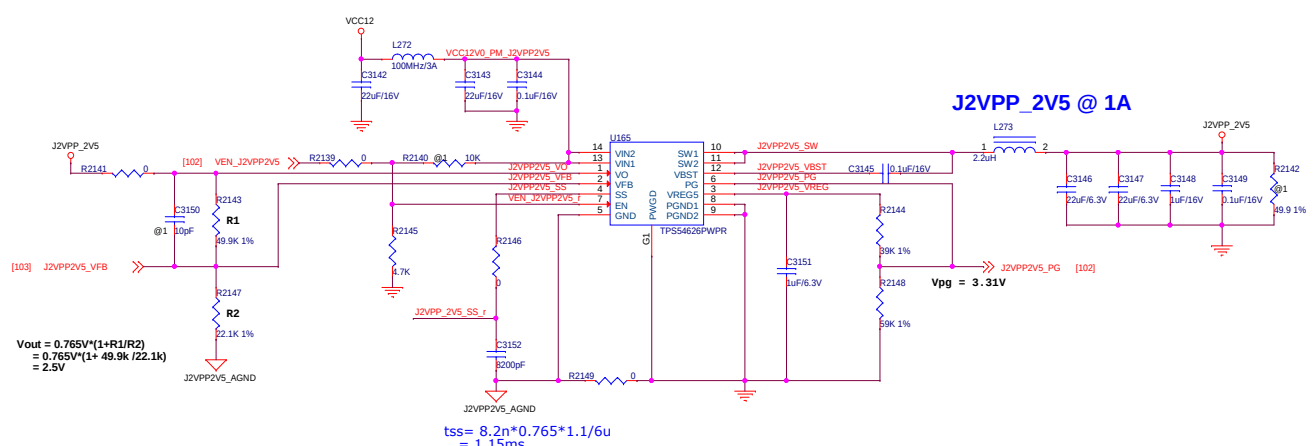
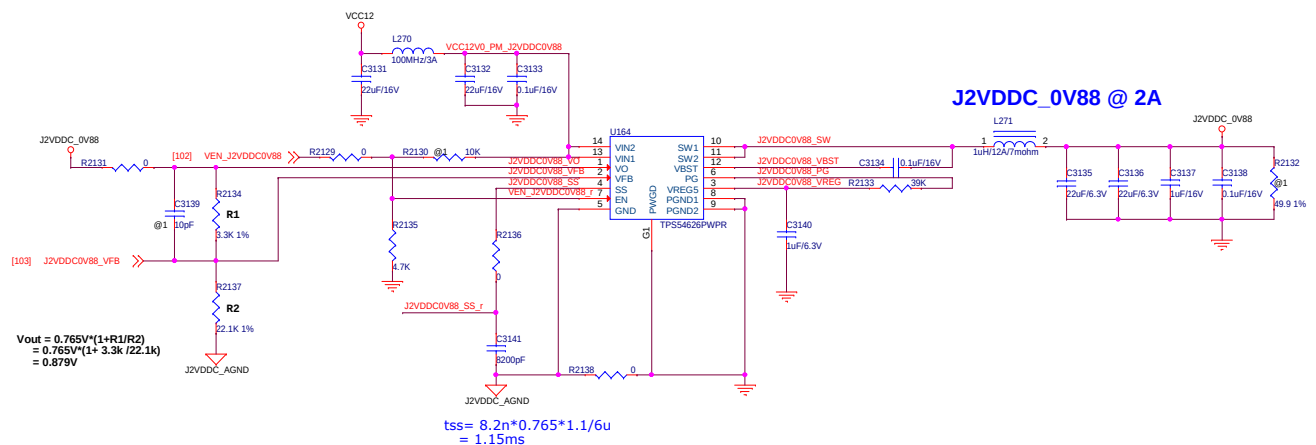
For J2_ROV @355A 12 Phase MOSFET
Expected totally current=355A



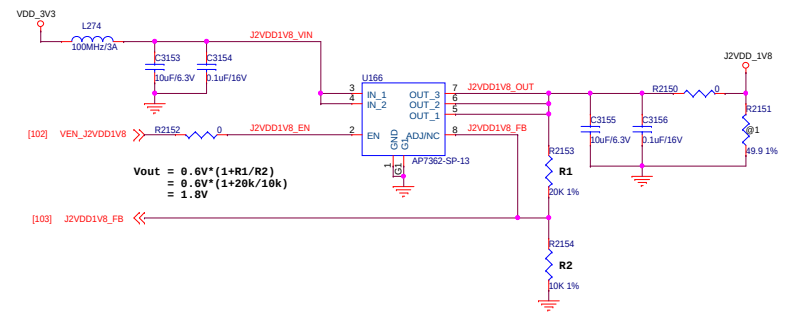


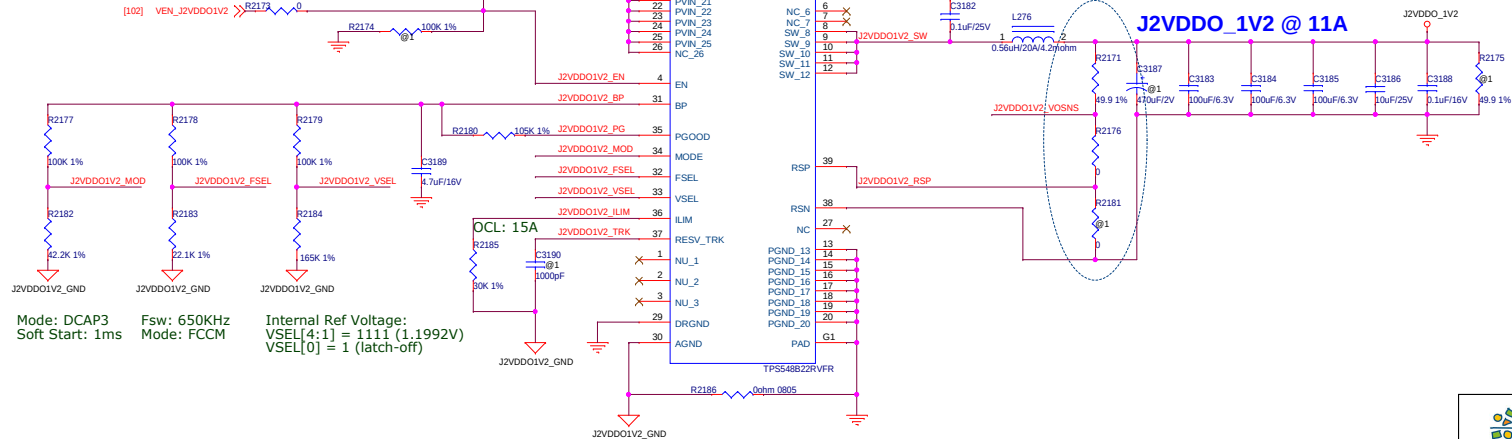
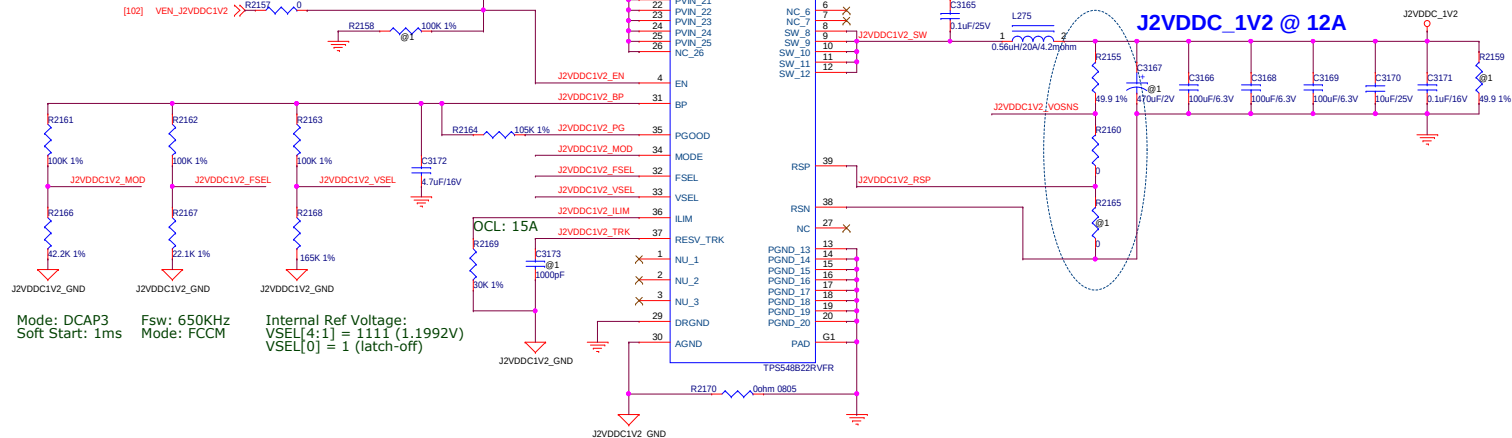
VR = 12
Phase# = 2 phases
Fsw = 600kHz
Vboot = 0.8V
Icc_TDC = 55A
Icc_MAX = A
Over current limit = A per phase
Slew rate fast = 5mV/us
Load Line = zero
Disable Dynamic Phase Shedding





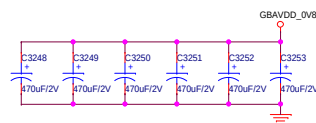
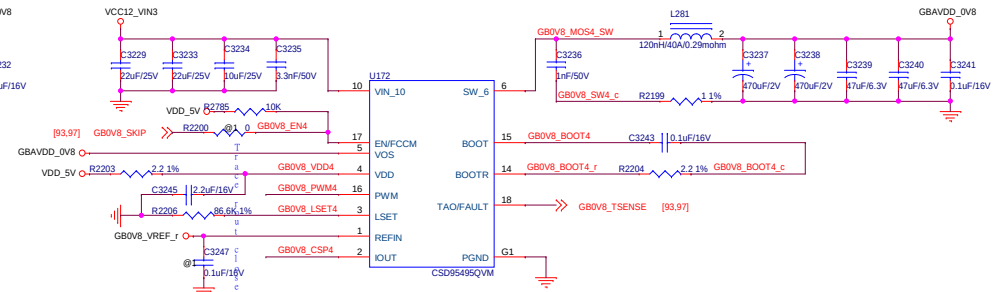
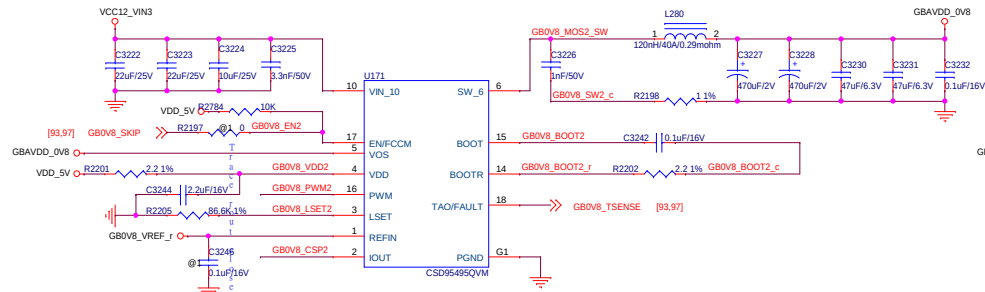
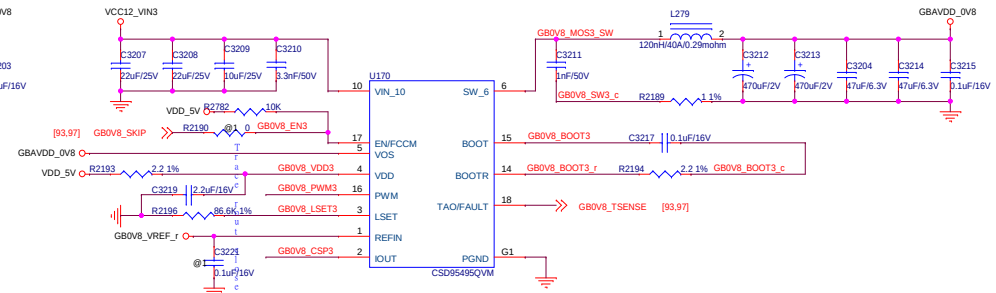
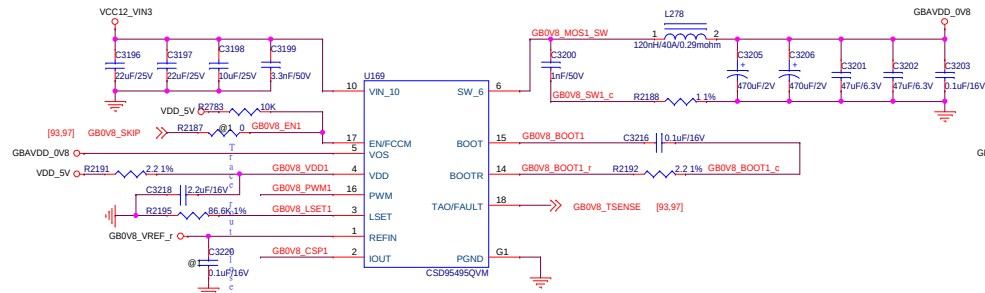
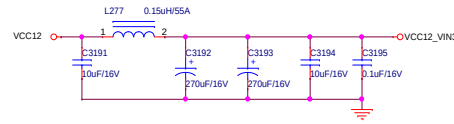
VDD3V3 convert to 1.8V @0.6A for J2





- [97] GB0V8_PWM1 >> GB0V8_PWM1
- [97] GB0V8_PWM2 >> GB0V8_PWM2
- [97] GB0V8_PWM3 >> GB0V8_PWM3
- [97] GB0V8_PWM4 >> GB0V8_PWM4
- [97] GB0V8_CSP1 >> GB0V8_CSP1
- [97] GB0V8_CSP2 << GB0V8_CSP2
- [97] GB0V8_CSP3 << GB0V8_CSP3
- [97] GB0V8_CSP4 << GB0V8_CSP4

For MOS 12V Input

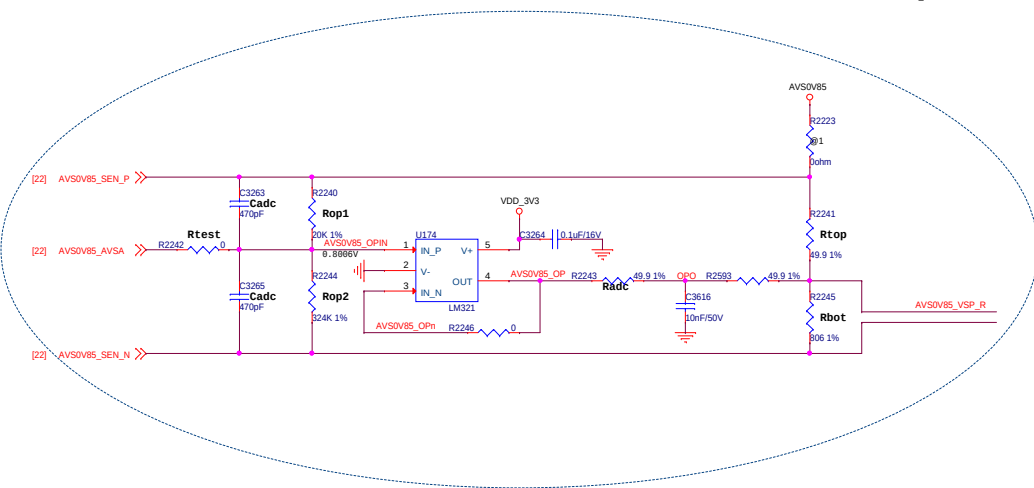


Place to Bottom



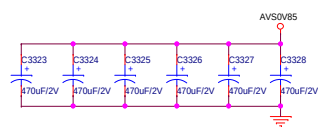
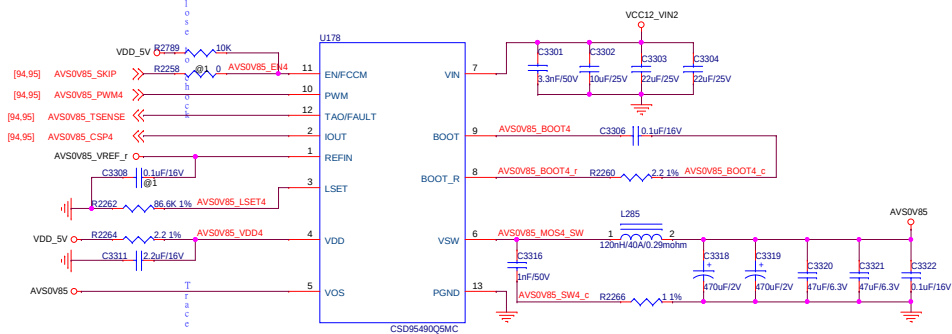
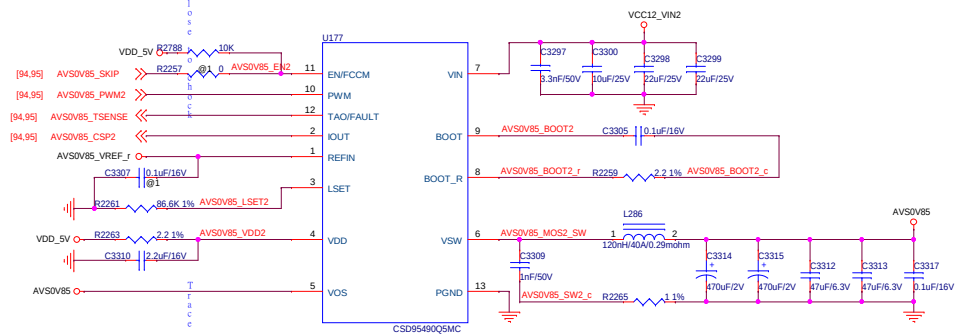
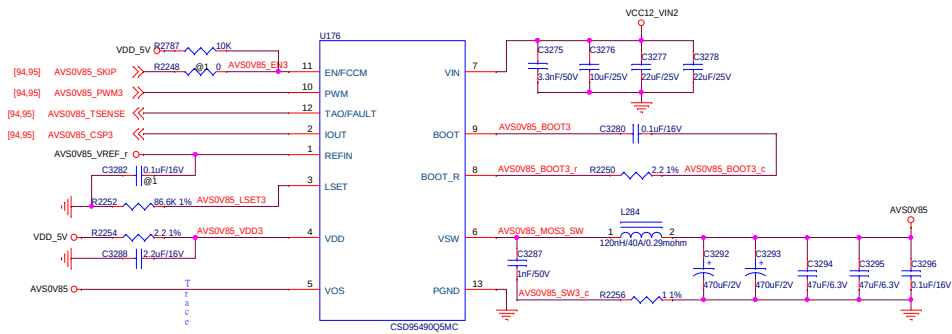
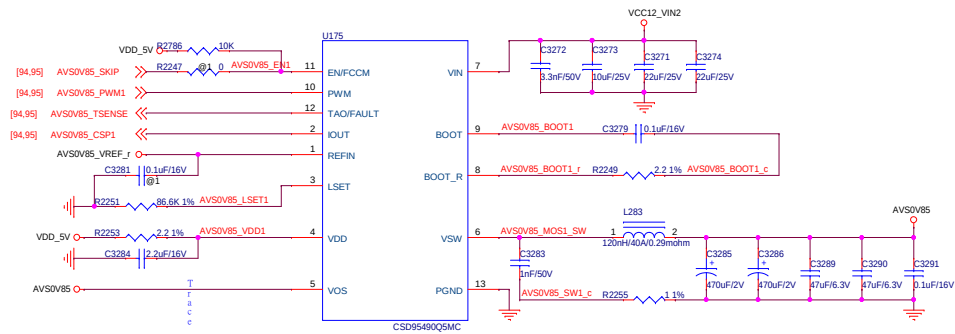
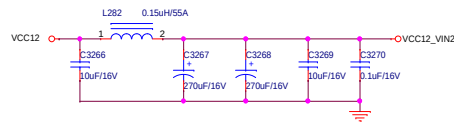
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Model Name		ASF7680BBX-0418-EC	Rev	R0A
Size	Page Name	Engineer		
C	P093_PWR_GBAVDD0V8_MOS	Chungyu Kuo		
Date:	Monday, March 11, 2019	Sheet	93	of 108

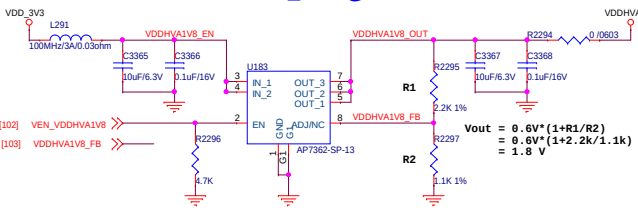
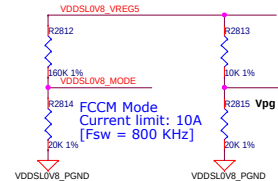


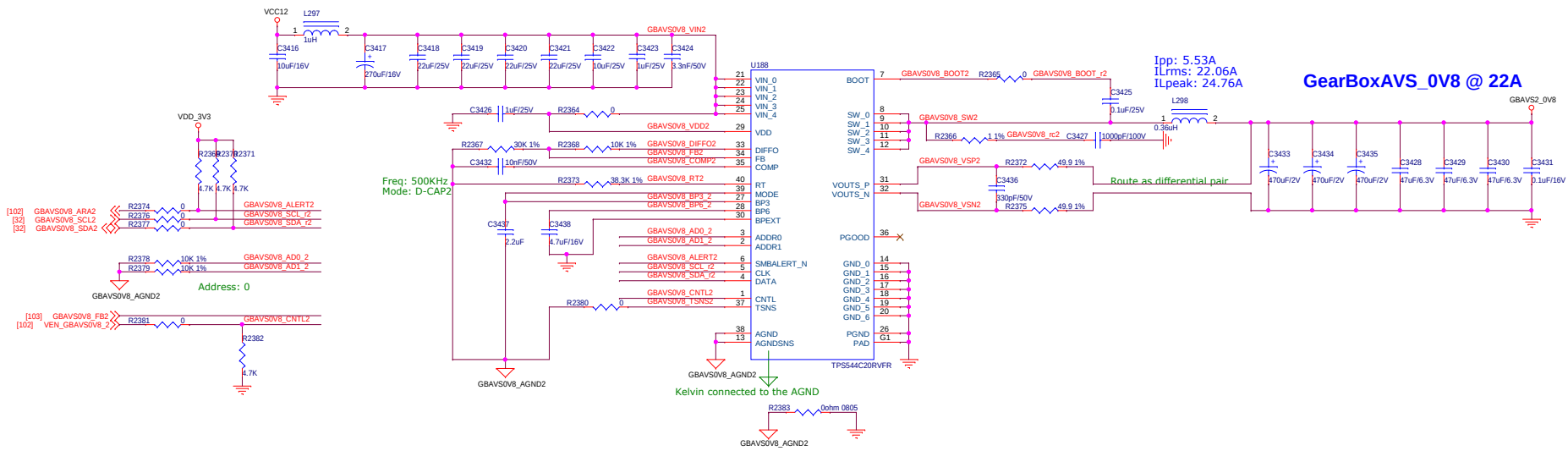
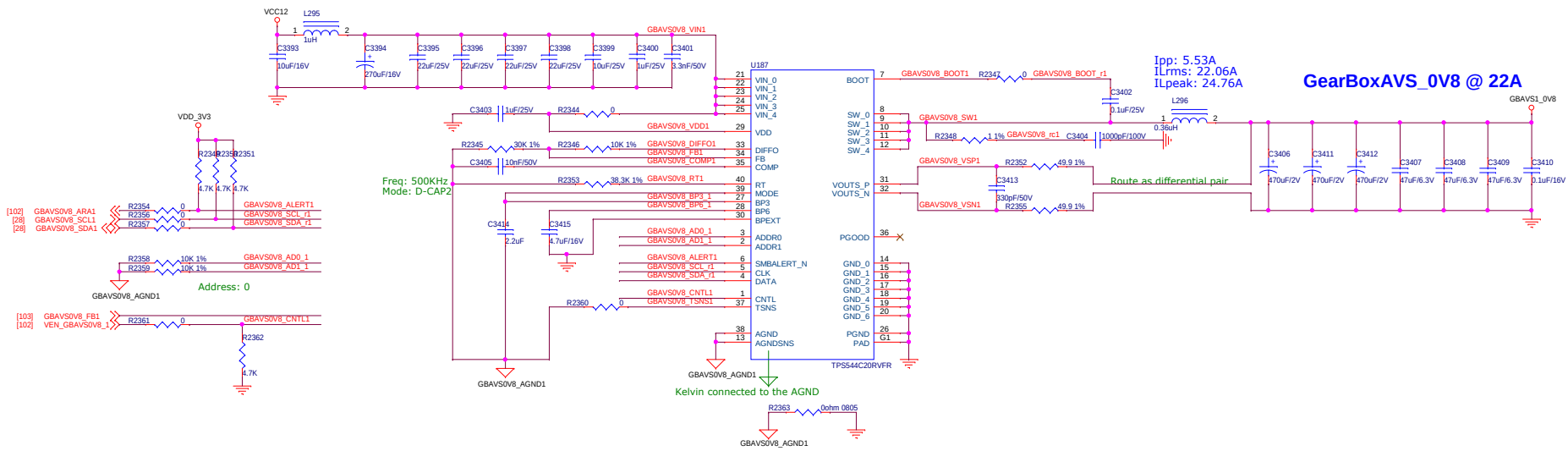
[94.95] AVS0V85_PWM1 >> AVS0V85_PWM1
 [94.95] AVS0V85_PWM2 >> AVS0V85_PWM2
 [94.95] AVS0V85_PWM3 >> AVS0V85_PWM3
 [94.95] AVS0V85_PWM4 >> AVS0V85_PWM4
 [94.95] AVS0V85_CSP1 << AVS0V85_CSP1
 [94.95] AVS0V85_CSP2 << AVS0V85_CSP2
 [94.95] AVS0V85_CSP3 << AVS0V85_CSP3
 [94.95] AVS0V85_CSP4 << AVS0V85_CSP4

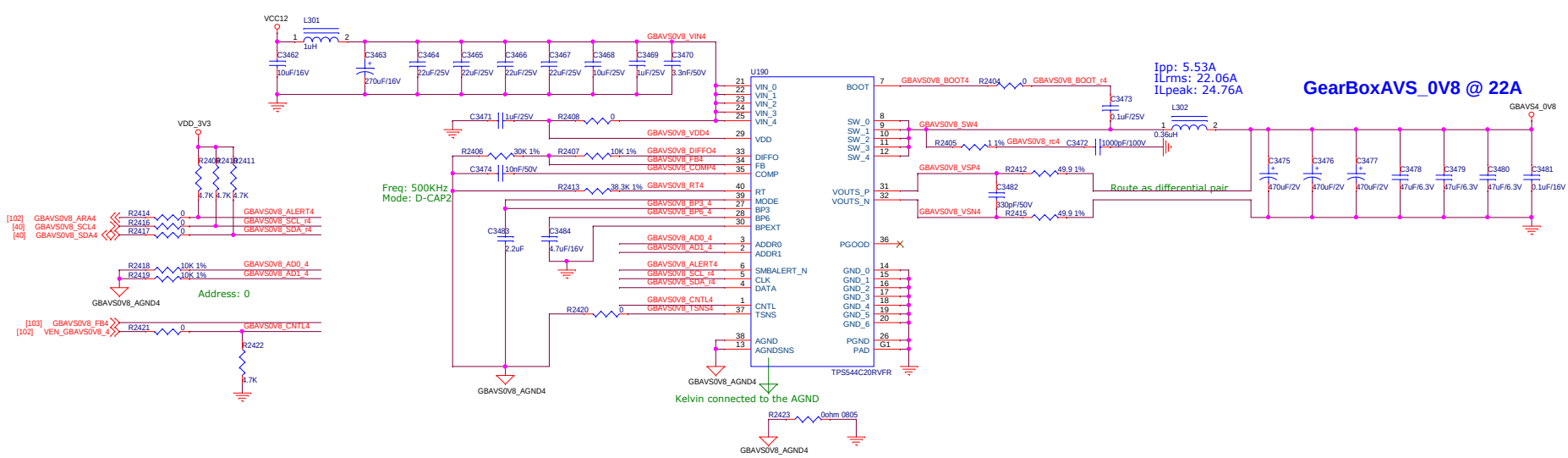
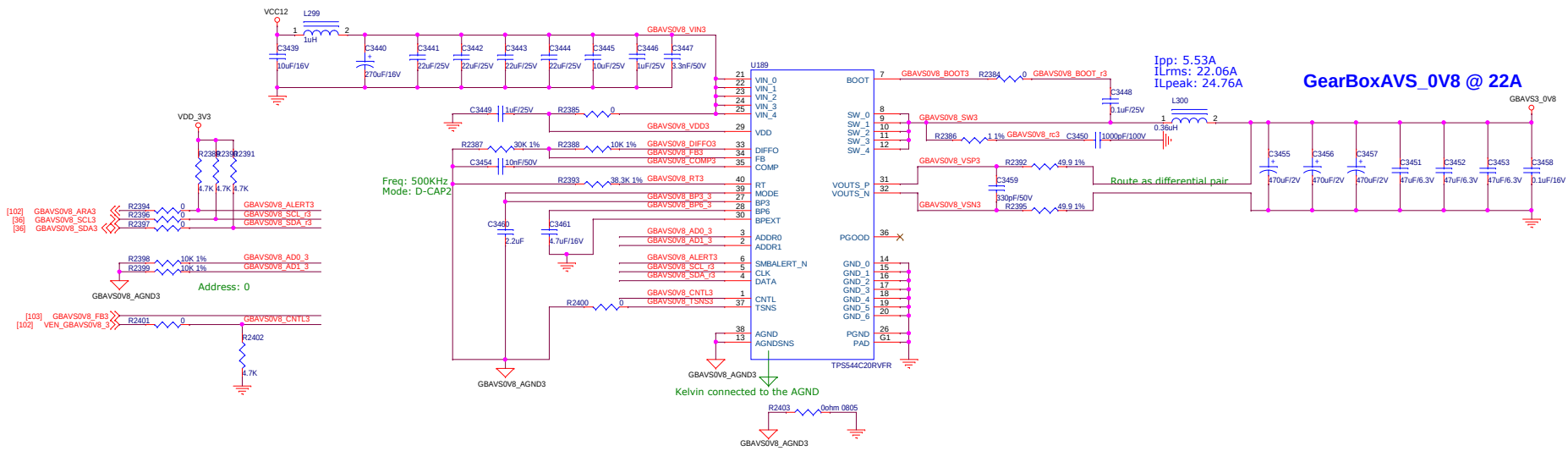
For MOS 12V Input



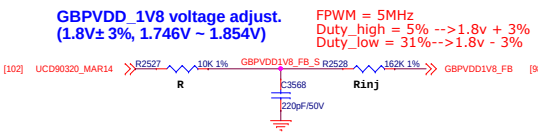
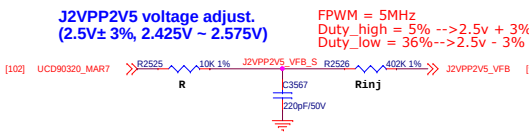
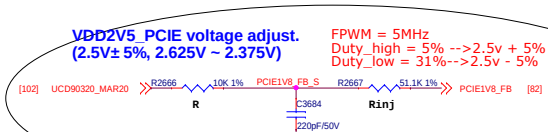
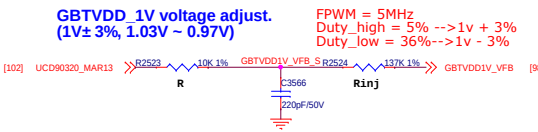
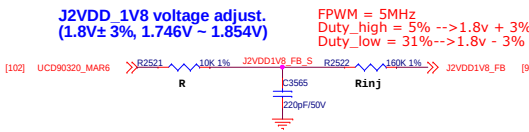
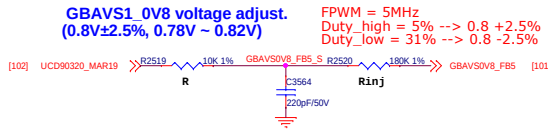
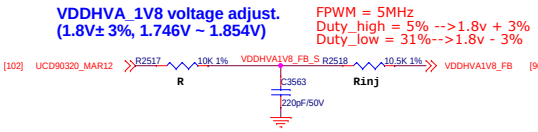
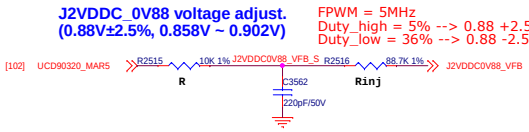
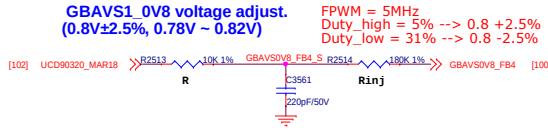
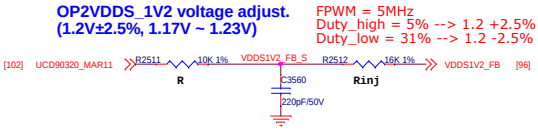
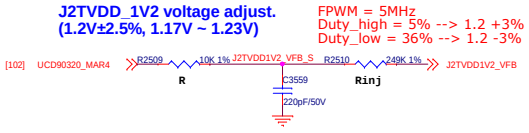
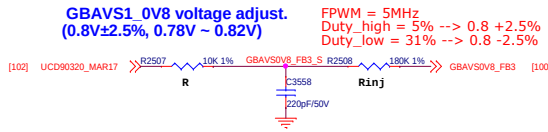
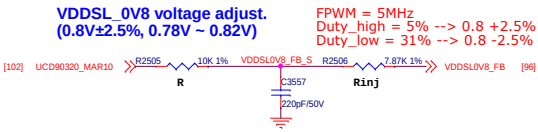
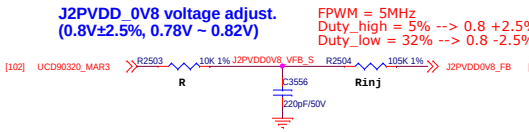
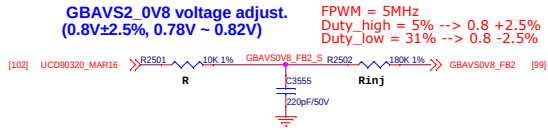
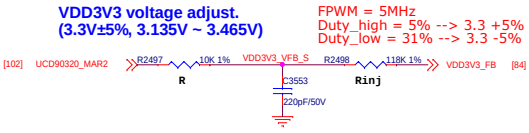
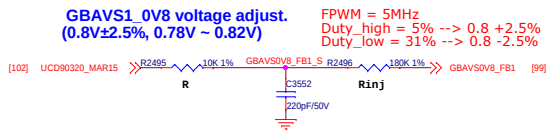
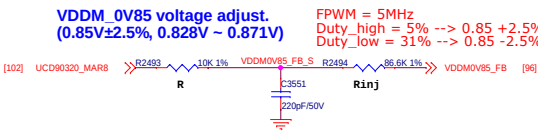
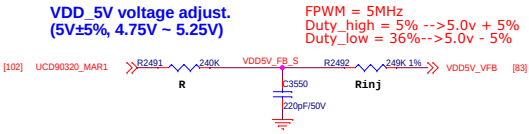
Place to Bottom



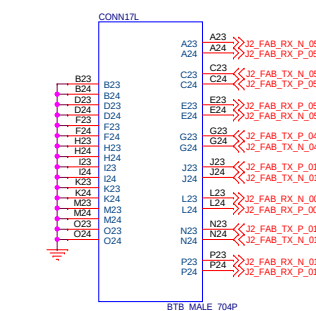
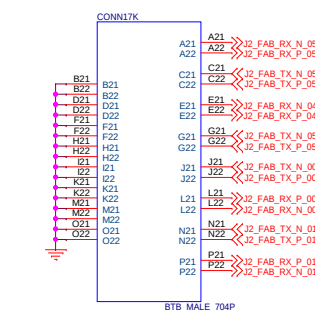
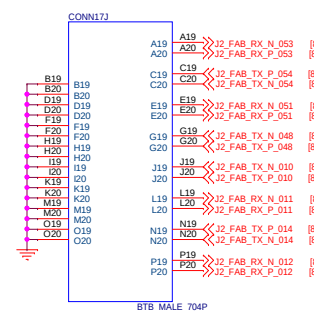
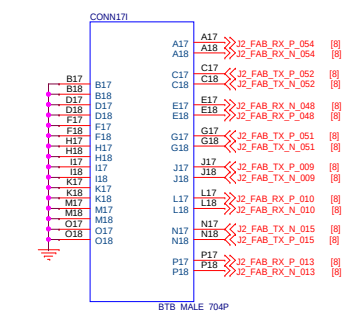
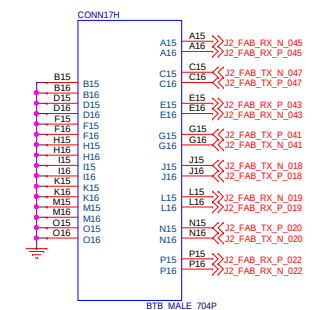
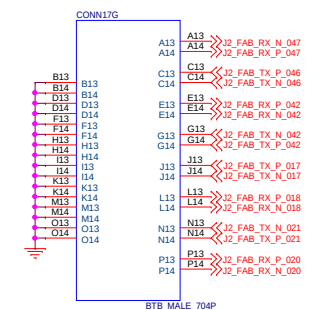
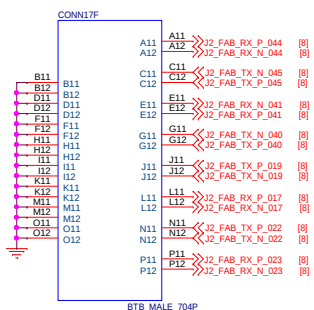
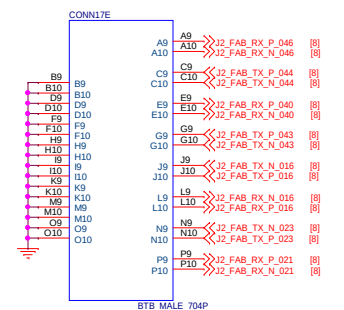
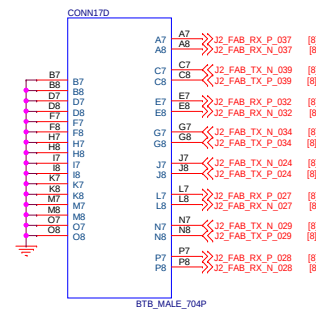
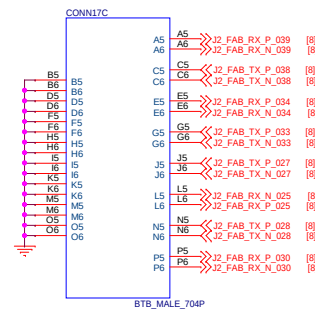
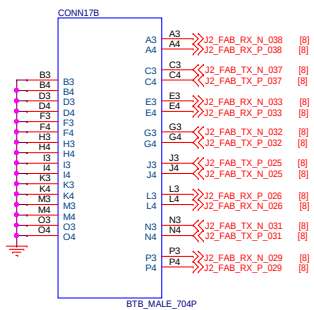
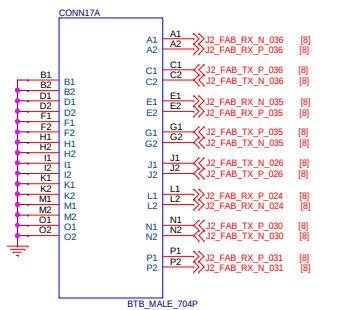


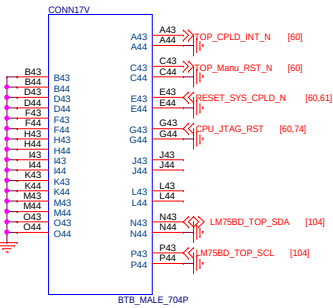
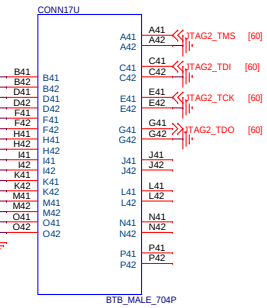
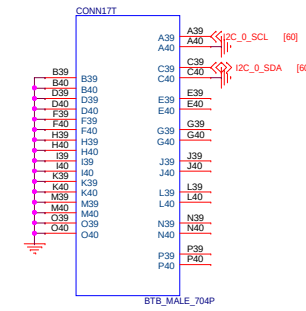
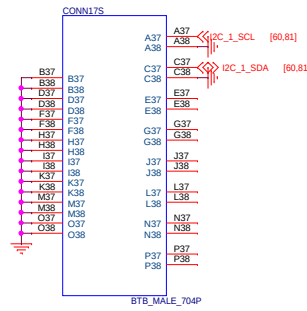
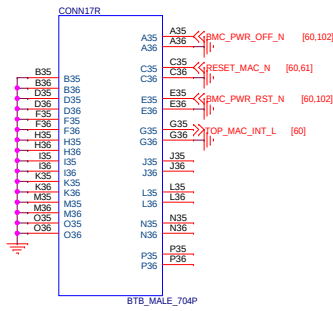
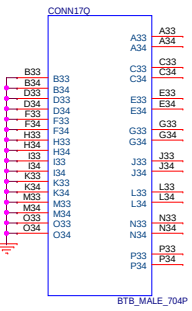
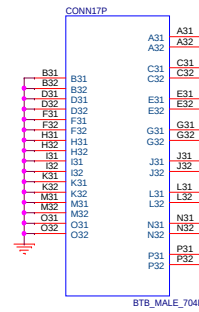
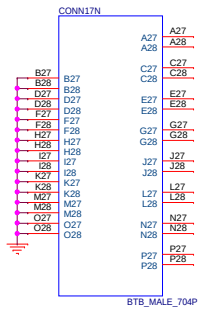
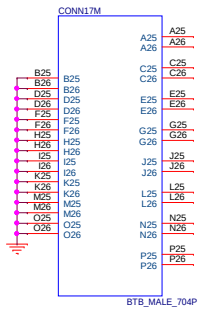


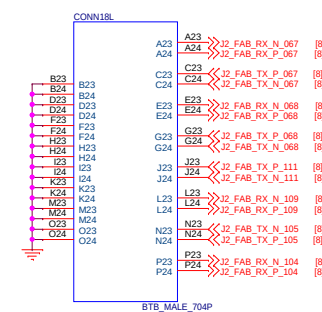
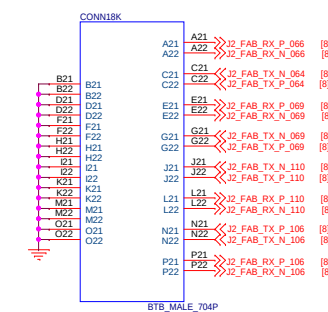
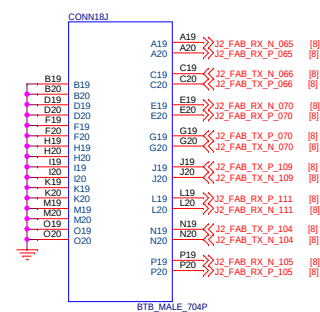
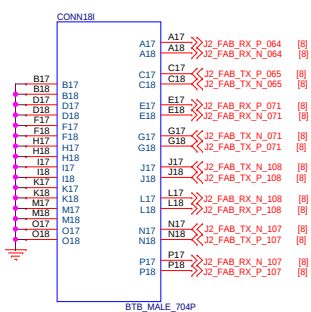
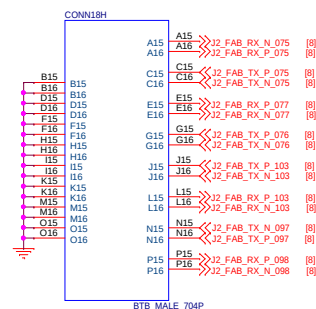
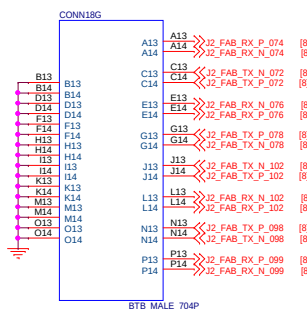
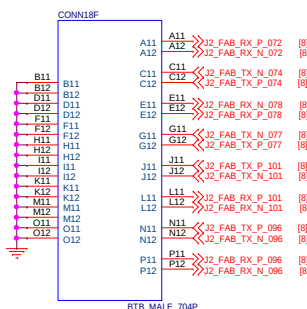
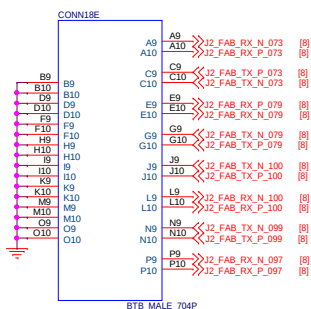
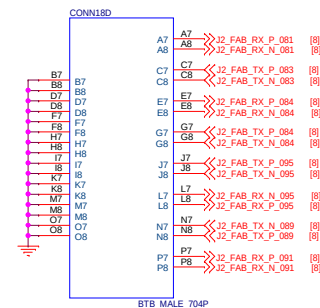
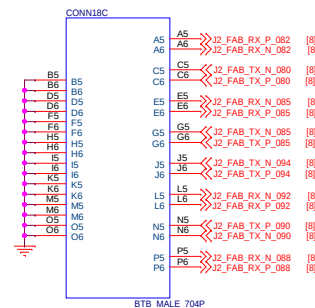
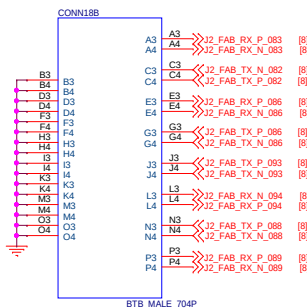
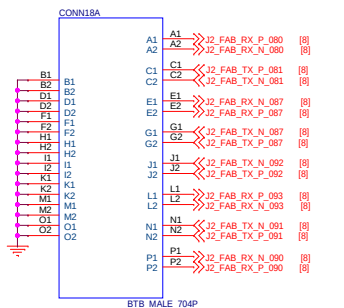
UCD90320 Closed Loop Margining Modes











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